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加密和签署

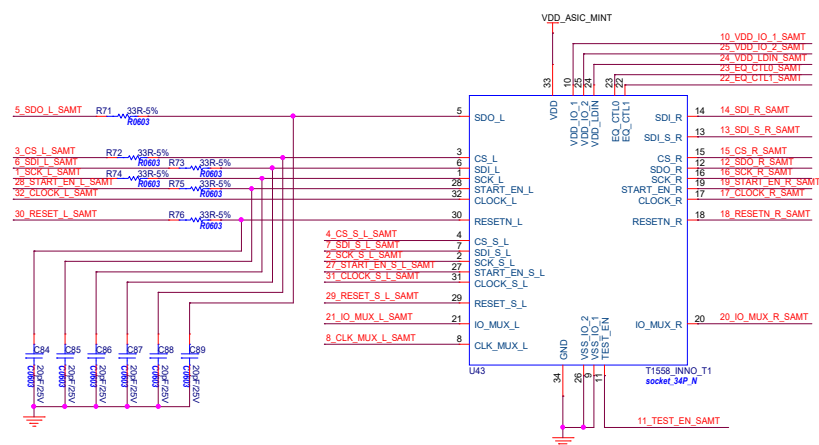


交互式动态表单

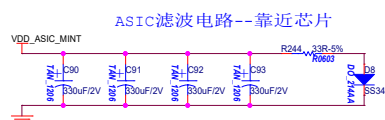


互联PDF文档

单ASIC电路-直接SMT贴片
9#、26#引脚为VSS IO,接GND
"SMT"后缀表示"Single-ASIC-SMT",即"单芯片"

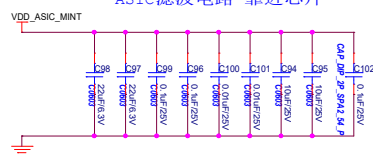


滤波电路



二极管作用为:"缓解上电瞬态冲击"

ASIC滤波电路-靠近芯片

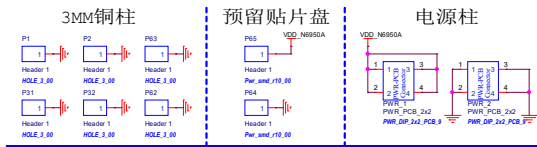


单芯片-SMT-ASIC网络信号-接至Socket

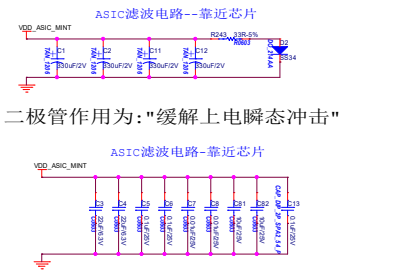
1 SCK L	1 SCK L SAMT	16 SCK R	16 SCK R SAMT
2 SCK_S.L	2 SCK_S.L SAMT	17 CLOCK R	17 CLOCK R SAMT
3 CS_L	3 CS_L SAMT	18 RESET R SAMT	18 RESET R SAMT
4 CS_S.L	4 CS_S.L SAMT	19 START EN R	19 START EN R SAMT
5 SDO_L	5 SDO_L SAMT	20 IO_MUX R	20 IO_MUX L SAMT
6 SDI L	6 SDI L SAMT	21 IO_MUX R	21 IO_MUX L SAMT
7 SDI_S.L	7 SDI_S.L SAMT	22 EQ_CTL1	22 EQ_CTL1 SAMT
8 CLK_L	8 CLK_L SAMT	23 IO_MUX R	23 IO_MUX L SAMT
9 VDD_IO_0	9 VDD_IO_0 SAMT	24 VDD_CTLN	24 VDD_CTLN SAMT
10 TEST_EN	10 TEST_EN SAMT	25 VDD_CTLN	25 VDD_CTLN SAMT
11 TEST_EN	11 TEST_EN SAMT	26 VDD_IO_0	26 VDD_IO_0 SAMT
12 SCK_R	12 SCK_R SAMT	27 START EN L	27 START EN L SAMT
13 SCK_S.R	13 SCK_S.R SAMT	28 START EN L SAMT	28 START EN L SAMT
14 CS_R	14 CS_R SAMT	29 RESET S.L	29 RESET S.L SAMT
15 CS_S.R	15 CS_S.R SAMT	30 RESET R	30 RESET R SAMT
16 S.R	16 S.R SAMT	31 CLOCK_S.L	31 CLOCK_S.L SAMT
		32 C_RESET	32 C_RESET SAMT
		33 CLOCK_L	33 CLOCK_L SAMT

Title <Title>			
Size B	Document Number <Doc>		Rev <RevCode>
Date:	Tuesday, November 20, 2018	Sheet 1 of 12	

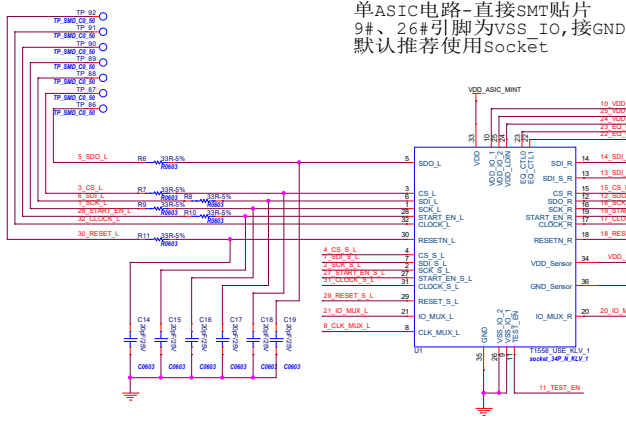
N6950A功率输入接口-0.40V/30A
3类:螺丝孔、电源口与贴片焊盘



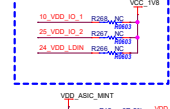
滤波电路



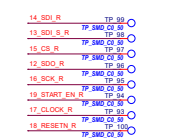
测试点-就近放置



外部供电使用

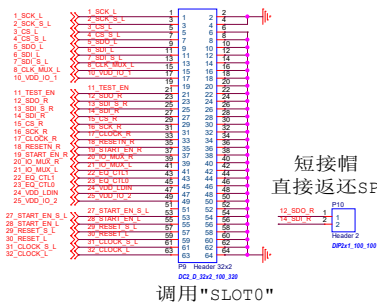


测试点-就近放置

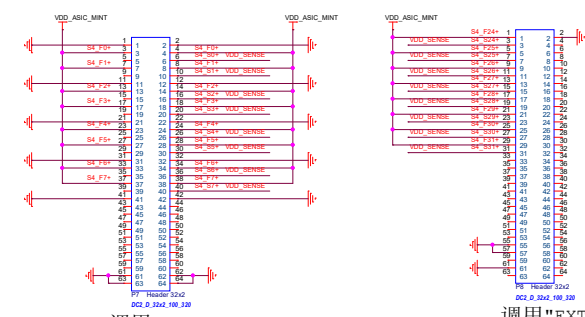


"牛角座/筒牛座"接口:ASIC通讯部分

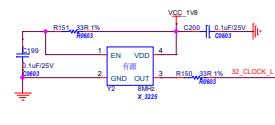
9#、26#引脚为VSS_IO, 接GND



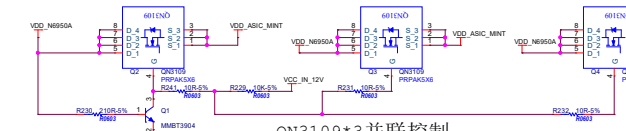
"牛角座/筒牛座"接口:供电部分



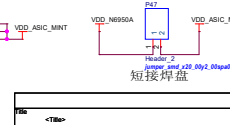
预留有源晶振电路-12MHz



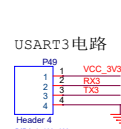
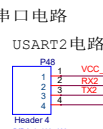
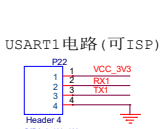
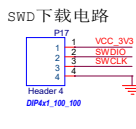
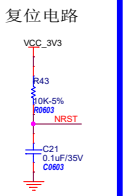
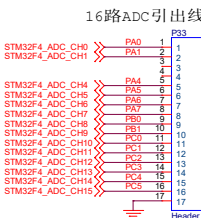
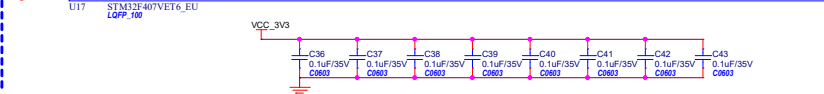
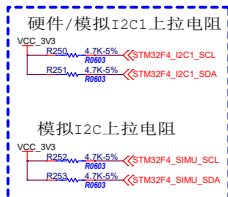
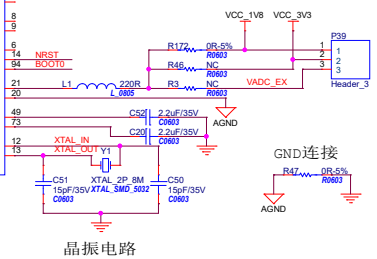
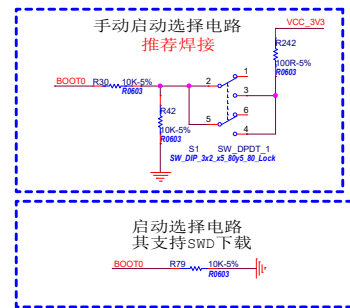
N6950A程控保护电路



预留短接电阻-默认不焊

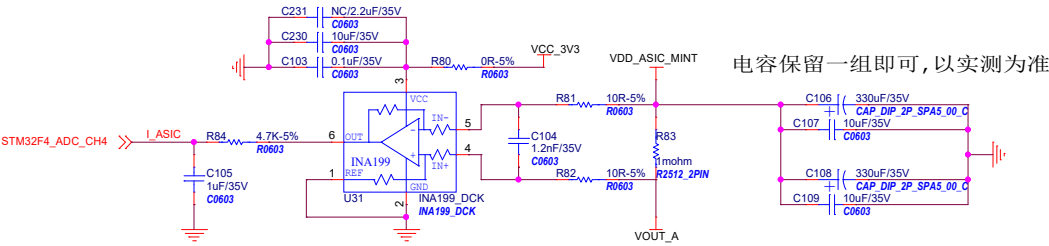


File	<Title>
Size	Document Number
Date	Version
Created	November 20, 2018
Revised	2 of 12

[illegible]

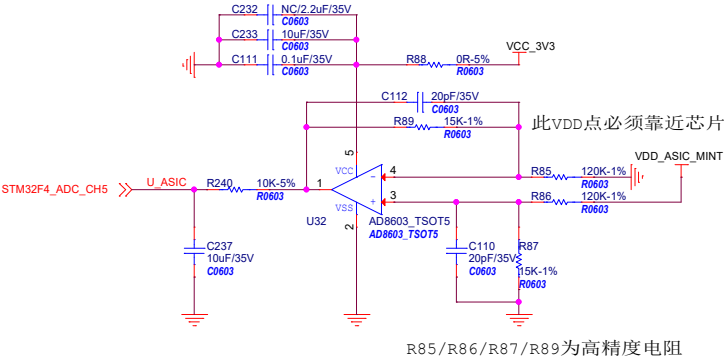
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Size A3	Document Number <Doc>		Rev <Rev>
Date:	Tuesday, November 20, 2018	Sheet	3 of 12

ASIC电流采样电路-尽量靠近芯片

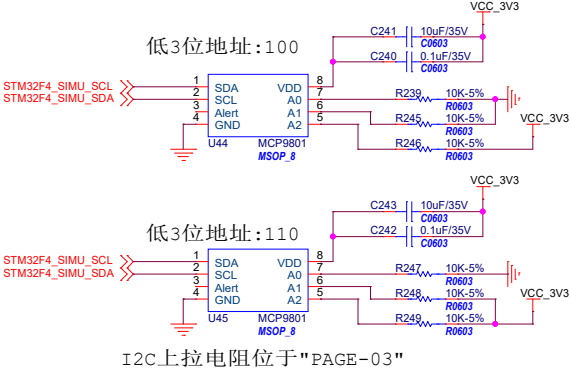


尽量靠近芯片
VoutA源自:TPS53681

ASIC电压采样电路-尽量靠近芯片

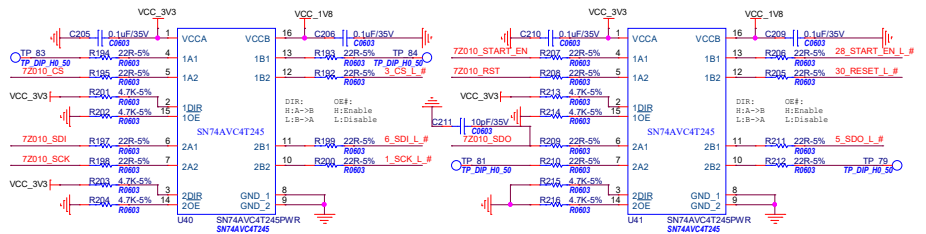


温度检测-温度传感器-2路



Title			
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Size	Document Number		Rev
Custom	<Doc>		<RevCode>
Date:	Tuesday, November 20, 2018	Sheet	4 of 12

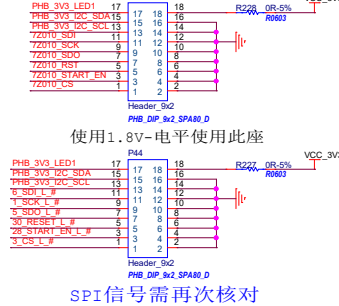
电平转换部分:+3.3V-Lever转+1.8V-Lever



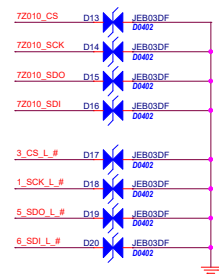
座子兼容电路,对应接口座为C5接口座

接口座电路-接至"控制板"
使用3.3V-电平使用此座

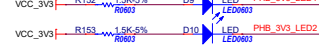
信号保护TVS电路



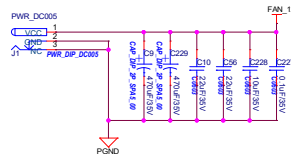
信号保护TVS电路



指示灯-指示状态



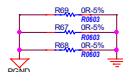
外接风扇输入-建议使用12VDC
同时做"总体外接电源输入"



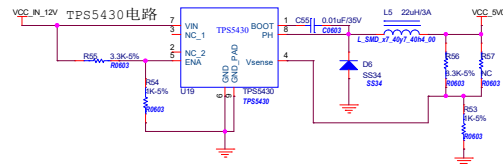
Socket散热风扇接口
尽量远离ASIC芯片



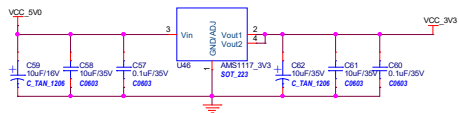
12V外接电源供电GND连接



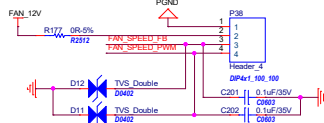
TPS5430降压电路-5.0V/2.0A



LDO转换-5.0V至3.3V电源电路
OUTPUT:3.3V/1.0A



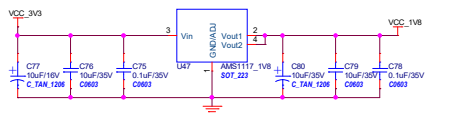
外接风扇接口-远离ASIC芯片
控制板取电



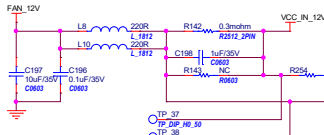
外接风扇接口-预留1
远离ASIC芯片



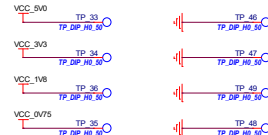
LDO转换-3.3V至1.8V电源电路
OUTPUT:1.8V/1.0A



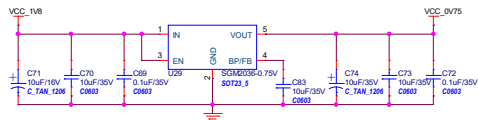
+12V供至TPS53681滤波/检测部分 若R143焊0Ω电阻,
尽量远离电源输入端 则不检测输入;



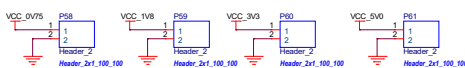
电源测试点



LDO转换-1.8V至0.75V电源电路
OUTPUT:0.75V/300mA

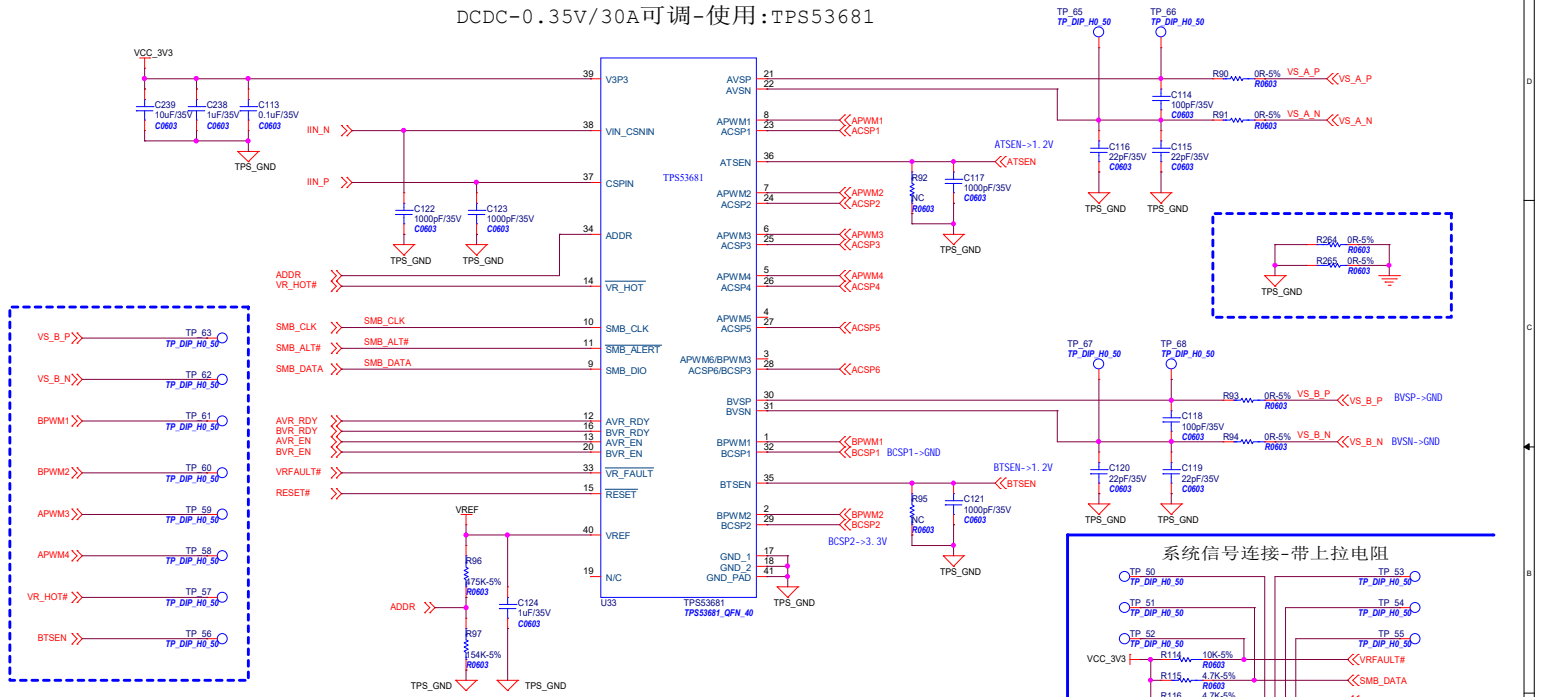


预留电源调试口

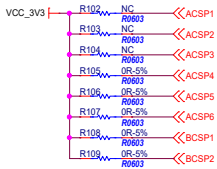


File	<Title>
Size	Document Number
Customer-Doc	
Date	Yuesday, November 20, 2018 19:00 6 of 12

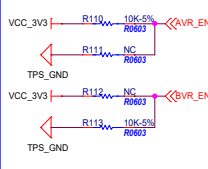
DCDC-0.35V/30A可调-使用:TPS53681



6路ACSP+2路BCSP-接"3V3"则"禁能对应路输出"
且"从低到高全级禁止"



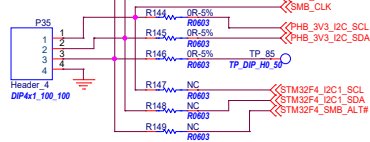
A/B相输出使能控制
-"拉高H"则"使能输出"



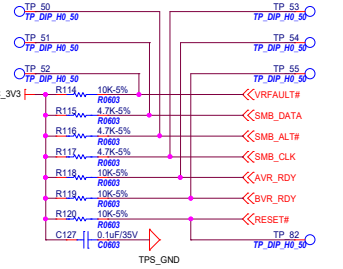
预留STM32控制、外部控制

SMB_ALT#

SMB_DATA

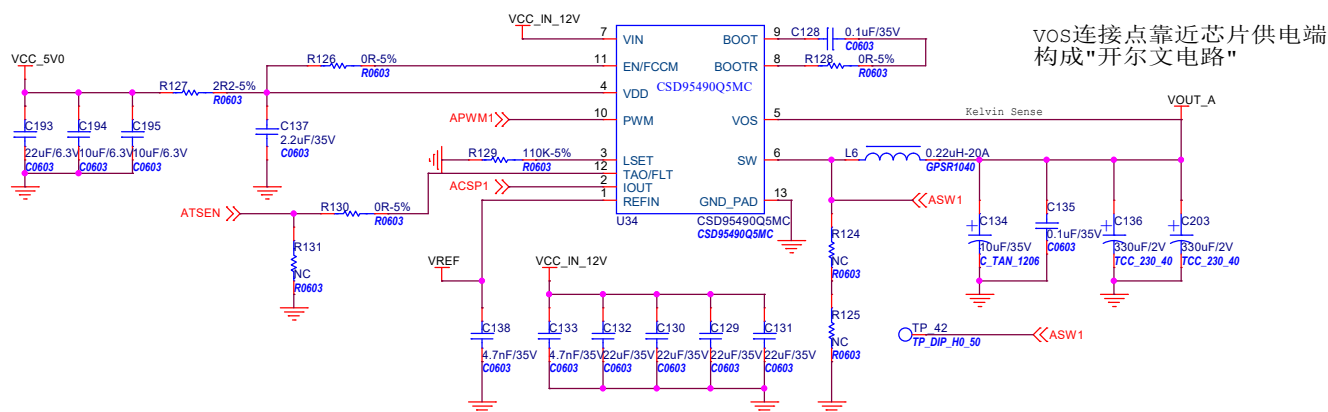


系统信号连接-带上拉电阻

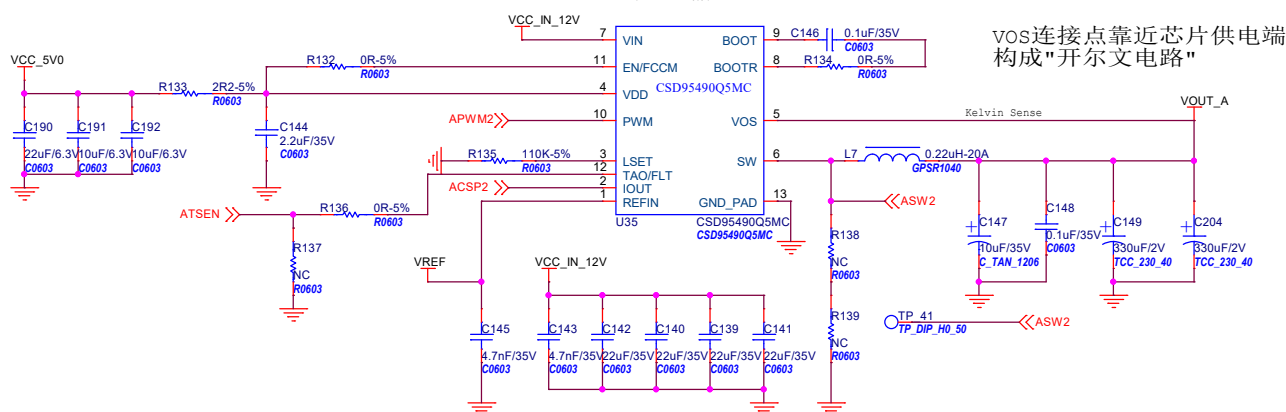


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Size B	Document Number <Doc>	Rev <RevCode>	
Date:	Tuesday, November 20, 2018	Sheet	7 of 12

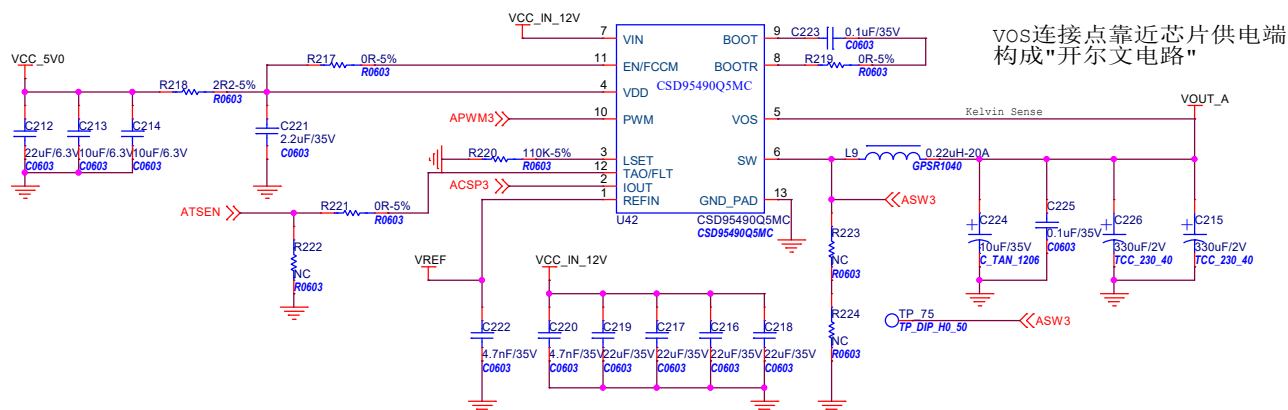
A相CH1路输出-第1路/共3路并联输出
暂定级联2路A相输出



A相CH2路输出-第2路/共3路并联输出
暂定级联2路A相输出

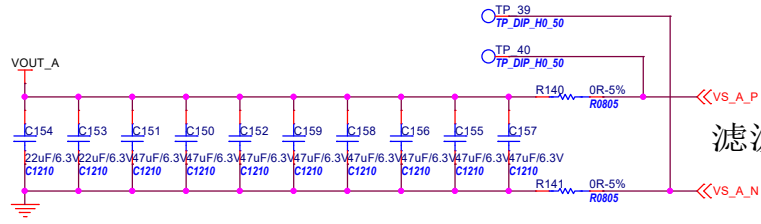


A相CH3路输出-第3路/共3路并联输出
暂定级联2路A相输出

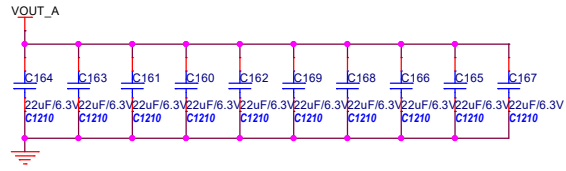


Title <Title>				
Size Custom	Document Number <Doc>			Rev <RevCode>
Date:	Tuesday, November 20, 2018	Sheet	8 of 12	

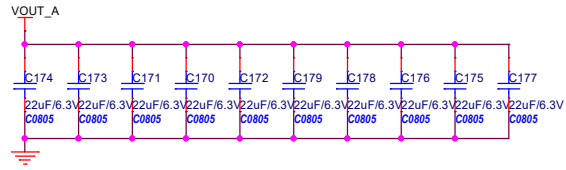
TPS53681输出滤波电路



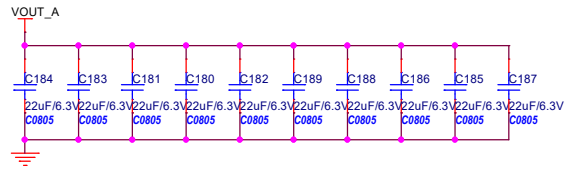
滤波瓷片电容-1210封装



滤波瓷片电容-1210封装



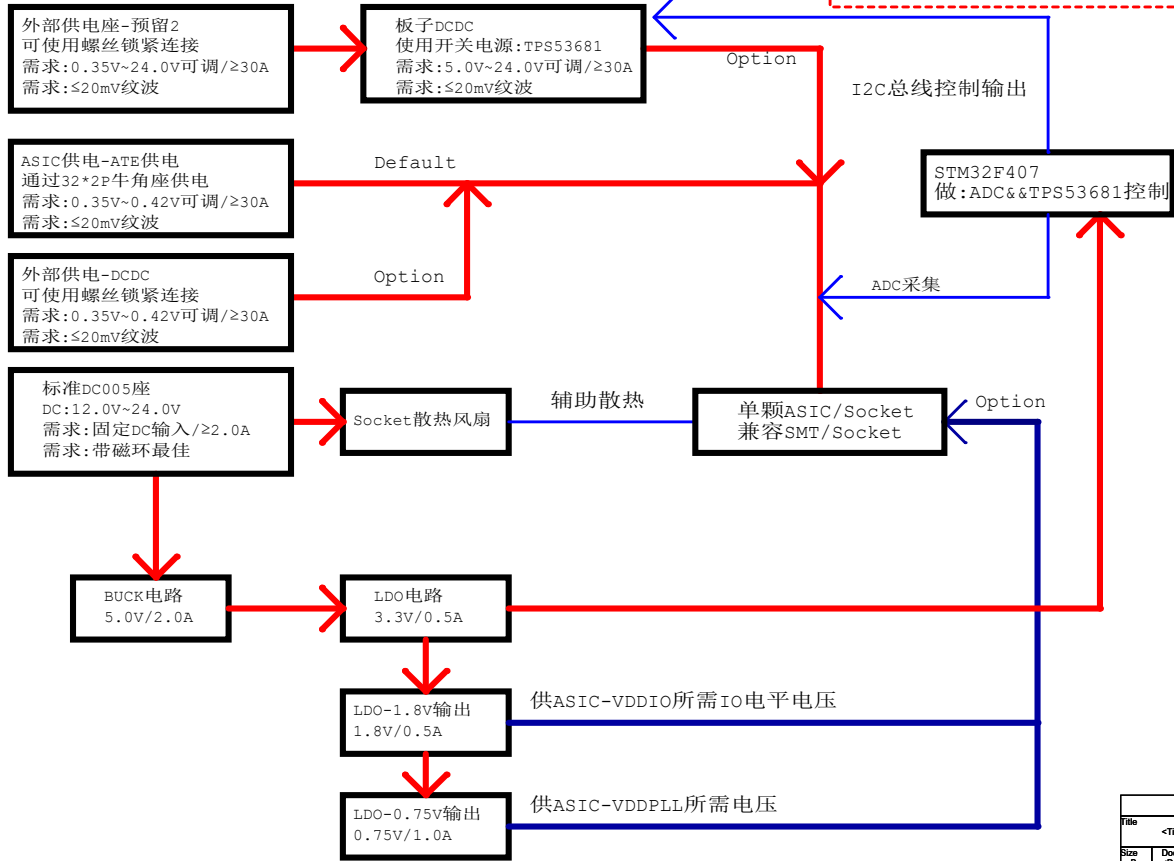
滤波瓷片电容-0805封装



滤波瓷片电容-0805封装

Title		
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Size	Document Number	Rev
A4	<Doc>	<RevCode>
Date:	Tuesday, November 20, 2018	Sheet 9 of 12

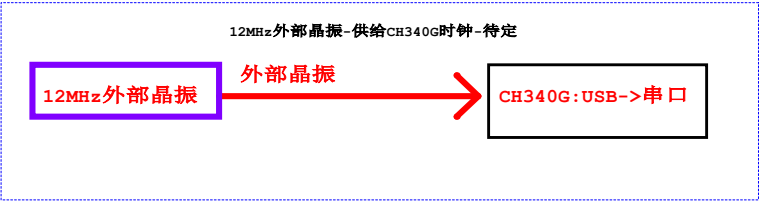
Power tree



Title		<Title>
Size	Document Number	Rev
B	<Doc>	<RevCode>
Date:		Tuesday, November 20, 2018
Sheet		10 of 12

Frequency and Sequence

Scheme1: 8MHz外部晶振 REF Select about <POWER&USB PORT>
8MHz外部晶振-供给STM32F407VET6时钟
12MHz外部晶振-供给CH340G时钟-待定



Title		
<Title>		
Size	Document Number	Rev
B	<Doc>	<RevCode>
Date: Tuesday, November 20, 2018 Sheet 11 of 12		

BOM标识说明部分：

- 1、"NC"表示"不焊接"
- 2、对"TPS53681":暂定使用其A相输出的2路共同输出

Socket电路部分：

- 1、对"Socket供电":直接支持ATE机台供电、N6950A供电,同时可支持板载DCDC供电

ASIC电路部分：

- 1、对"ASIC供电":直接支持ATE机台供电、N6950A供电,同时可支持板载DCDC供电

"TPS53681"BUCK电路部分：

- 1、对"TPS53681":暂定使用其A相输出的3路共同输出
- 2、对"TPS53681":控制总线为"SMBUS-I2C通讯",预留"STM32F4"及"外部接线座"2种方式

Title		<Title>	
Size	A	Document Number	Rev
<Doc>		<RevCo	
Date:	Tuesday, November 20, 2018		Sheet 12 of 12