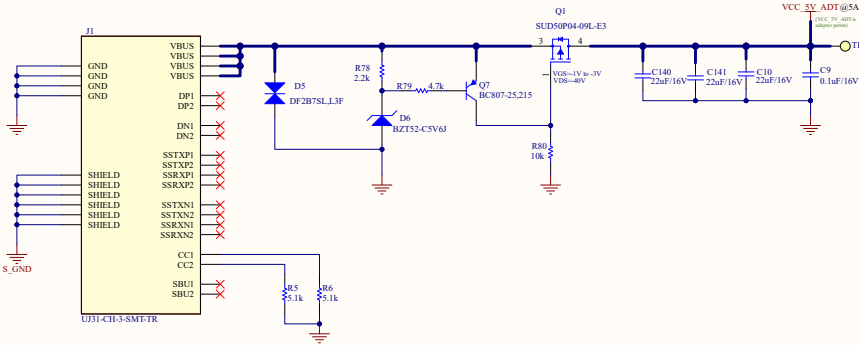
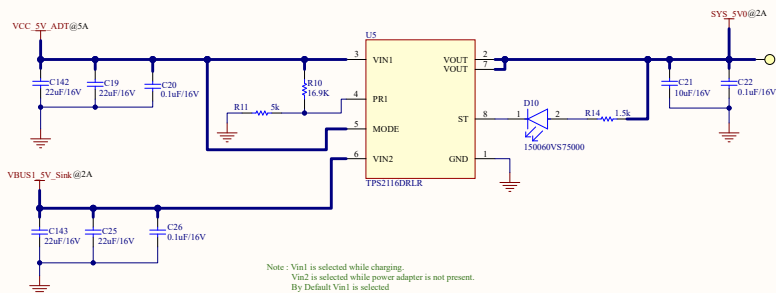


Power Adapter

Type-C Connector (Power Port)



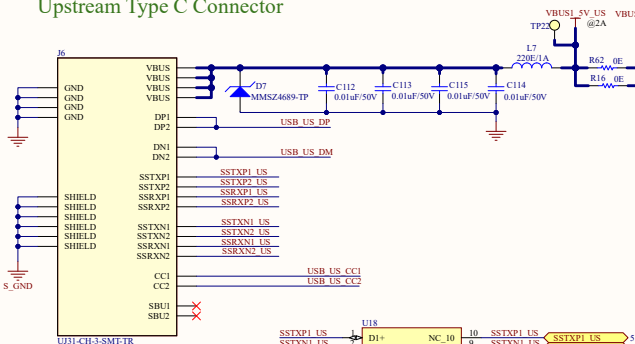
Voltage Switch For VBUS@2A



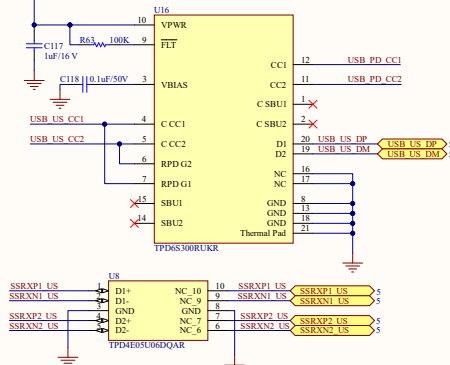
Note: Vin1 is selected while charging.
Vin2 is selected while power adapter is not present.
By Default Vin1 is selected

Up Stream Port (Always Tablet Connected)

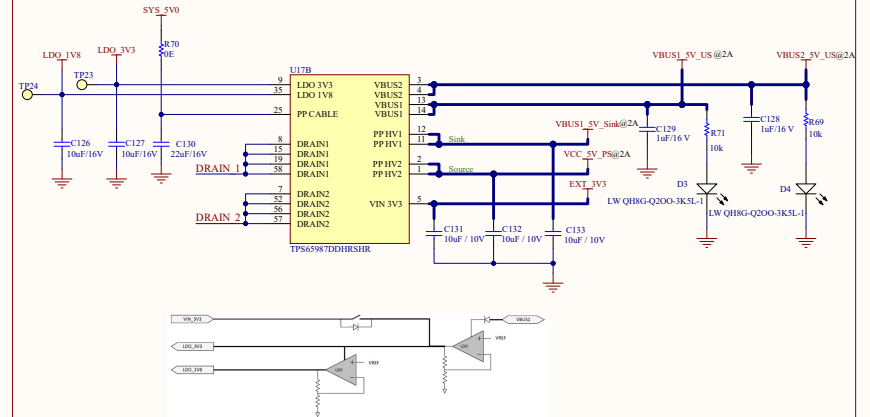
Upstream Type C Connector



Protection Circuit



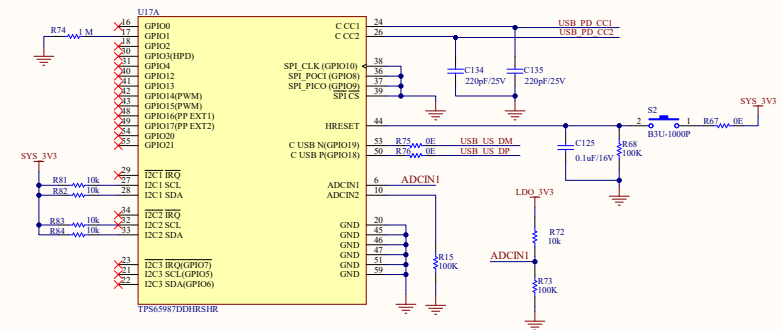
PD Controller



Note-1: The normal power supply input is VIN_3V3. In this mode, current flows from VIN_3V3 to LDO3V3 to power the core 3.3-V circuitry and I/Os and LDO_3V3 to LDO_1V8 to power the 1.8-V core digital circuitry.

Note-2: When VIN_3V3 power is unavailable and power is available on VBUS1 or VBUS2, the TPS65987D is powered from VBUS. In this mode, the voltage on VBUS1 or VBUS2 is stepped down through an LDO to LDO_3V3 and LDO_1V8.

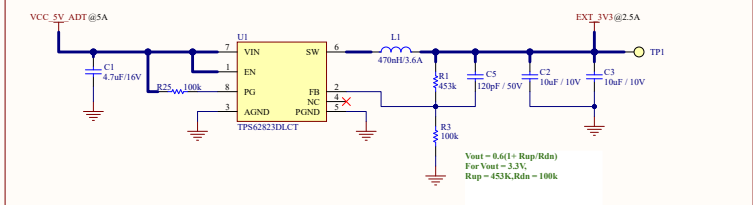
Note-3: LDO_3V3 may be generated from either VIN_3V3 or SYS_3V3. If LDO_3V3 is generated from VBUS, TPS65987D port only operate as sinks.



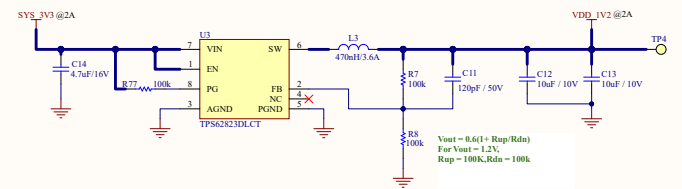
MH1 MH2 MH3 MH4 FID1 FID2

Power Supply

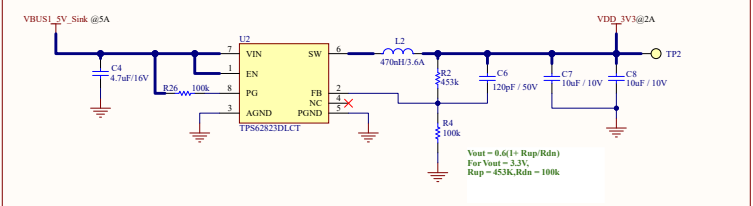
3.3V@2A Buck Regulator(Charging)



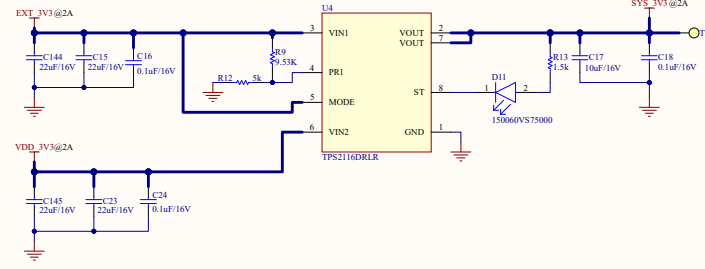
1.2V Buck Regulator@1.5A



3.3V@2A Buck Regulator(Discharging)

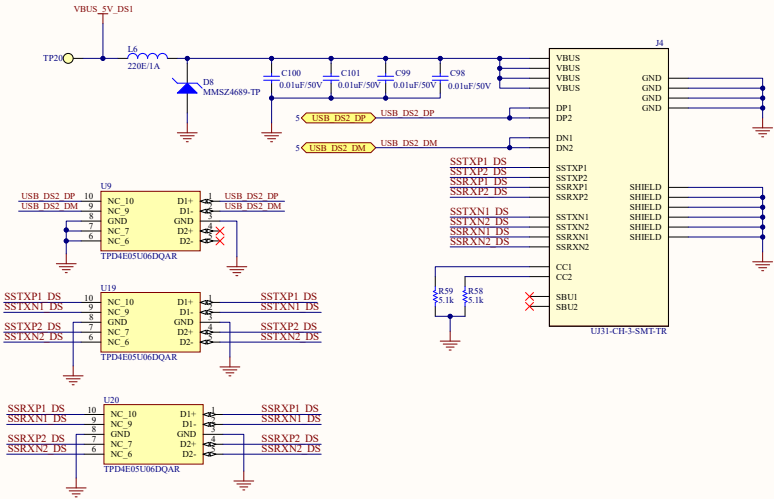


Voltage Switch For 3.3V@2A

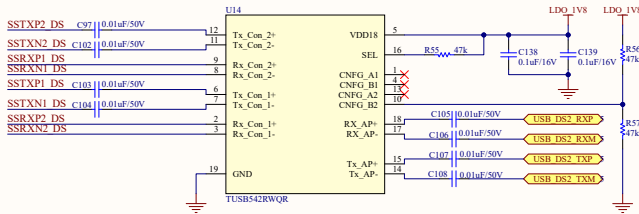


Down Stream Port

Type-C Connector (Card Reader)



DownStream 2:1 MUX



5GMM Connector (RaPID)

