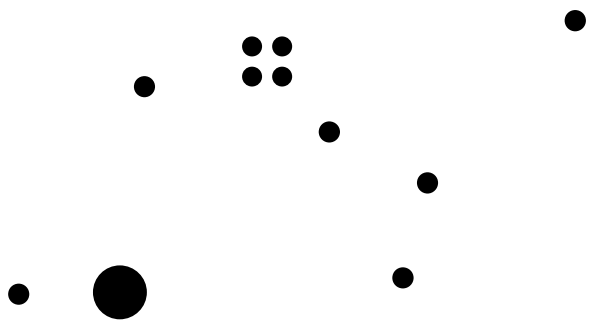
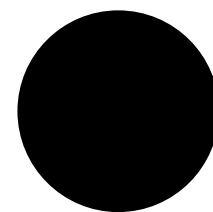
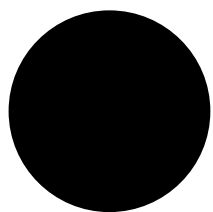
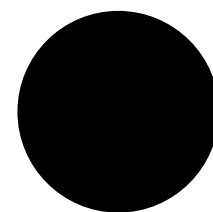
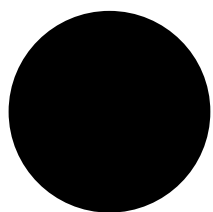


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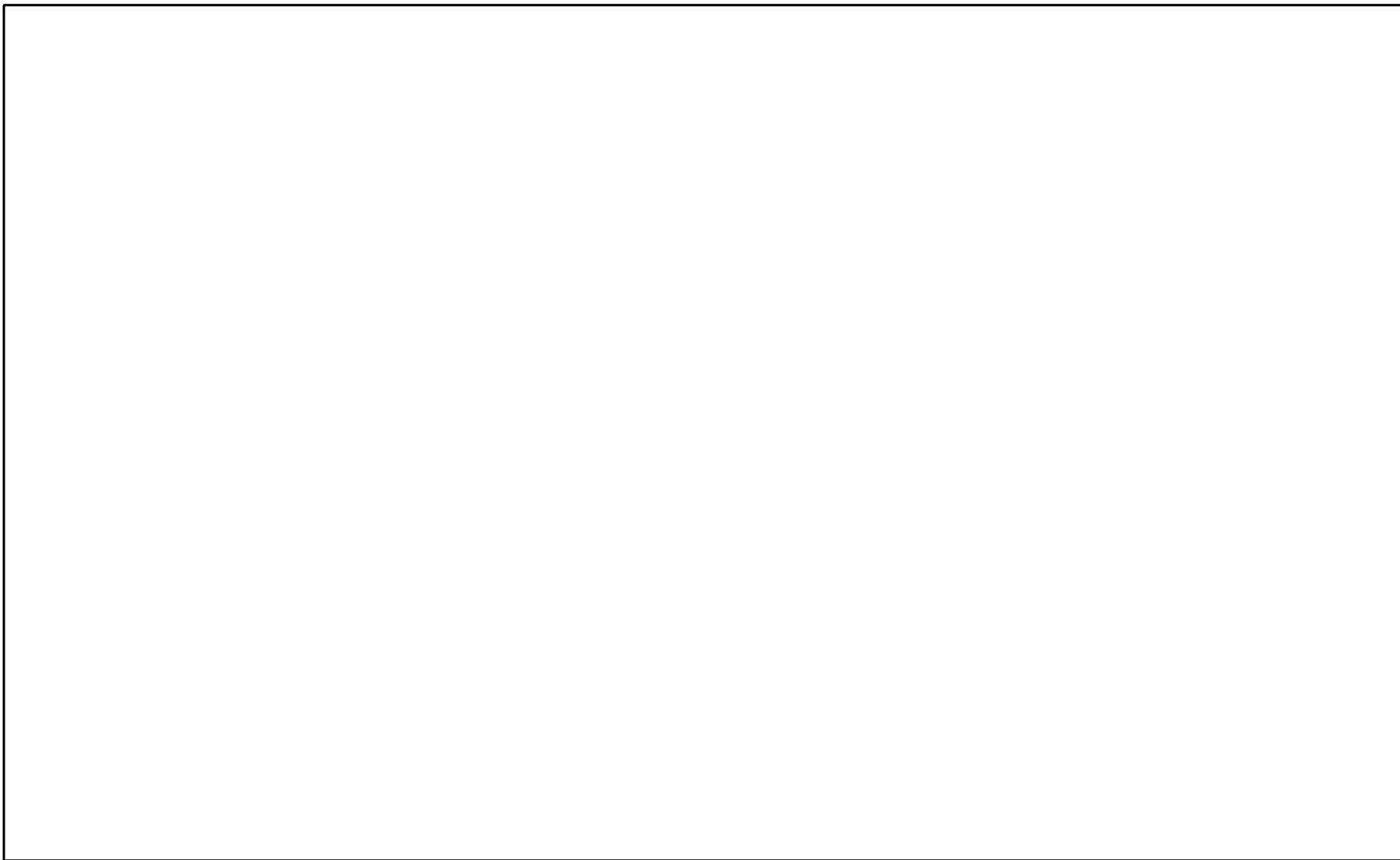
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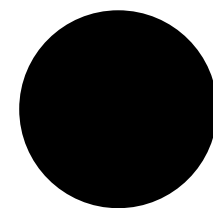
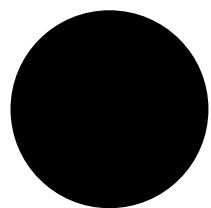
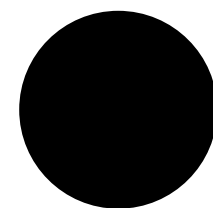
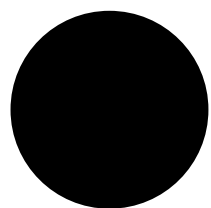
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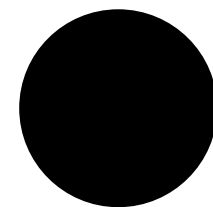
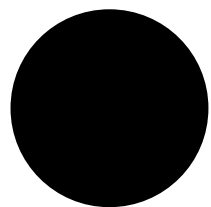
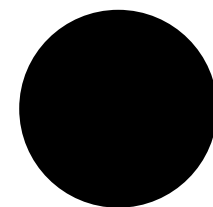
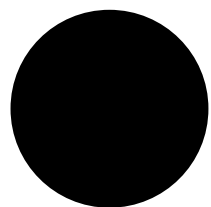
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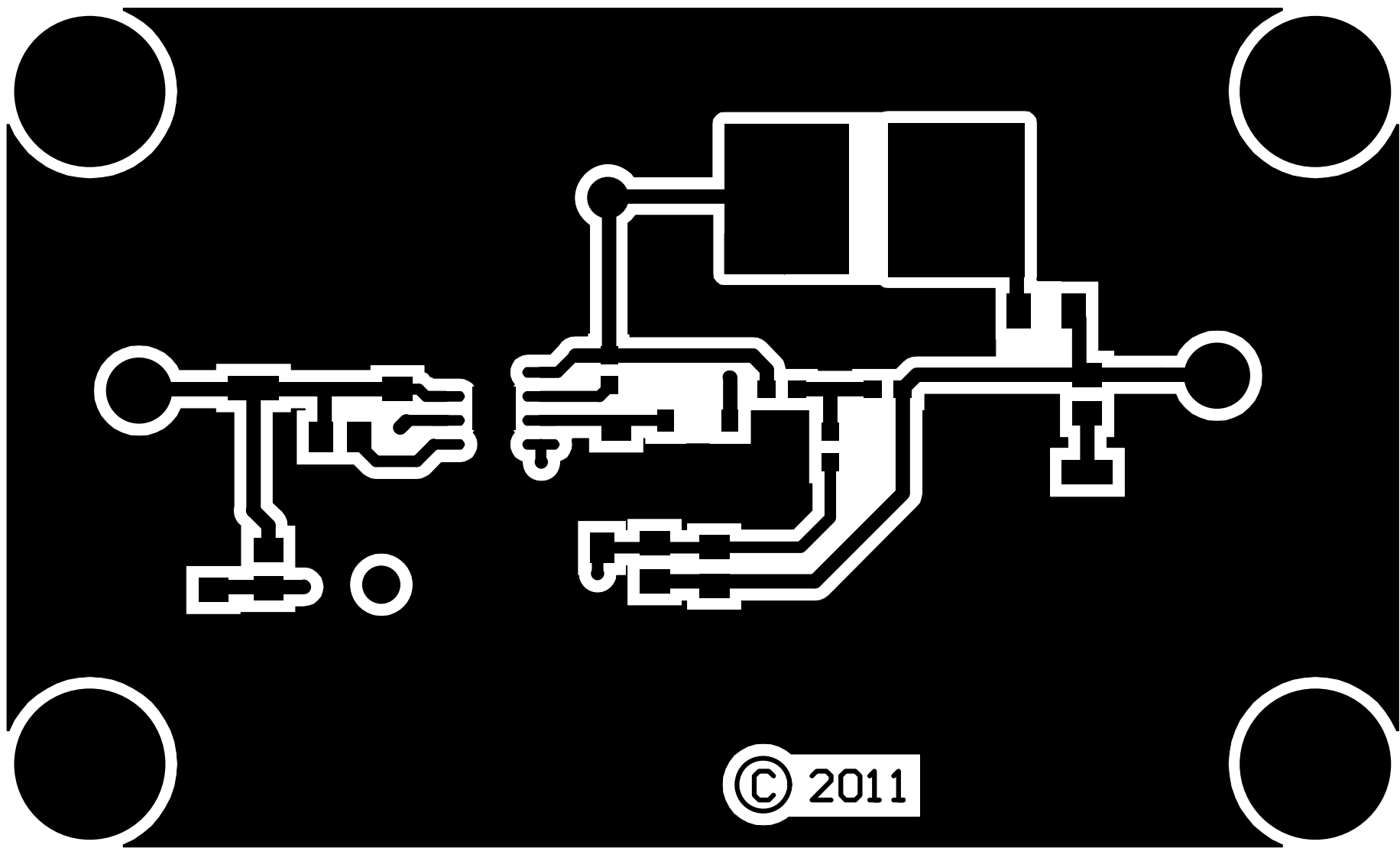
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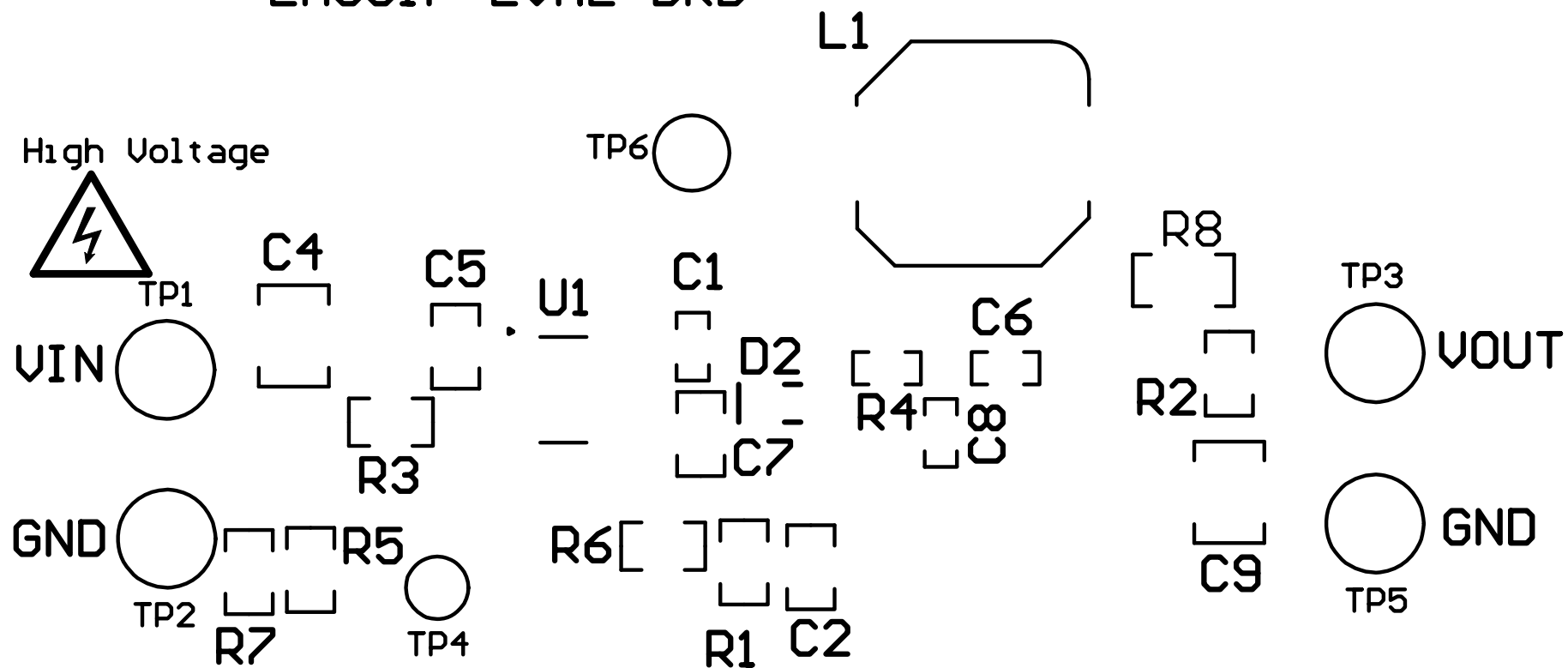


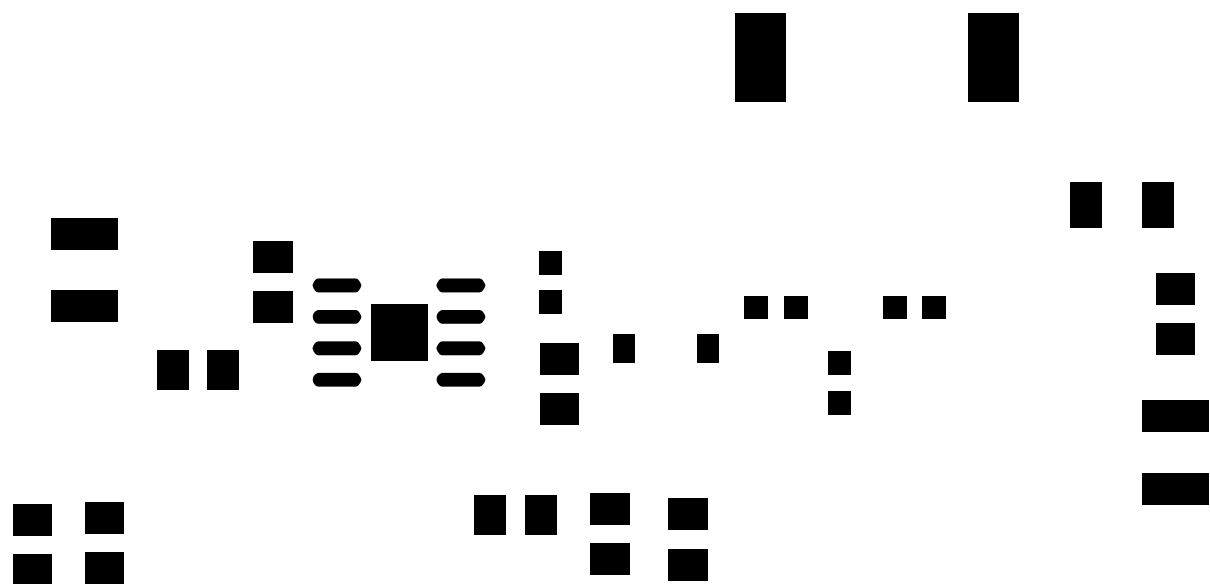


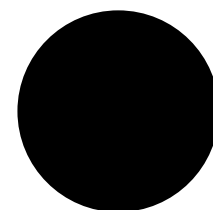
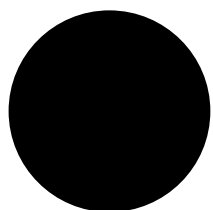
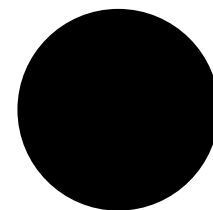
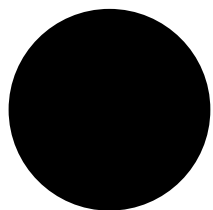


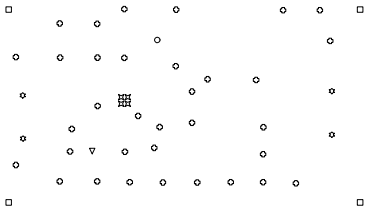
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LM5017 EVAL BRD









Symbol	Ht Count	Finished Hole Size	Plated	Hole Type
H	4	12.992mil (0.33mm)	PTH	Round
O	34	14mil (0.3556mm)	PTH	Round
V	1	40mil (1.016mm)	PTH	Round
O	1	63mil (1.6002mm)	PTH	Round
V	4	98.425mil (2.5mm)	PTH	Round
□	4	125.984mil (3.2mm)	PTH	Round
48 Total				

Drill Table

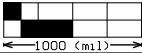
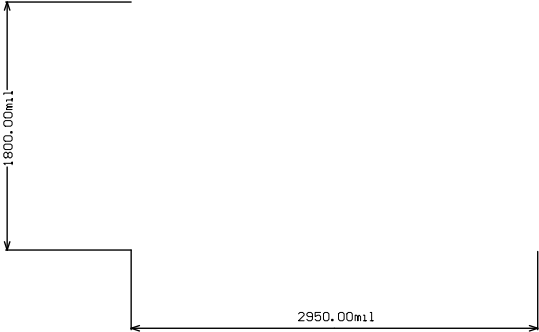
ALL ARTWORK VIEWED FROM TOP SIDE	LM5017 EVAL BRD		
Drill Drawing	880	600711.pcb_minorRev	
LM5017a_ Buck_111006.GD1	10/21/2011	9:16:26 AM	NATIONAL SEMICONDUCTOR

ALL ARTWORK VIEWED FROM TOP SIDE	LM5017 EVAL BRD		
M10 Fab Notes	880	600711 .pcb_minorRev	
LM5017a_ Buck_111006.GM10	10/21/2011 9:16:26 AM	NATIONAL SEMICONDUCTOR	

DESIGN INFORMATION	
BOARD SIZE (REFER ALSO ARRAY/PANEL PROFILING INFORMATION)	
3450MIL	X 4950MIL
Number of Layers : 2	
MIN. TRACK WIDTH: 8 MIL	
MIN. CLEARANCE: 8 MIL	
MIN. VIA PAD SIZE: 24 MIL	
MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL	
PER IPC-D-275 CLASS 2 LEVEL C	
REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL	
MATERIAL:	
<input checked="" type="checkbox"/> FR-4 <input type="checkbox"/> FR-4 High Tg <input type="checkbox"/> OTHER	
THICKNESS: <input checked="" type="checkbox"/> 62 MIL (1.6mm) +/-10% <input type="checkbox"/> OTHER	
TOLERANCE: <input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2	
<input type="checkbox"/> OTHER +/-	
BOW & TWIST: <input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2	
<input type="checkbox"/> OTHER +/-	
COPPER THICKNESS (FINISHED):	
OUTER: <input type="checkbox"/> 1.4MIL (1oz) <input type="checkbox"/> 2MIL (1.4oz) <input checked="" type="checkbox"/> 2.8MIL (2oz)	
INNER SIGNAL: <input type="checkbox"/> 1.4MIL (1oz) <input checked="" type="checkbox"/> 2.8MIL (2oz)	
DRILLING:	
REFERENCE: <input checked="" type="checkbox"/> AS SHOWN <input checked="" type="checkbox"/> NC DRILL FILES	
PTH MIN COPPER THICKNESS: <input checked="" type="checkbox"/> 1MIL <input type="checkbox"/> OTHER	
BOARD FINISH:	
SILKSCREEN: <input checked="" type="checkbox"/> TOP <input checked="" type="checkbox"/> BOTTOM	
SILKSCREEN COLOR: <input checked="" type="checkbox"/> WHITE <input type="checkbox"/> OTHER	
SOLDER RESIST COLOR:	
<input checked="" type="checkbox"/> GREEN <input type="checkbox"/> BLUE <input type="checkbox"/> OTHER	
SURFACE FINISH: <input type="checkbox"/> IMMERSION GOLD (ENIG) <input type="checkbox"/> Pb-FREE HASL	
<input checked="" type="checkbox"/> OTHER IMMERSION SILVER	
ARRAY/PANEL:	
<input type="checkbox"/> CUT AND TRIM PER MECH LAYER 1	
<input type="checkbox"/> N.C. ROUTE <input checked="" type="checkbox"/> V. SCORE	
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF:	
<input checked="" type="checkbox"/> ANSI IPC-A-600F CLASS -> <input type="checkbox"/> 1 <input checked="" type="checkbox"/> 2 <input type="checkbox"/> 3	
<input checked="" type="checkbox"/> UL 94V-0 <input checked="" type="checkbox"/> RoHS <input type="checkbox"/> OTHER PER ORDER	
ADDITIONAL REQUIREMENTS:	
MICROSECTION: <input type="checkbox"/> YES	
BARE BOARD ELEC. TEST: <input checked="" type="checkbox"/> NONE <input type="checkbox"/> REQUIRED <input type="checkbox"/> PER ORDER	
MANUFACTURER'S ID/LOGO: <input type="checkbox"/> RAIL <input type="checkbox"/> METAL <input checked="" type="checkbox"/> SILK	

Layer Stack Up Detail for: LM5017 Buck_110728.PcbDoc						
Layer Name	Gerber Document	Copper Thickness	Dielectric Height	Dielectric Material	Dielectric Constant	Dielectric Type
Top Solder Mask	<.GTS>		0.4mil	Solder Resist	3.50	
Top Layer	<.GTL>	2mil				
			56mil	FR-4	4.80	Core
Bottom Layer	<.GBL>	2mil				
Bottom Solder Mask	<.GBS>		0.4mil	Solder Resist	3.50	

ALL ARTWORK VIEWED FROM TOP SIDE	LM5017 EVAL BRD		
M12 Stackup	880	600711 .pcb_minorRev	
LM5017a_ Buck_111006.GM12	10/21/2011 9:16:26 AM	NATIONAL SEMICONDUCTOR	



ALL ARTWORK VIEWED FROM TOP SIDE	LM5017 EVAL BRD		
M2 Board Dimensions	880	600711 .pcb_minorRev	
LM5017a_ Buck_111006.GM2	10/21/2011	9:16:25 AM	NATIONAL SEMICONDUCTOR

1	2	3	4	5	6
A					
B					
C					
D					
ALL ARTWORK VIEWED FROM TOP SIDE		LM5017 EVAL BRD		National Semiconductor and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. National and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. National and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.	
M9 Title Sheet		880	600711.pcb_minorRev		
LM5017a_ Buck_111006.GM9		10/21/2011 9:16:25 AM	NATIONAL SEMICONDUCTOR		
1	2	3	4	5	6



PROJECT TITLE: LM5017 EVAL BRD	
FILE NAME: LM5017a_ Buck_111006.PcbDoc	
FAB DRAWING NUMBER: 551600711.PCB_MinorRev	
DESIGNED FOR: Public Release	
SCALE: 1.00	ALTUM DESIGNER VERSION: 10.0.0.22577