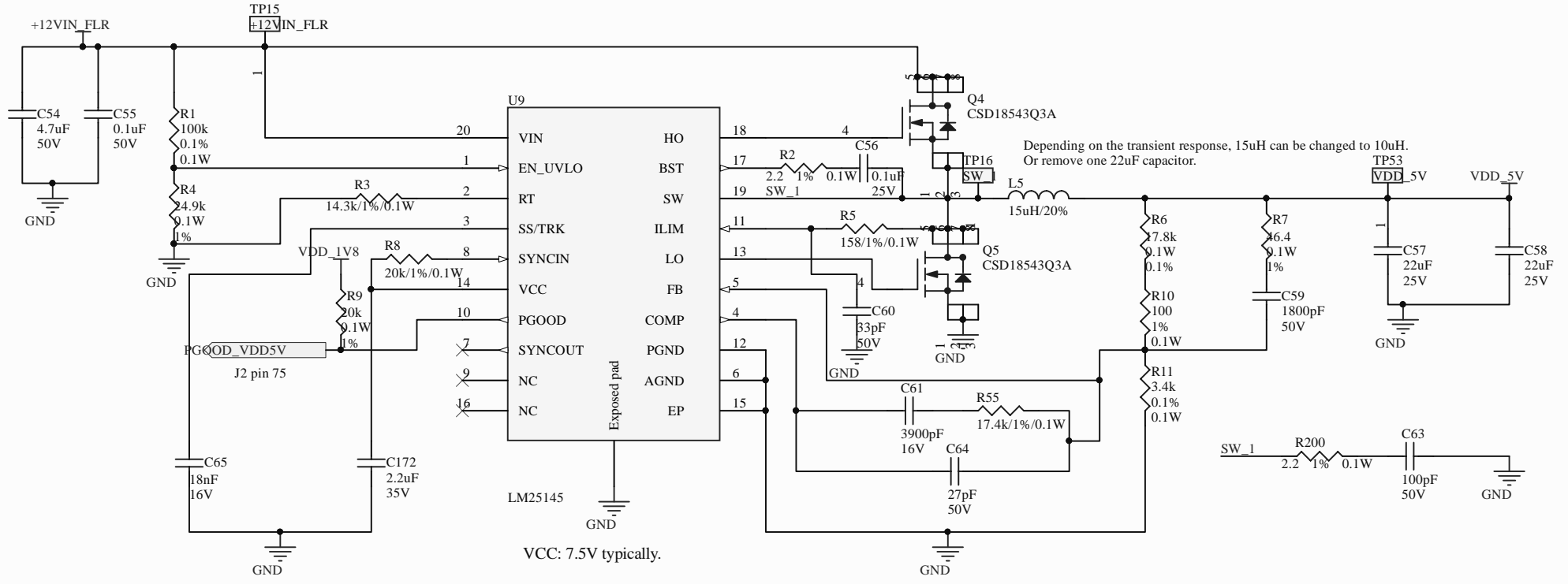


1. Fsw: 700KHZ; efficiency: 72% at 0.6A; 77% at 1A
2. Delt IL: 0.3A (peak to peak IL)
3. Current limit set point: 4A
4. Cout: effective capacitance including 10 pcs paralleled 10uF on core board input capacitance (typical value): 73.2uF
5. Overall ESR including 10 pcs of 10uF at core board_Cout=1.11mohm
6. soft start: 1.4ms
7. UVLO turn on: 6V; UVLO turn off: 5V
8. Compensation: crossover frequency: 80KHZ; phase margin: 58
9. Place R236 and C235 closed to SW pin and PGND.



Title			iVHM_24V to 5V power supply		
Size	Number		Revision		
A4			B		
Date:	6/29/2022		Sheet 1 of 19		
File:	C:\Users\...\24V to 5V power supply.SchDoc		Drawn By: Man qiusheng		