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Orderable: TPS56339EVM		Designed for: Public Release	Mod. Date: 3/12/2019
TID #: N/A		Project Title: TPS56339EVM	
Number: BSR040	Rev: A	Sheet Title:	
SVN Rev: Version control disabled		Assembly Variant: 001	Sheet: 1 of 2
Drawn By: Ryan Hu		File: BSR040A_Schematic.SchDoc	Size: B
Engineer: Ryan Hu		Contact: http://www.ti.com/support	

DNP

FID1

DNP

FID2

DNP

FID3

SH-J1

H1

SJ-5303 (CLEAR)

H2

SJ-5303 (CLEAR)

H3

SJ-5303 (CLEAR)

H4

SJ-5303 (CLEAR)

PCB Number: BSR040

PCB Rev: A

PCB

LOGO

Texas Instruments

PCB

LOGO

WEEE logo

PCB

LOGO

FCC disclaimer

CAUTION HOT SURFACE

CE Mark

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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Orderable: TPS56339EVM		Designed for: Public Release	Mod. Date: 3/18/2019
TID #: N/A		Project Title: TPS56339EVM	
Number: BSR040	Rev: A	Sheet Title:	
SVN Rev: Version control disabled		Assembly Variant: 001	Sheet: 2 of 2
Drawn By: Ryan Hu		File: BSR040A_EVM_Hardware.SchDoc	Size: B
Engineer: Ryan Hu		Contact: http://www.ti.com/support	