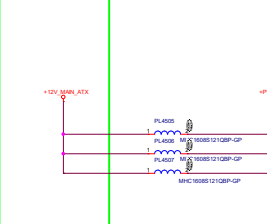


OFFPAGE-Signal

Need EE Check



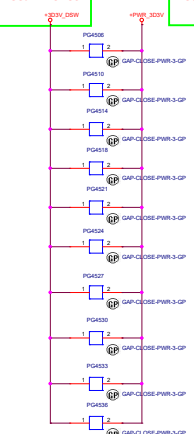
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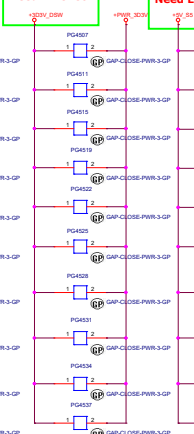
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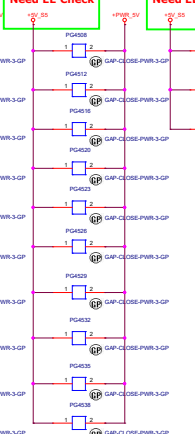
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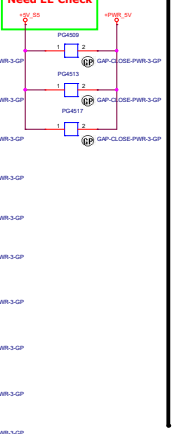
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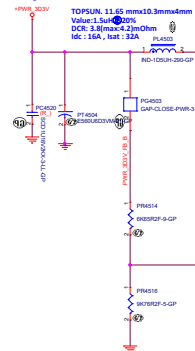


Need EE Check



Power rail :+PWR_303V
Vin = 12V, Vout = 3.3V
PWM Frequency : 475kHz
I_{max} = 18.3A, 27.5A <CCP< 36.6A
LIR=3.36A/18.3A=18.4%(Error: 20%~40%)
Output ripple current = 3.36A

ELITE/560uF/6.3V/6.8*9mm,
Ripple Current=4.7Arms,ESR=8mohm
Total Cout capacitance=560uF



$$V_{out} = 2 * \left(\frac{R1}{R1 + R2} \right) = 2 * \left(\frac{16}{16 + 9.76} \right) = 3.36V$$

Table 3. EnablingPGOOD State (TPS81275B-C)

EN1	EN2	VREG5	VREG3	CH1 (5Vout)	CH2 (3.3Vout)	VLK1	PGOOD
OFF	OFF	ON	ON	OFF	OFF	OFF	Low
ON	OFF	ON	ON	ON	OFF	ON	Low
OFF	ON	ON	ON	OFF	ON	OFF	Low
ON	ON	ON	ON	ON	ON	ON	High

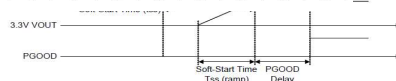


Figure 6. RT6575B Timing

10uF/25V, Ripple Current = 100mA
ELITE/270uF/16V/6.8*9.0mm,
Ripple current=5.08A,ESR=10mohm.
Input ripple current = 8.2A
Total input capacitance=290uF

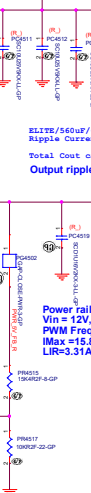
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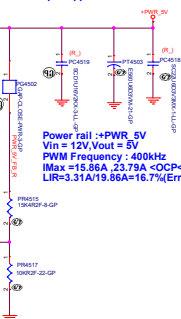
PU4501	RT6576D	RT6585B
PR4518	[R_]	200
PR4511	80.6K	19.1K
PR4512	80.6K	19.1K

Input ripple current = 7.2A
10uF/25V, Ripple Current = 100mA
ELITE/270uF/16V/6.8*9.0mm,
Ripple current=5.08A,ESR=10mohm.
Total input capacitance = 290uF

Need EE Check



Power rail :+PWR_5V
Vin = 12V, Vout = 5V
PWM Frequency : 400kHz
I_{max} = 15.86A, 23.75A <CCP< 31.72A
LIR=3.31A/15.86A=16.7%(Error: 20%~40%)
Output ripple current = 3.31A



Need EE Check

