Sequencer Selection Guide

	UCD90240	UCD90160	UCD90120A	UCD90124A	UCD9090	UCD90910	UCD9081
# Rails Sequenced	24	16	12	12	10	10	8
# of Monitor Inputs	24	16	13	13	11	13	8
ADC Ref Accuracy	External	0.5% Internal	0.5% Internal	0.5% Internal	0.5% Int ot Ext	0.5% Internal	External
Voltage Margining*	24	10	10	10	10	10	N/A
Fan Control*	N/A	N/A	N/A	4	N/A	10	N/A
Multi-phase PWM clock outputs*	24	8	8	8	8	8	N/A
ACPI Sleep State Control	Yes	Yes	Yes	No	Yes	No	No
Current and Temp Monitor Scaling	Yes	No	Yes	Yes	Yes	Yes	No
NV Fault Logs	100	8	16	10	15	20	8
Other NV Logging	Blackbox, Peaks, Resets, RTC	Peaks, Resets, Run-time clock	Peaks, Resets, Run-time clock	Peaks, Resets, Run-time clock	Peaks, Resets, Run-time clock	Peaks, Resets, Run-time clock	N/A
Max GPI/GPO*	24/12	8/16	8/12	8/12	8/10	8/10	0/4
Internal Temp Sensor	Yes	Yes	Yes	Yes	Yes	Yes	No
Communication and Programming I/F **	PMBus/I2C, JTAG	PMBus/I2C, JTAG	PMBus/I2C, JTAG	PMBus/I2C, JTAG	PMBus/I2C, JTAG	PMBus/I2C, JTAG	I2C
Watchdog Timer	Yes	Yes	Yes	Yes	Yes	Yes	No
Package Type (size, mm)	157-ball BGA (9x9)	64-pin QFN(9x9)	64-pin QFN(9x9)	64-pin QFN (9x9)	48-pin QFN (7x7)	64-pin QFN (9x9)	32-pin QFN (5x5)
Availability	Production	Production	Production	Production	Production	Production	Production

* Table shows the max number of each feature supported by each device. For example, the UCD90124 has 12 PWM pins used as any combination of margining, PWM, fan control, or GPIO up to the max listed. See data sheets for details.

** JTAG interface is for programming only.