

Registers:

Charger Advanced Comm Errors

BQ25750 Default View BQ25750 Field View

Registers

Save Registers Load Registers Start Log Write Register Read Register Auto Read: OFF Update Mode: Immediate I2C Address: D6(68) Default View

16 Bit Registers

VAC_DPM_FLAG: Normal	VAC_FLAG: Normal
CV_TMR_FLAG: Normal	CHARGE_FLAG: CHARGE_STAT[2:0] bits changed (transition to any state)
PG_FLAG: Normal	ACFET_FLAG: Normal
BATFET_FLAG: Normal	TS_FLAG: Normal
REVERSE_FLAG: Normal	FSW_SYNC_FLAG: Normal
MPPT_FLAG: Normal	VAC_UV_FLAG: Normal
VAC_OV_FLAG: Normal	IBAT_OCP_FLAG: Normal
VBAT_OV_FLAG: Normal	TSHUT_FLAG: Normal
CHG_TMR_FLAG: Normal	DRV_OKZ_FLAG: Normal

Mask

☐ ADC_DONE_MASK	☐ IAC_DPM_MASK	☐ VAC_DPM_MASK	☐ WD_MASK
☐ CV_TMR_MASK	☐ CHARGE_MASK	☐ PG_MASK	☐ ACFET_MASK
☐ BATFET_MASK	☐ TS_MASK	☐ REVERSE_MASK	☐ FSW_SYNC_MASK
☐ MPPT_MASK	☐ VAC_UV_MASK	☐ VAC_OV_MASK	☐ IBAT_OCP_MASK
☐ VBAT_OV_MASK	☐ TSHUT_MASK	☐ CHG_TMR_MASK	☐ DRV_OKZ_MASK

Reverse Mode Battery Discharge Current

IBAT_REV: 20A ☒ EN_CONV_FAST_TRANSIENT

Gate Driver Strength and Dead Time

BOOST_HS_DRV: Fastest	BUCK_HS_DRV: Fastest
BOOST_LS_DRV: Fastest	BUCK_LS_DRV: Fastest
BOOST_DEAD_TIME: 45ns	BUCK_DEAD_TIME: 45ns

Part Information

PART_NUM: BQ25750 DEV_REV: 2

Auto Read register Off

Registers

Save Registers Load Registers Start Log Write Register Read Register Auto Read: OFF Update Mode: Immediate I2C Address: D6(68) Default View

16 Bit Registers

ADC_DONE_STAT: Conversion not complete	IAC_DPM_STAT: Normal
VAC_DPM_STAT: Normal	WD_STAT: Normal
CHARGE_STAT: Fast Charge (CC mode)	PG_STAT: Power Good
TS_STAT: Normal	MPPT_STAT: MPPT Disabled
FSW_SYNC_STAT: Normal, no external clock detected	CV_TMR_STAT: Normal
REVERSE_STAT: Reverse Mode off	ACFET_STAT: ACFET on
BATFET_STAT: BATFET off	VAC_UV_STAT: Input Normal
VAC_OV_STAT: Input Normal	IBAT_OCP_STAT: Battery current normal
VBAT_OV_STAT: Normal	TSHUT_STAT: Normal
CHG_TMR_STAT: Normal	DRV_OKZ_STAT: Normal

Flag

ADC_DONE_FLAG: Conversion not complete	IAC_DPM_FLAG: Normal
VAC_DPM_FLAG: Normal	WD_FLAG: Normal
CV_TMR_FLAG: Normal	CHARGE_FLAG: CHARGE_STAT[2:0] bits changed (transition to any state)
PG_FLAG: Normal	ACFET_FLAG: Normal
BATFET_FLAG: Normal	TS_FLAG: Normal
REVERSE_FLAG: Normal	FSW_SYNC_FLAG: Normal
MPPT_FLAG: Normal	VAC_UV_FLAG: Normal
VAC_OV_FLAG: Normal	IBAT_OCP_FLAG: Normal
VBAT_OV_FLAG: Normal	TSHUT_FLAG: Normal
CHG_TMR_FLAG: Normal	DRV_OKZ_FLAG: Normal

Mask

Auto Read register Off

BQ25750 Default View BQ25750 Field View

Registers

Save Registers Load Registers Start Log Write Register Read Register Auto Read: OFF Update Mode Immediate I2C Address D6(6B) Default View

16 Bit Registers

General Single-Bit

☒ EN_TERM ☒ EN_PRECHG ☒ EN_CHG_TMR ☒ EN_TMR2X

☐ WD_RST ☐ DIS_CE_PIN ☒ EN_CHG_BIT_RESET_BEHAVIOR ☐ EN_HIZ

☐ EN_IBAT_LOAD ☒ EN_CHG ☒ EN_ICHG_PIN ☒ EN_ILUM_HIZ_PIN

☐ DIS_PG_PIN ☐ DIS_STAT_PINS ☐ FORCE_STAT4_ON ☐ FORCE_STAT3_ON

☐ FORCE_STAT2_ON ☐ FORCE_STAT1_ON ☐ REG_RST ☐ EN_IAC_LOAD

☒ EN_PFM ☐ FORCE_BATFET_OFF ☐ PWRPATH_REDUCE_VDRV ☐ EN_BATFET_IDEAL_DIODE

☐ EN_AUTO_REV ☐ EN_REV ☐ FORCE_SWEEP ☐ EN_MPPT

☒ JEITA_ISETH ☒ EN_JEITA ☒ EN_TS ☐ BCOLD

☐ SYSREV_UV ☐ ADC_EN ☒ ADC_RATE ☐ ADC_AVG

☐ ADC_AVG_INIT ☐ IAC_ADC_DIS ☐ IBAT_ADC_DIS ☐ VAC_ADC_DIS

☐ VBAT_ADC_DIS ☐ VSYS_ADC_DIS ☐ TS_ADC_DIS ☒ VFB_ADC_DIS

General Multi-Bit

VBAT_LOWV 71.4% x VFB_REG

WATCHDOG Disable

CV_TMR disable

P_AND_O_TMR 0.5s

TS_T5 34.375% (60C)

TS_T2 68.4% (10C)

JEITA_VSET 97.6% x VFB_REG

BHOT 34.2% (60C)

TOPOFF_TMR Disable

CHG_TMR 12hr

VRECHG 97.6% x VFB_REG

FULL_SWEEP_TMR 3min

TS_T3 44.8% (45C)

TS_T1 73.25% (0C)

JEITA_ISETC 20% x ICHG_REG

ADC_SAMPLE 13 bit effective resolution

Status

ADC_DONE_STAT Conversion not complete

VAC_DPM_STAT Normal

CHARGE_STAT Fast Charge (FC mode)

IAC_DPM_STAT Normal

WD_STAT Normal

PG_STAT Power Good

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8 Bit Registers

General Multi-Bit

VFB_REG 1536 mV

IAC_DPM 50000 mA

IAC_REV 50000 mA

IPRECHG 4000 mA

VAC_MPP 0 mV

ADC Result

IAC_ADC - Use Field View 0 mA

VAC_ADC 0 mV

VSYS_ADC 0 mV

VFB_ADC 0 mV

ICHG_REG 2500 mA

VAC_DPM 4200 mV

VSYS_REV 5000 mV

ITERM 2000 mA

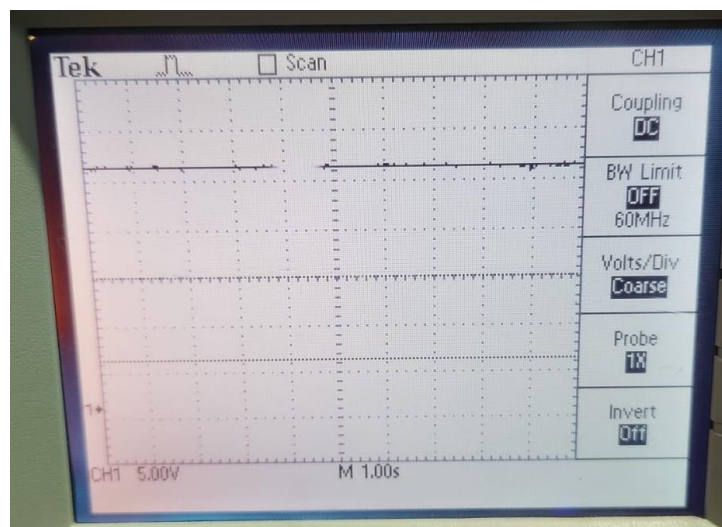
IBAT_ADC - Use Field View 0 mA

VBAT_ADC 0 mV

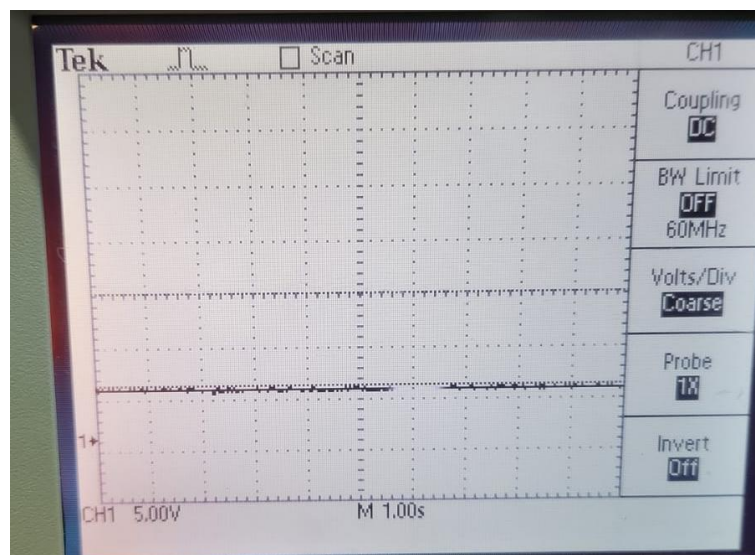
TS_ADC 0.0000 %

Auto Read register Off

TP4- ACDRV:



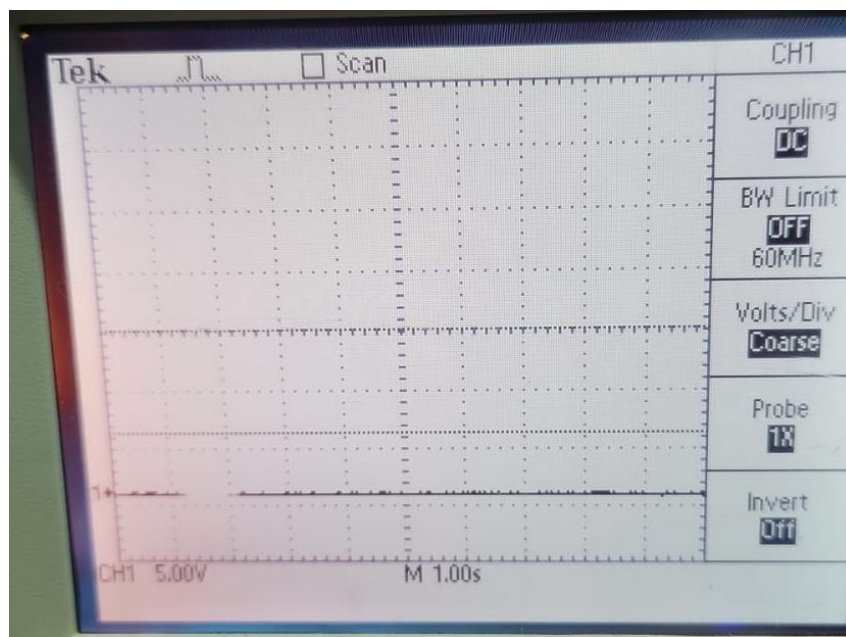
TP15- DRAV SUP:



TP2- SYS:



TP18- SW1:



TP19- SW2:

