







AREA REPRESENTS
TERMINAL PAD DIMENSIONS

.033(8)
[.85]
.098(4)
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CUSTOMER TO DETERMINE LAND LAYOUT

LOT CODE & DATE CODE

REFERENCE LAND SIZE

## ELECTRICAL SPECIFICATIONS @ 25° C unless otherwise noted:

PARAMETER		TEST CONDITIONS	VALUE
D.C. RESISTANCE	1-2	@20ºC	0.133 ohms ±10%
D.C. RESISTANCE	3-5	@20ºC	0.035 ohms ±20%
D.C. RESISTANCE	6-10	tie(6+7, 9+10), @20°C	0.0120 ohms max.
INDUCTANCE	3-5	10kHz, 100mV, Ls	21.00μH ±10%
SATURATION CURRENT	3-5	20% rolloff from initial	6.2A TBD
LEAKAGE INDUCTANCE	3-5	tie(1+2, 6+7+9+10),100kHz, 10mV, Ls	150nH typ., 300nH max.
DIELECTRIC	1-10	tie(2+3, 6+7), 1875VAC, 1 second	1500VAC, 1 minute
DIELECTRIC	1-5	625VAC, 1 second	500VAC, 1 minute
TURNS RATIO		(3-5):(2-1)	1:1, ±1%
TURNS RATIO		(3-5):(6-10), tie(6+7, 9+10)	2:1, ±1%

## 3 PRI 18-36Vdc 250kHz 5 \$ \$ 2 AUX 10V - 20mA

## **GENERAL SPECIFICATIONS:**

OPERATING TEMPERATURE RANGE: -40°C to +125°C including temp rise.

Designed to comply with the following requirements as defined by IEC60950-1, EN60950-1, UL60950-1/CSA60950-1 and AS/NZS60950.1:

- Functional insulation only

Customer to tie terminals 6+7 and 9+10 on PC board.

Application of the transformer allows for the leadwires between terminals 6&7 and 9&10 to solder bridge.

Wire insulation & RoHS status not affected by wire color. Wire insulation color may vary depending on availability.

DFM DRAWING TITLE PART NO. SP Packaging Specifications Tolerances unless otherwise specified: Angles: ±1° DATE 8/23/2018 Method: Tape & Reel Decimals: ±.005 [.13] **TRANSFORMER** PKG-1053 Fractions: ±1/64 Footprint: ± .005 [.13] ENG LJG 750317933 REV. 00 CONVENTION PLACEMENT This drawing is dual dimensioned. Dimensions in brackets are in millimeters. DATE 9/10/2018 SPECIFICATION SHEET 1 OF 1 www.we-online.com/midcom

.082 REF.(8)

[2.08]