

Reducing Precharge Phase Region for bq24650

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Battery Power Applications

ABSTRACT

The bq24650 is a battery charger that operates in three phases: precharge phase, fast-charge current regulation, and fast-charge voltage regulation. In many applications, a long precharge region is not required when using bq24650 as a charger. Thus, the following application note describes a method of reducing the precharge region for this charger. In this method, a circuit is designed to force the voltage of the feedback pin above the lower threshold for fast charge. This keeps the charger in fast-charge mode regardless of the battery voltage.

1 bq24650 Description

The bq24650 is a highly integrated switch-mode battery charger for solar panels. Its input voltage can vary from 5 V to 28 V. It charges batteries with adjustable voltage from 2.1 V to 26 V with feedback reference set internally to 2.1 V. This device charges batteries in three phases based on the state of charge of the battery at any given time as shown in [Figure 1](#).

The first phase is the precharge current regulation phase. In this phase, the voltage on the feedback pin must be lower than the typical value of 1.55 V (V_{LowV} typical). The purpose of this phase is to revive the deeply discharged cells. The charger applies a precharge current, usually very small, that can be set using the SRP and SRN pins. In the fast-charge current regulation phase, the feedback voltage must be between 1.55 V and 2.1 V. The charging current is 10 times higher than the current of the precharge phase. As the battery charges with time, its terminal voltage increases. As it reaches the expected regulation voltage level, the fast-charge phase ends and the fast-charge voltage regulation starts.

In this phase, the voltage is kept constant, and the current drops as the battery charges until termination. During termination, the charger stops injecting current into the battery as shown in [Figure 1](#).

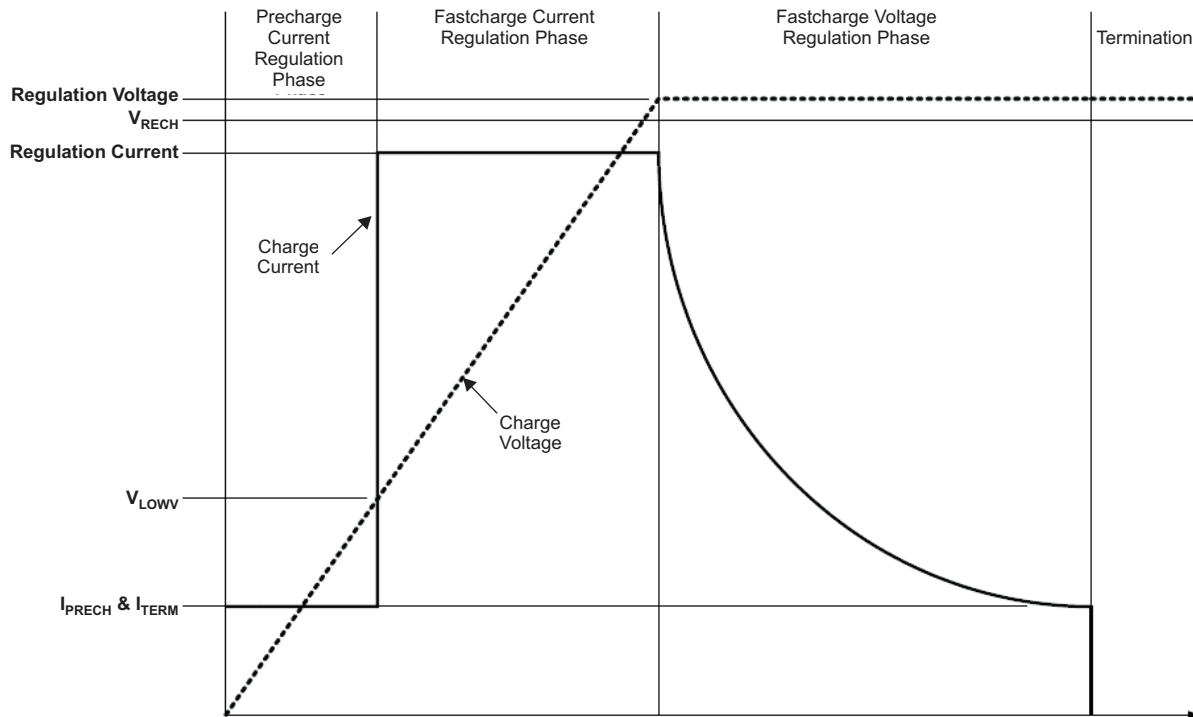


Figure 1. Typical Charge Profile

2 Circuit to Reduce the Precharge Region

As described in the previous section, if the battery voltage is below the V_{LOWV} , the charger enters the precharge phase. In some applications, the precharge region needs to be reduced. The following circuit partially disables the precharge region. The battery voltage can go down to about 2 V, and the charger is still working in the fast-charge region.

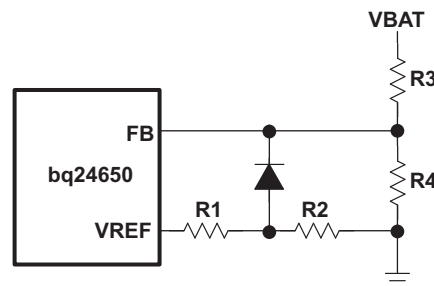


Figure 2. Circuit to Partially Disable Precharge Mode

The circuit in [Figure 2](#) clamps the FB pin above the V_{LOWV} . V_{REF} is always constant to 3.3 V and R1, R2, R3, and R4 are designed such that:

$$(V_{FB\ REG} + V_{LOWV}) / 2 = V_{REF} R2 / (R1 + R2) - V_F$$

$$V_{FB\ REG} = V_{BAT(Full\ Charge)} R4 / (R3 + R4)$$

Where:

- $V_{FB\ REG}$: Feedback regulation voltage
- V_{LOWV} : Precharge to fast-charge threshold voltage
- V_{REF} : Reference voltage
- $V_{BAT\ (Full\ Charge)}$: Battery voltage at full charge
- V_F : Forward diode voltage

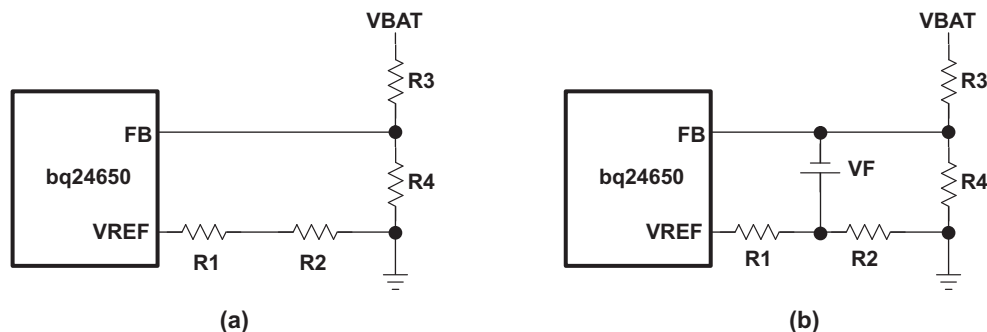


Figure 3. Circuit to Partially Disable Precharge Mode During Precharge and Fast-Charge Battery Voltage Levels

- If $V_{BAT} \times R4 / (R3 + R4) > V_{LOWV}$, the disable precharge mode circuit appears as in [Figure 3 \(a\)](#). During these conditions, the FB pin connected directly to the battery voltage through the resistor divider.
- If $V_{BAT} \times R4 / (R3 + R4) < V_{LOWV}$, the diode is automatically forward-biased as shown in [Figure 3 \(b\)](#). The clamp circuit forces the feedback pin voltage to approximately $(V_{FB\ REG} - V_{LOWV})/2$.

R2 must be at least 10 times smaller than R4 to make the clamper circuit dominant during the precharge phase. However, low resistance values on R1 and R2 can cause power loss from the reference pin to the ground. For that reason, A 1-10 kΩ range is preferred for R1 and R2. Also, the leakage current from the FB is about 100 nA. To have a good accuracy in the feedback pin reading, R3 and R4 must be in the range of 100 kΩ to 1000 kΩ. A design example is illustrated with results in the forthcoming sections.

3 Design Example

The bq24650 evaluation board is used to test the proposed circuitry. The feedback voltage regulation is given in the data sheet ([SLUSA75](#)) to be $V_{FB\ REG}$: 2.1 V and V_{LOWV} : 1.55 V. The reference voltage is V_{REF} : 3.3 V. In the EVM board, the R3 and R4 values have been given to be R3 = 499 kΩ and R4 = 100 kΩ. They are designed for $V_{BAT\ (Full\ Charge)}$: 12.57 V. The diode is chosen to have approximately 0.5-V forward voltage. Using Equation 1 of the bq24650 data sheet, R1 and R2 can be designed. Resistor R2 = 10 kΩ was chosen to be 10 times smaller than R4; solving Equation 1 for R1, the value was found to be 5 kΩ.

4 Test Result

First, the evaluation board is tested without the proposed circuitry, and the result is shown in [Figure 4](#). The precharge phase is active for any battery voltage less than 9.3 V. However, in the second test, the proposed circuitry is used, and the result shown in [Figure 5](#). The precharge region is reduced to a battery voltage of approximately 2 V. Any battery voltage above the 2-V level, the precharge phase is disabled.

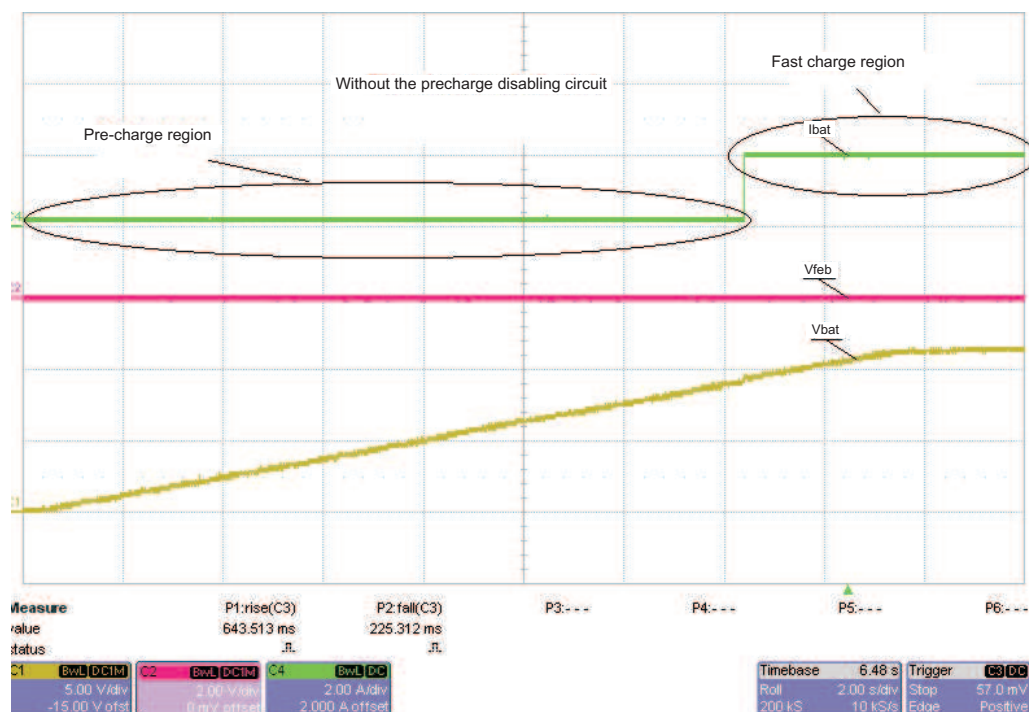


Figure 4. Without Additional Circuit, Precharge Region Fully Active

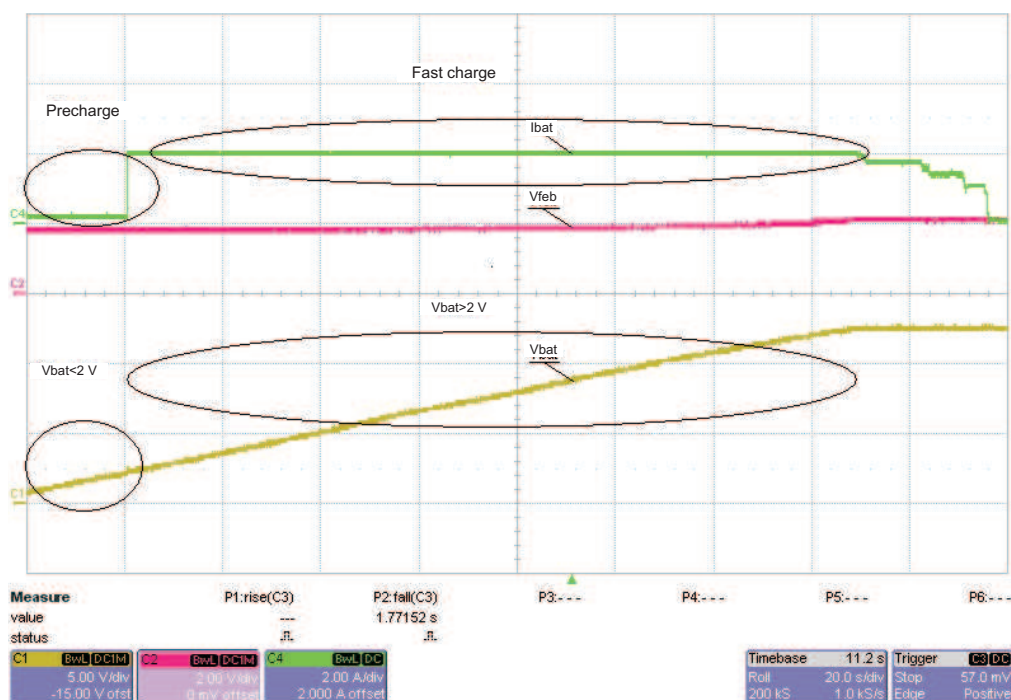


Figure 5. With Precharge Circuit, Precharge Region Disabled Partially

5 Conclusion

The precharge circuit described in this application report partially disables the precharge phase of the bq24650. It forces the feedback pin to regulation regardless of the battery voltage. However, the precharge phase cannot be fully disabled because of the SRP and SRN pins sensing the battery voltage. As the battery voltage drops below 2 V, the sensed feedback pin voltage is ignored, and the charger is forced to operate in the precharge mode.

6 References

1. *Using the bq24650 to Charge a Sealed, Lead-Acid Battery* application report ([SLVA437](#))
2. *bq24650, Synchronous Switch-Mode Battery Charge Controller for Solar Power With Maximum Power Point Tracking* data sheet ([SLUSA75](#))

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