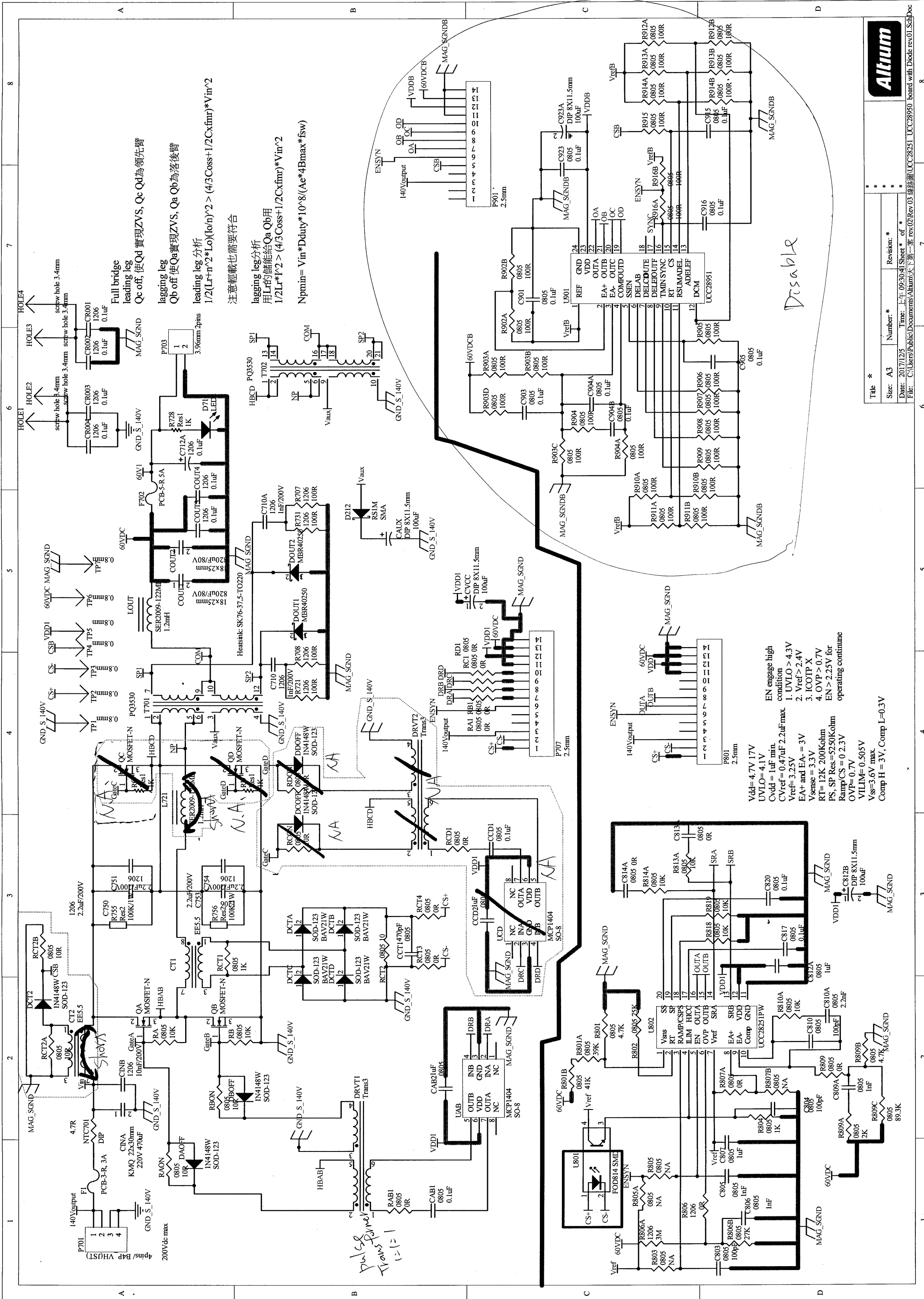




1 2 3 4 5 6 7 8

Full bridge leading leg Qc off, 使Qd 實現ZVS, Qc Qd為領先臂

lagging leg Qb off, 使Qa實現ZVS, Qa Qb為落後臂

leading leg 分析 $1/2(Lr+n^2*Lo)(Io/n)^2 > (4/3Coss+1/2Cxfr)*Vin^2$

注意輕載也需要符合

lagging leg 分析 用Lr的儲能給Qa Qb用 $1/2Lr*n^2 > (4/3Coss+1/2Cxfr)*Vin^2$

$Npmin = Vin * Dduty * 10^8 / (Ae * 4Bmax * fsw)$

Disable

Vdd=4.7V 17V
UVLO=4.1V
Cvdd=1uF min.
CVref=0.47uF 2.2uF max.
Vref=3.25V
EA+ and EA- = 3V
Vsense = 3.3V
RT=12K 200Kohm
PS, SP Res=5250Kohm
OVP=0.7V
VILIM=0.505V
Vss=3.6V max.
Comp H = 3V, Comp L=0.3V

EN engage high condition
1. UVLO > 4.3V
2. Vref > 2.4V
3. ICOTP X
4. OVP > 0.7V
EN > 2.25V for operating continue



Title	*
Size	A3
Number	*
Revision	*
Date	2017/12/5
Time	上午 09:30:41
Sheet	* of *
File	C:\Users\Public\Documents\Altium\UCC28951 board with Diode rev01.SchDoc