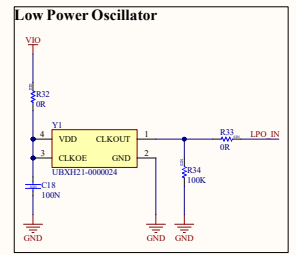
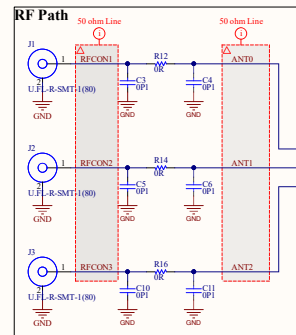


Schematic/Assembly Revision			
Rev.	Description	Date	By
A	Initial Release		iban

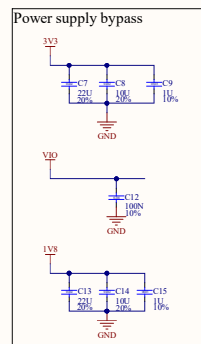
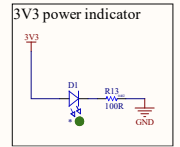
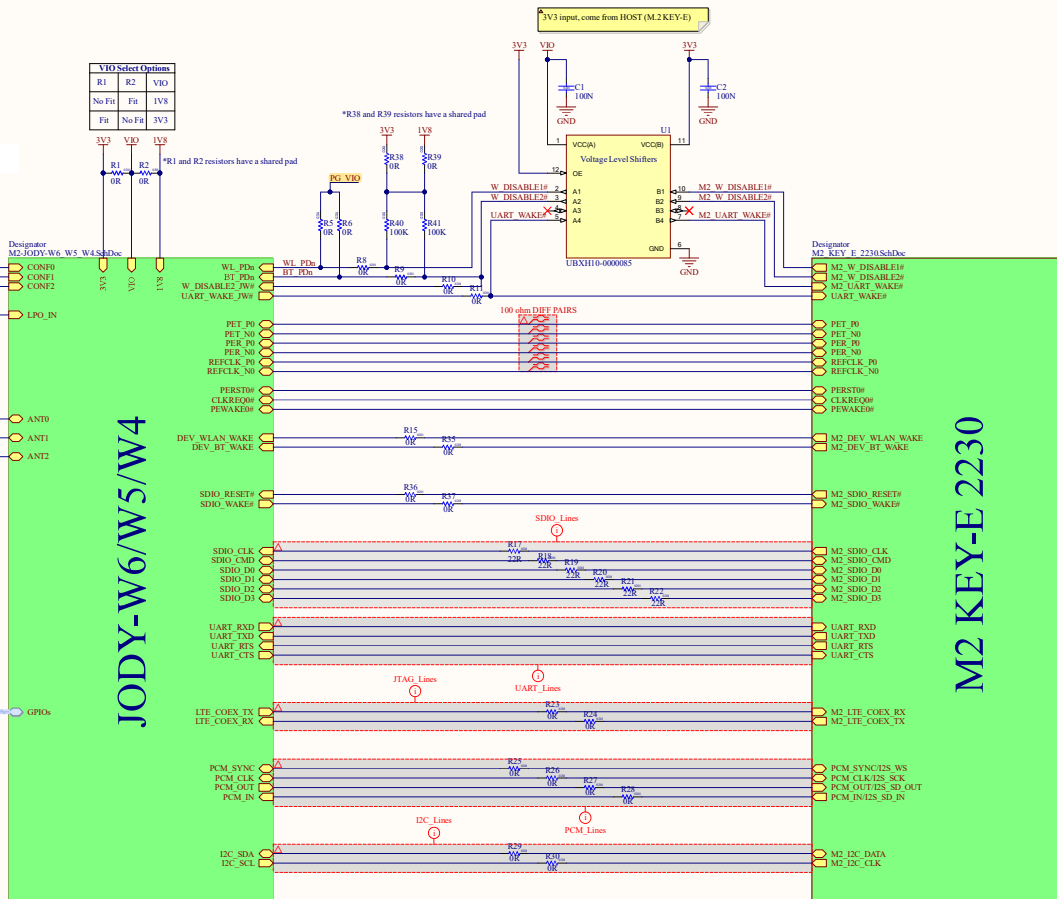
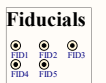
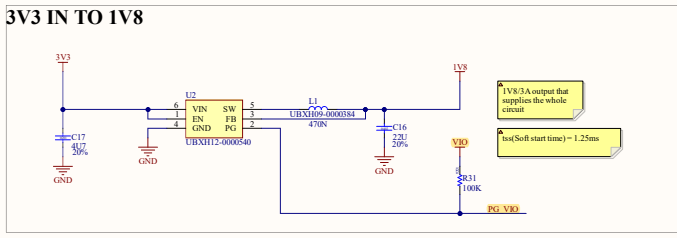
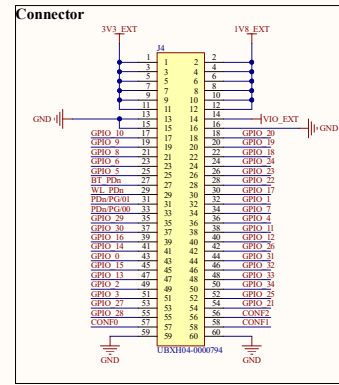
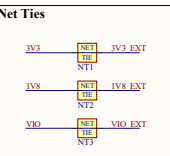
JODY-W6/W5/W4 Board Options					
Module	CONF2	CONF1	CONF0	WLAN	BT
JODY-W4	other	1	0	SDIO	UART
JODY-W4	other	1	1	PCle	UART
JODY-W5	other	1	1	SDIO	UART
JODY-W6	other	0	0	PCle	PCle
JODY-W6	other	0	1	PCle	UART

VIO Select Options		
R1	R2	VIO
No Fit	Fit	1V8
Fit	No Fit	3V3

- Set switch CONF0, CONF1 to OFF and remove CONF2 resistor to control through pin header connector
- No external pull-up resistor required to set a configuration bit to 1 SW1



- JODY-W6 Fan-Out signals**
- GPIOs
 - GPIO 0 - GPIO0
 - GPIO 1 - GPIO1
 - GPIO 2 - GPIO2
 - GPIO 3 - GPIO3
 - GPIO 4 - GPIO4
 - GPIO 5 - GPIO5
 - GPIO 6 - GPIO6
 - GPIO 7 - GPIO7
 - GPIO 8 - GPIO8
 - GPIO 9 - GPIO9
 - GPIO 10 - GPIO10
 - GPIO 11 - GPIO11
 - GPIO 12 - GPIO12
 - GPIO 13 - GPIO13
 - GPIO 14 - GPIO14
 - GPIO 15 - GPIO15
 - GPIO 16 - GPIO16
 - GPIO 17 - GPIO17
 - GPIO 18 - GPIO18
 - GPIO 19 - GPIO19
 - GPIO 20 - GPIO20
 - GPIO 21 - GPIO21
 - GPIO 22 - GPIO22
 - GPIO 23 - GPIO23
 - GPIO 24 - GPIO24
 - GPIO 25 - GPIO25
 - GPIO 26 - GPIO26
 - GPIO 27 - GPIO27
 - GPIO 28 - GPIO28
 - GPIO 29 - GPIO29
 - GPIO 30 - GPIO30
 - GPIO 31 - GPIO31
 - GPIO 32 - GPIO32
 - GPIO 33 - GPIO33
 - GPIO 34 - GPIO34
 - PDn (PG0) - PDn (PG0)
 - PDn (PG1) - PDn (PG1)
 - PDn (PG2) - PDn (PG2)



Prepared By iblox	Project:	PCB:	Revision: A
Author: iban	Board: M2 JODY-W6	Top_Level.SchDoc	Size: A2
Team:	Release Date:		Page: 1 / 3