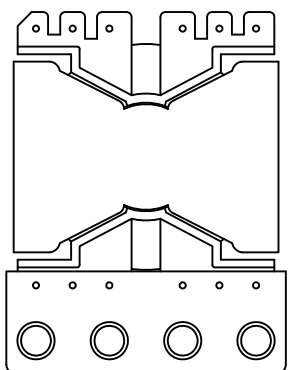
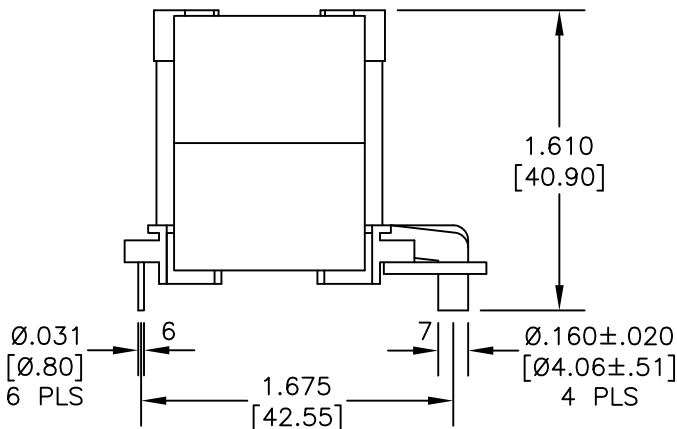
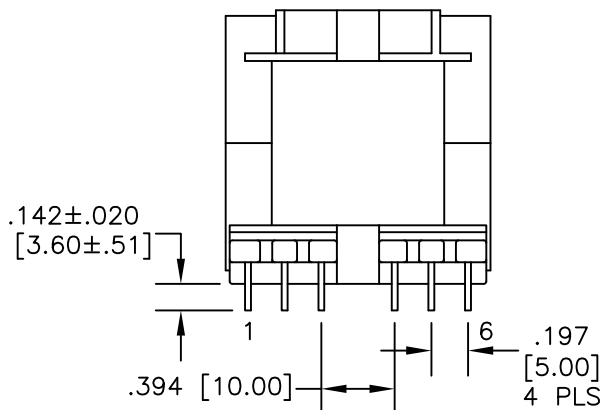


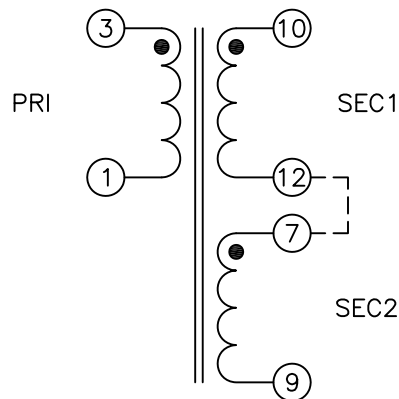
SUGGESTED PCB LAYOUT



PRELIMINARY

TESTING

1. TURNS RATIO (100%): AT 100 KHz, 1 VRMS
(3-1) : (10-12) = 21 : 1 = 21.00 ±2% (20.58-21.42)
(3-1) : (7-9) = 21 : 1 = 21.00 ±2% (20.58-21.42)
2. OCL (100%): AT 100 KHz, 0.100 VRMS
(3-1) = 2.75 ±5% mH (2.61-2.89 mH)
3. LL (QC): AT 100 KHz, 1 VRMS
(3-1) = 15 uH MAX., SHORT (7-9-10-12)
4. DCR (QC):
(3-1) = 0.090 OHMS MAX
(10-12), (7-9) = 0.30 mOHMS MAX
5. HIPOT (100%): 60 Hz, 2 SEC
(1,3) TO (7,9,10,12) : 3100 VRMS
(1,3,7,9,10,12) TO (CORE) : 2000 VRMS



SCHEMATIC

ALL DIMENSIONS GIVEN IN INCHES [MM].
TOLERANCES UNLESS OTHERWISE SPECIFIED.
LINEAR: .XX±.01 [.X±.25] .XXX±.005 [.XX±.13]
ANGULAR: ± 1°

VITEC ELECTRONICS CORP.

PLANT LOCATION: CARLSBAD, CALIFORNIA

PART NO: AF5032A

REV: 01

TESTING / MECHANICAL

SCALE: 1/1

SIZE: A

FILE: AF5032A-7.DWG

PG. 7 OF 7