

1.6-MHz Synchronous Switched-Mode
 With 4-A Integrated N-MOSFETs
 Up to 94% Efficiency
 4.5-V to 17-V Input
 Feedback Regulation Voltage 2.1V
 30-V Input Rating With Adjustable Overvoltage Protection
 Automatic Power Path Selector Between Adapter and Battery

±0.5% Charge Voltage Regulation
 ±4% Charge Current Regulation
 ±4% Input Current Regulation

BC_STAT Status:
 LOW when charge in progress
 Blinking at 0.5Hz when fault occurs
 HIGH when charge is complete or in SLEEP mode

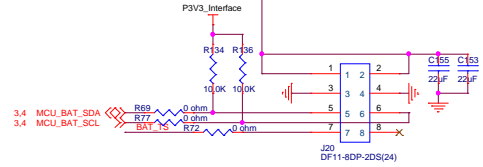
BC_STAT 19-217/GHC-VR1S2/3T D15 240 Ohm R269 P3V3_Interface

Fast charge current set point (ISET)

 $I_{chg} = V_{iset} / (20 \times R_{SR} = 10m\ ohm)$
 $I_{chg} = 1.28A$

Input current set point (ACSET)

 $I_{dpm} = V_{acset} / (20 \times R_{ac} = 10m\ ohm)$
 $I_{dpm} = 3.5A$



 7.6V
 8.7V
 6000mAh (0.2C)
 I2C, 400KHz
 Cell KPL765984-3.8V-6000mAh

TS SNS103B23435FE1Z040EHD15

00	27.9650K Ohm
10	18.1667K Ohm
25	10.0000K Ohm
45	4.88790K Ohm
50	4.13860K Ohm
60	3.00720K Ohm

Feedback Regulation Voltage 2.1V @(VT3 < VTS < VT1)
 Feedback Regulation Voltage 2.05V @(VT4 < VTS < VT3)
 Feedback Regulation Voltage 2.025V @(VT5 < VTS < VT4)

VT1 T1(0°C) threshold, Charge suspended below this temperature.
 VTS rising, As Percentage to VREF 70.2% 70.8% 71.4%

VT2 T2(10°C) threshold, Charge back to ICHARGE / 2 and VFB = 2.1 V
 below this temperature. VTS rising, As Percentage to VREF
 68% 68.6% 69.2%

VT3 T3(45°C) threshold, Charge back to ICHARGE and VFB = 2.05V
 above this temperature. VTS falling, As Percentage to VREF
 55.5% 56.1% 56.7%

VT4 T4(50°C) threshold, Charge back to ICHARGE and VFB = 2.025V
 above this temperature. VTS falling, As Percentage to VREF
 53.2% 53.7% 54.2%

VT5 T5(60°C) threshold, Charge suspended above this temperature
 VTS falling, As Percentage to VREF 47.6% 48.1% 48.6%