



Pin Name	Description	Default
SPI_CLK, SPI_MISO, /A[RT]_YX0	SPINAND_addr select: 3'b110: nRMC mode enable 3'b000: SPI NOR 4B mode 3'b001: SPI NOR 3B mode 3'b010: SPI NAND dummy prepend 3'b011: SPI NAND dummy append	3'b111
GPIO0	Boot from flash 1'b0: disable (Boot from Internal rom) 1'b1: enable	1'b1
SPI_CS, /C[2C]_SCL	Operation mode: Z'b00: Reserved Z'b01: Reserved Z'b10: Reserved Z'b11: Normal mode	2'b11
BGPOR_TP, BGPOR_TN	Cystal select: Z'b00: 25MHz XTAL Z'b01: 40MHz single-end voltage mode Z'b10: 25MHz Differential current mode Z'b11: 40MHz Differential current mode	NA



