

OTP EPROM Write NG

2015. 07. 29

1. Setting Value

Texas Instruments - bq76PL536 Evaluation

File Command Tools Log Plot Help

Global Registers

DEVICE: Device 03 STACK HEIGHT: 3 POLL: ☐ RATE: 9 LOG STACK: ☐ VIEW PLOT: ☐ HW IO CTRL: ☐ ALERT: ☐ LAST_CRC: ☐ STACK: ☐ FAILT: ☐ LOG: ☐ INTERFACE: ☐

V STACK: 36.895

Device Registers

GPAI: 0.001 ADDR: 3 LOG THIS DEVICE: ☐ TS2: 26.9 TS1: 26.5 VIEW TS AS RATIO: ☐ Refresh

V CELL6: 0.0007
V CELL5: 0.0019
V CELL4: 3.7023
V CELL3: 3.7012
V CELL2: 3.6993
V CELL1: 3.7027

STATUS	AR	FAULT	ALERT	-	ECC_COR	UVLO	CBT	DRDY	R
COV_FAULT	-	-	OV[6]	OV[5]	OV[4]	OV[3]	OV[2]	OV[1]	R
CUV_FAULT	-	-	UV[6]	UV[5]	UV[4]	UV[3]	UV[2]	UV[1]	R
ALERT	AR	PARITY	ECC_ERR	FORCE	TS0	SLEEP	OT2	OT1	RW
FAULT	-	-	I_FAULT	FORCE	POR	CRC	CUV	COV	RW
ADC_CTRL	-	ADC_ON	TS2	TS1	GPAI	CS[2]	CS[1]	CS[0]	RW
IO_CTRL	AUX	GPIO_OUT	GPIO_IN	-	-	SLEEP	TS2	TS1	RW
CB_CTRL	-	-	CBAL[6]	CBAL[5]	CBAL[4]	CBAL[3]	CBAL[2]	CBAL[1]	RW
CB_TIME	0	Seconds							
FUNC_CFG	ADCT1	ADCT0	GPAI_REF	GPAI_SRC	ON[1]	ON[0]	-	-	OTP
IO_CFG	-	-	-	-	-	-	-	CRC_DIS	OTP

Protected Registers

CFG_COV: 2.00 Enable
CFG_COVT: Disable us
CFG_CUV: 0.70 Enable
CFG_CUVT: Disable us
CFG_OT: TS2 Disable TS1 Disable °C
CFG_OTT: Disable ms

User Registers

USER 1: 0 USER 3: 0
USER 2: 0 USER 4: 0

Quick Access

REG X	ADDR	DATA	Read	Write
REG Y			Read	Write
REG Z			Read	Write
			Read All	Write All

Aardvark v3.51 | SPI Mode 1 | 1000 KHz

처음 연결한 상태에서 Device 3의 모습입니다. 현재 설정되어 있지 않은 상태입니다.

1. Setting Value

Texas Instruments - bq76PL536 Evaluation

File Command Tools Log Plot Help

Global Registers

DEVICE: Device 03

STACK HEIGHT: 3

V STACK: 51.687

POLL RATE: 9

LOG STACK

VIEW PLOT

HW IO CTRL

AI FRT

LAST CRC

STACK

FAIL T

LOG

INTERFACE

Device Registers

V BRICK: 14.793

V CELL6: 0.0007

V CELL5: 0.0019

V CELL4: 3.7027

V CELL3: 3.7008

V CELL2: 3.6993

V CELL1: 3.7031

ADDR: 3

LOG THIS DEVICE

TS2: 26.9

TS1: 26.7

VIEW TS AS RATIO

Refresh

Protected Registers

CFG_COV: 2.00

CFG_COVT: Disable

CFG_CUV: 0.70

CFG_CUVT: Disable

CFG_OT: TS2 Disable

CFG_OTT: Disable

Enable

us

Enable

us

TS1 Disable

ms

User Registers

USER 1: 0

USER 2: 0

USER 3: 0

USER 4: 0

Quick Access

REG X: ADDR DATA

REG Y: ADDR DATA

REG Z: ADDR DATA

Read Write

Read Write

Read Write

Read All Write All

Device Registers Table

Register	Value	Access
STATUS	AR FAULT ALERT - ECC_COR UVLO CBT DRDY	R
COV_FAULT	- - OV[6] OV[5] OV[4] OV[3] OV[2] OV[1]	R
CUV_FAULT	- - UV[6] UV[5] UV[4] UV[3] UV[2] UV[1]	R
ALERT	AR PARITY ECC_ERR FORCE TSD SLEEP OT2 OT1	RW
FAULT	- - I_FAULT FORCE POR CRC CUV COV	RW
ADC_CTRL	- ADC_ON TS2 TS1 GPAI CS[2] CS[1] CS[0]	RW
IO_CTRL	AUX GPIO_OUT GPIO_IN - - SLEEP TS2 TS1	RW
CB_CTRL	- - CBAL[6] CBAL[5] CBAL[4] CBAL[3] CBAL[2] CBAL[1]	RW
CB_TIME	0 Seconds	
FUNC_CFG	ADCT1 ADCT0 GPAI_REF GPAI_SRC CN[1] CN[0] - -	OTP
IO_CFG	- - - - - - - CRC_DIS	OTP

Aardvark v3.51 | SPI Mode 1 | 1000 kHz

FUNC_CFG 를 설정하는 중입니다

1. Setting Value

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File Command Tools Log Plot Help

Global Registers

DEVICE: Device 03 STACK HEIGHT: 3 POLL: ☐ RATE: 9

V STACK: 51.687

LOG STACK ☐ VIEW PLOT ☐ HW ID CTRL ☐ AI FRT ☐ IAST CRC ☐ STACK ☐ FAIL T ☐ LOG ☐ INTFRACE ☐

Device Registers

V BRICK: 14.793 V CELL6: 0.0007 V CELL5: 0.0019 V CELL4: 3.7027 V CELL3: 3.7008 V CELL2: 3.6993 V CELL1: 3.7031

ADDR: 3 TS2: 26.9 TS1: 26.7

☐ LOG THIS DEVICE ☐ VIEW TS AS RATIO

Refresh

Protected Registers

CFG_COV: 4.70 Enable CFG_COVT: 1000 ms CFG_CUV: 2.80 Enable CFG_CUVT: 100 ms CFG_OT: TS2 70 TS1 70 °C CFG_OTT: 1000 ms

User Registers

USER 1: 0 USER 3: 0 USER 2: 0 USER 4: 0

Quick Access

REG	ADDR	DATA	Read	Write
REG X			Read	Write
REG Y			Read	Write
REG Z			Read	Write
			Read All	Write All

Aardvark v3.51 | SPI Mode 1 | 1000 KHz

Protected Registers 설정한 상태 입니다

1. Setting Value

Texas Instruments - bq76PL536 Evaluation

File Command Tools Log Plot Help

Global Registers

DEVICE: Device 03 STACK HEIGHT: 3 POLL: LOG STACK: AI FRT: I ASST CRC: STACK: V STACK: 51.687 RATE: 9 VIEW PLOT: FAIL T: LOG: INTFACE:

Device Registers

V BRICK: 14.793 ADDR: 3
V CELL6: 0.0007
V CELL5: 0.0019
V CELL4: 3.7027
V CELL3: 3.7008
V CELL2: 3.6993
V CELL1: 3.7031

TS2: 26.8
TS1:

Protected Registers

CFG_COV: 4.70 Enable
CFG_COVT: 1000 ms
G_CUV: 2.80 Enable
G_CUVT: 100 ms
FG_OT: 70 °C
G_OTT: 1000 ms

User Registers

USER 1: 0 USER 3: 0
USER 2: 0 USER 4: 0

Quick Access

REG X: ADDR: DATA: Read Write
REG Y: Read Write
REG Z: Read Write
Read All Write All

Program OTP Registers

☐ Program All Devices on Stack
☒ Program This Device

Program Cancel

Aardvark v3.51 | SPI Mode 1 | 1000 KHz

Program OTP Registers 를 실행하고 전원을 인가하여 Program 을 진행합니다.

1. Setting Value

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File Command Tools Log Plot Help

Global Registers

DEVICE: Device 03 STACK HEIGHT: 3 POLL: ☐ RATE: 9 V STACK: 36.897

LOG STACK ☐ VIEW PLOT ☐ HW IO CTRL ☐ AI FRT ☐ IAST CRC ☐ STACK ☐ FAULT ☒ LOG ☐ INTERFACE ☐

Device Registers

GPAI: 0.001 ADDR: 3 ☐ LOG THIS DEVICE
 V CELL6: 0.0007 TS2: 26.7
 V CELL5: 0.0019 TS1: 26.5 ☐ VIEW TS AS RATIO
 V CELL4: 3.7027
 V CELL3: 3.7012
 V CELL2: 3.6997
 V CELL1: 3.7031

Refresh

Protected Registers

CFG_COV: 2.00 Enable
 CFG_COVT: Disable us
 CFG_CUV: 0.70 Enable
 CFG_CUVT: Disable us
 CFG_OT: TS2: Disable TS1: Disable °C
 CFG_OTT: Disable ms

User Registers

USER 1: 0 USER 3: 0
 USER 2: 0 USER 4: 0

Quick Access

REG X: ADDR: DATA: Read Write
 REG Y: Read Write
 REG Z: Read Write
 Read All Write All

Device Registers Table

STATUS	AR	FAULT	ALERT	-	ECC_COR	UVLO	CBT	DRDY	R
COV_FAULT	-	-	OV[6]	OV[5]	OV[4]	OV[3]	OV[2]	OV[1]	R
CUV_FAULT	-	-	UV[6]	UV[5]	UV[4]	UV[3]	UV[2]	UV[1]	R
ALERT	AR	PARITY	ECC_ERR	FORCE	TS2	SLEEP	OT2	OT1	RW
FAULT	-	-	I_FAULT	FORCE	POR	CRC	CUV	CDV	RW
ADC_CTRL	-	ADC_ON	TS2	TS1	GPAI	CS[2]	CS[1]	CS[0]	RW
IO_CTRL	AUX	GP_ID_OUT	GP_ID_IN	-	-	SLEEP	TS2	TS1	RW
CB_CTRL	-	-	CBAL[6]	CBAL[5]	CBAL[4]	CBAL[3]	CBAL[2]	CBAL[1]	RW
CB_TTIME	0	-	-	-	-	-	-	-	-
FUNC_CFG	ADCT1	ADCT0	GPAI_REF	GPAI_SRC	CN[1]	CN[0]	-	-	OTP
IO_CFG	-	-	-	-	-	-	-	CRC_DIS	OTP

Aardvark v5.51 SPI Mode 1 1000 KHz

Program을 완료한 상태에서 Reload 한 상태입니다. 값이 설정 되지 않은 것을 확인 할 수 있습니다.
 다음과 같은 문제에 대해 해결방법을 요청합니다.