

SYSTEM INPUT SPECIFICATIONS:			
Maximum bulk voltage at light load, high line	$V_{BULK(max)}$	425	Vdc
Minimum bulk voltage at full load, low line	$V_{BULK(min)}$	110	Vdc
SYSTEM OUTPUT SPECIFICATIONS:			
Highest output voltage at which SR must operate	$V_{OUT(max)}$	13,5	Vdc
Regulated output voltage in steady-state	$V_{OUT(nom)}$	13,44	Vdc
Lowest output voltage at which SR must operate	$V_{OUT(min)}$	13,2	Vdc

USER-SELECTED SYSTEM PARAMETERS			
Switching frequency at full load, low line	f_{SW}	65	kHz
Maximum primary-side duty cycle	D_{MAX}	0,52	
Minimum primary-side ON time	$t_{ON(min)}$	950	ns
TRANSFORMER			
Primary to Secondary Turns Ratio	N_{PS}	6,9	
Primary Magnetizing Inductance	L_{PRI}	420	μH
Total primary-side switched-node capacitance at $V_{BULK(min)}$ (includes MOSFET C_{OSS} , winding capacitance, and reflected SR-MOSFET C_{OSS})	C_{SWN}	200	pF
SR MOSFET (Q_{SR})			
Total SR MOSFET gate charge at $V_{gs} = V_{DD}$, (up to 13 V max)	Q_G	30	nC

