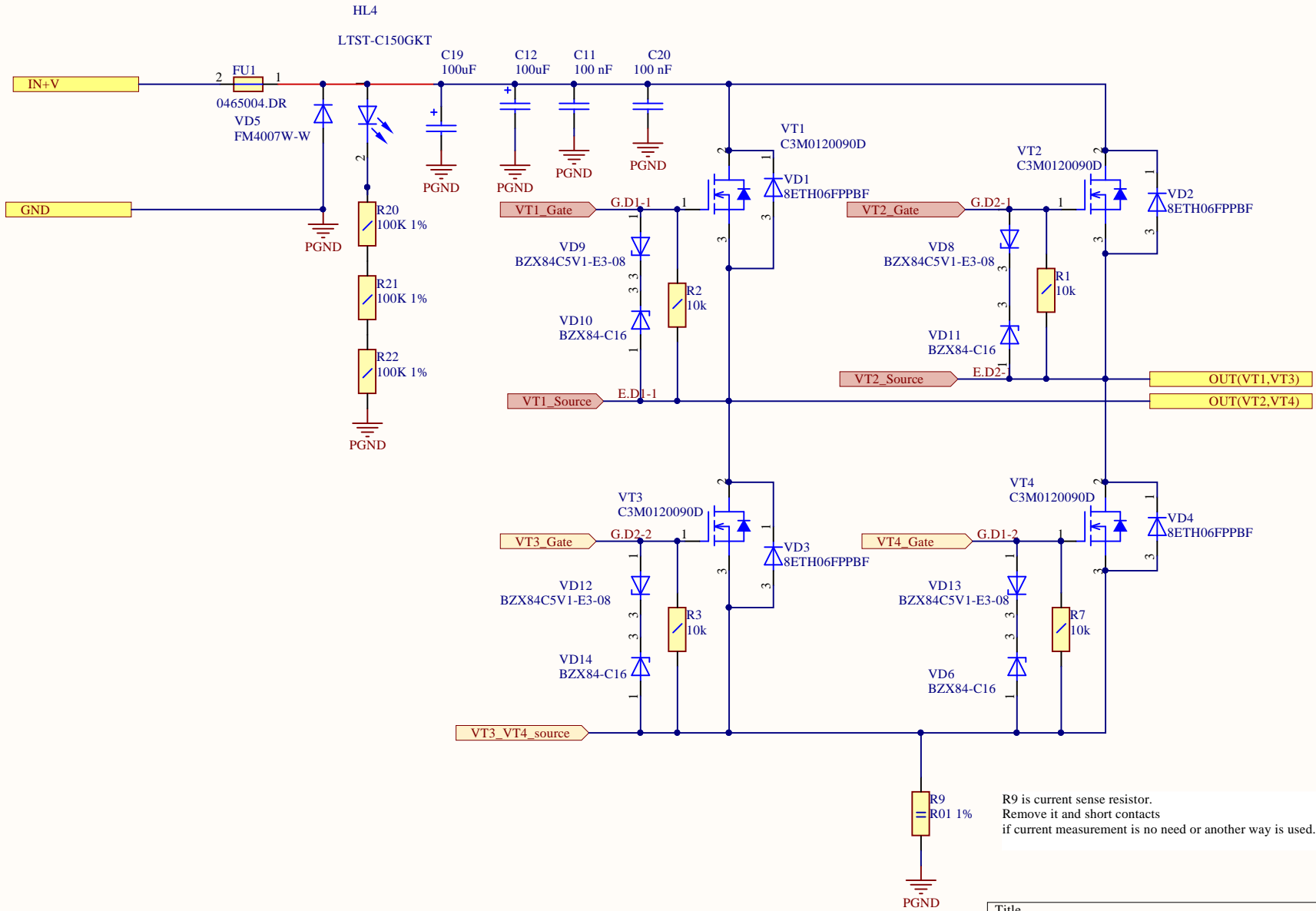


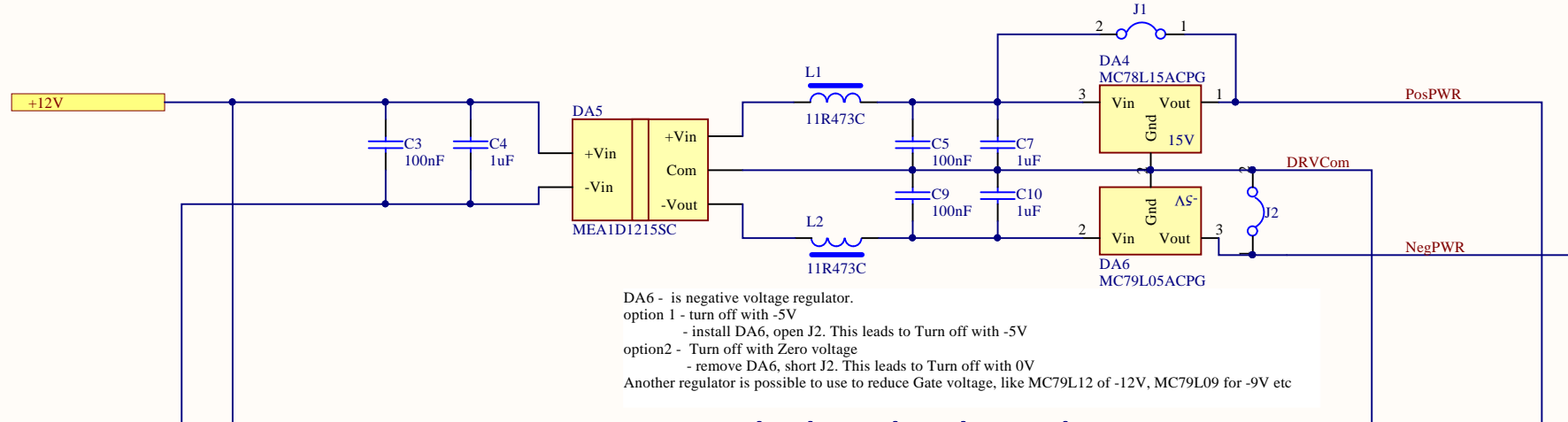
| | | |
|-------|----------------------------------|-----------|
| Title | | |
| Size | Number | Revision |
| A4 | | |
| Date: | 20.04.2019 | Sheet of |
| File: | D:\DOCs\...\BridgeV3_main.SchDoc | Drawn By: |



R9 is current sense resistor.
Remove it and short contacts
if current measurement is no need or another way is used.

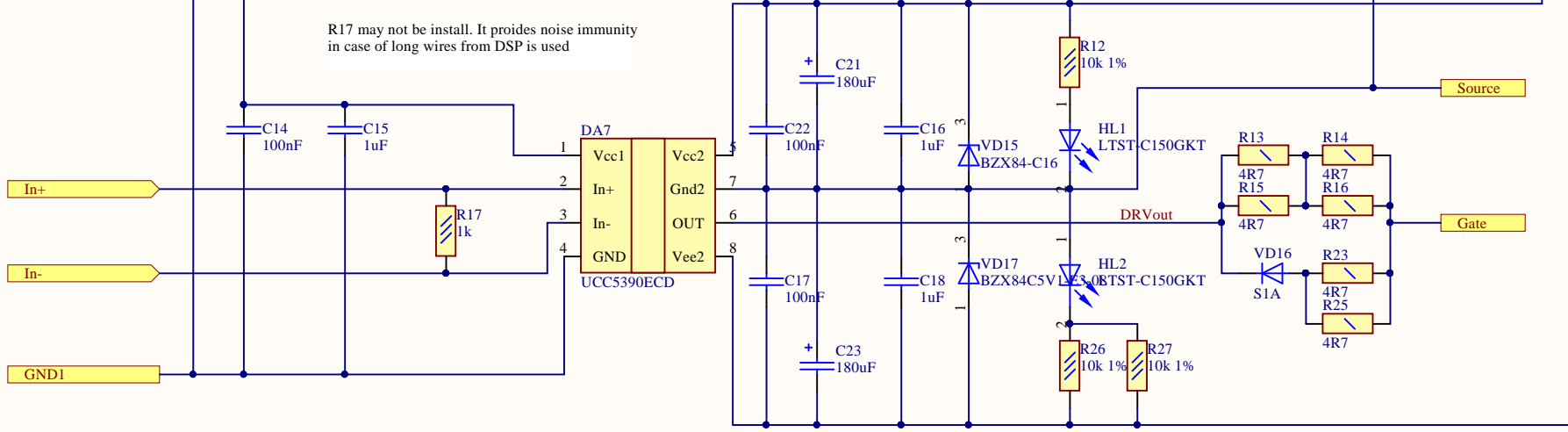
| | | |
|-------|-----------------------------------|-----------|
| Title | | |
| Size | Number | Revision |
| A4 | | |
| Date: | 20.04.2019 | Sheet of |
| File: | D:\DOCs\...\BridgeV3_power.SchDoc | Drawn By: |

DA4 - is positive voltage regulator.
 option 1 - without regulator:
 - remove DA4, short J1
 option 2 - with regulator
 - install DA4, open J1
 Another regulator is possible to use to reduce Gate voltage, like MC78L12 of 12V, MC78L09 for 9V



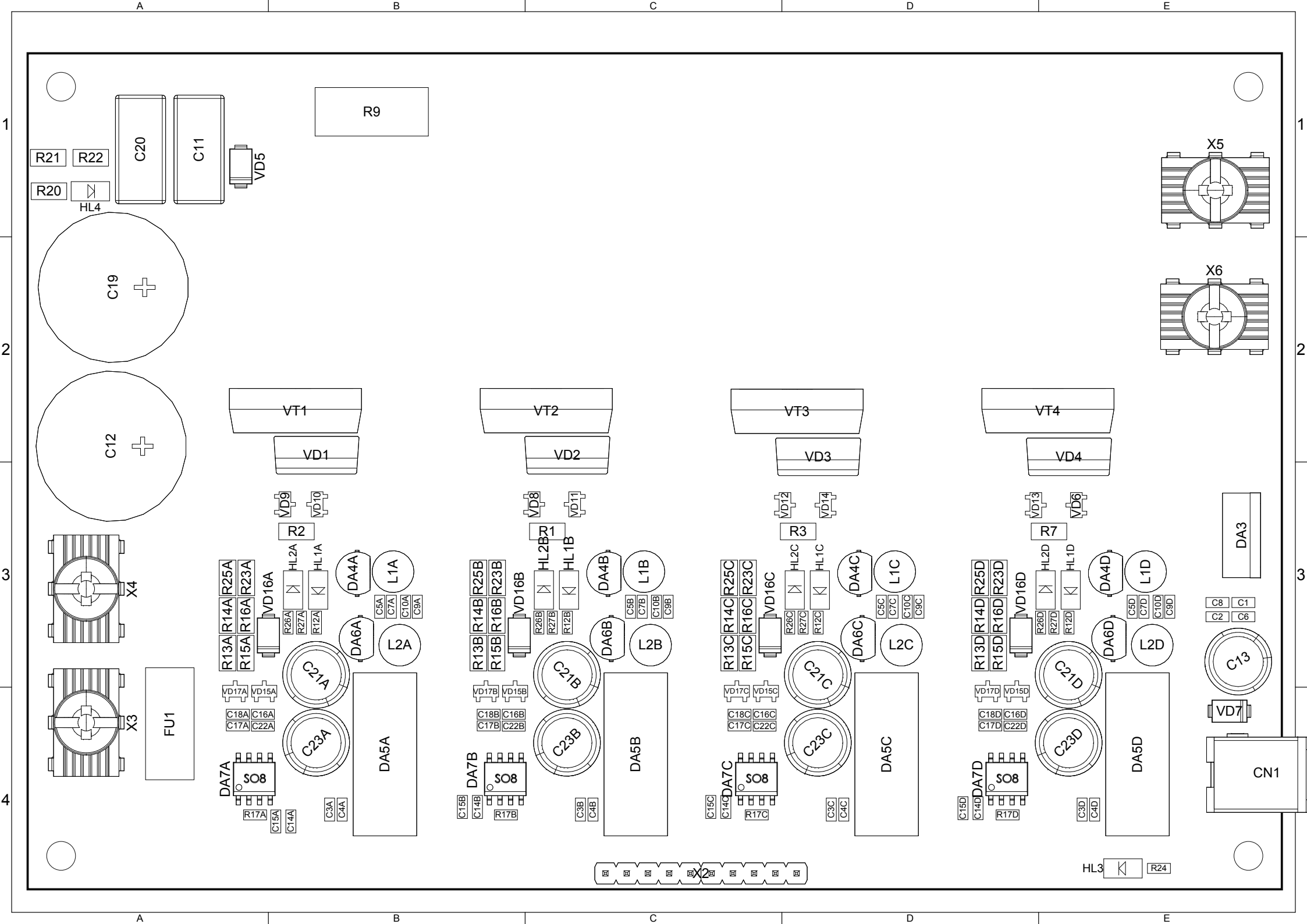
DA6 - is negative voltage regulator.
 option 1 - turn off with -5V
 - install DA6, open J2. This leads to Turn off with -5V
 option2 - Turn off with Zero voltage
 - remove DA6, short J2. This leads to Turn off with 0V
 Another regulator is possible to use to reduce Gate voltage, like MC79L12 of -12V, MC79L09 for -9V etc

R17 may not be install. It provides noise immunity in case of long wires from DSP is used



C21, C23 - Those cap provide high current for driver, so should be LOW ESR like 0.016 - 0.025 Ohm Capacitance, better to have above 150 uF.

| | | |
|-------|-------------------------------|-----------|
| Title | | |
| Size | Number | Revision |
| A4 | | |
| Date: | 20.04.2019 | Sheet of |
| File: | D:\DOCs\...\GateDriver.SchDoc | Drawn By: |



Bridge Test Board v3

Diagonal #1 - UT1, UT3
Diagonal #2 - UT2, UT4

VT1 VT2 VT3 VT4

