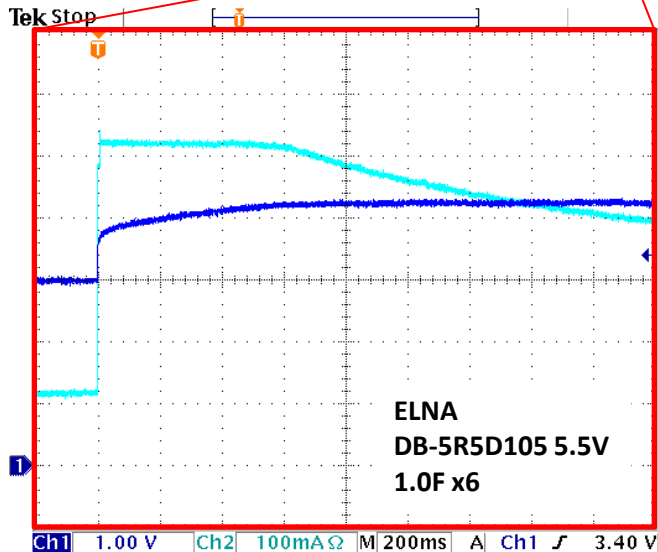
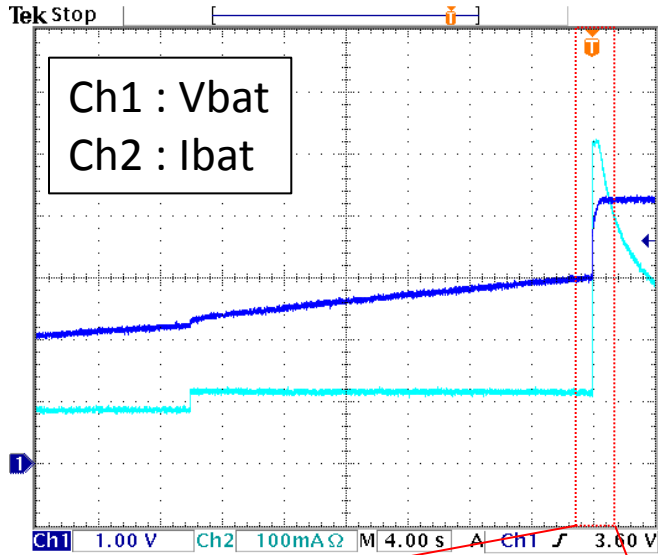


Charging profile with electric double-layer cap



Texas Instruments - BQ2419x EVM - GUI v1.5.0.1

File Help

Read Write Auto Read: OFF Write On Change: ON I2C Address: 6B I2C Activity: R I=6B A=0A D=23 C=371

Input Voltage Limit: 4.36 V
Input Current Limit: 1.2 A
Minimum System Voltage Limit: 3.50 V
USB OTG: 1.3 A
ICHG: 512 mA
Reduce ICHG by 80%:
Pre-Charge Current Limit: 128 mA
Termination current Limit: 256 mA
Charge Voltage Limit: 4.208 V
BATLOWV: 3.0 V
Battery Recharge Threshold: 100 mV
Termination Indicator Threshold: Match Term
Fast Charge Timer: 8 hrs
I2C Watchdog Timer Limit: Disabled
IR Compensation Resistor: 0 mOhm
IR Compensation Voltage Clamp: 0mV
Thermal Regulation Threshold: 120 C
Fast Charge Current At Low Temp: 50%
Charge Voltage at High Temp: 4.05V

Charge Battery Configuration

Enable HIZ
 Reset Registers
 Enable Termination
 Enable Safety Timer
 Start D+/D- detection
 Enable 2X extended safety timer
 Turn Off Q4
 INT on CHRGM_FAULT
 INT on BAT_FAULT

WatchDog Timer

Reset periodic resets: OFF

STATUS

VBUS: Unknown
CHRG: Not Charging
Input DPM: Not DPM
PGOOD: Power Good
THERM: Normal
VSYS: In Regulation

FAULT

WATCHDOG: Normal
OTG: Normal
CHRG: Normal
BAT: Normal
NTC: Normal

PART

Device ID: 100
JEITA: Disabled

Reg	A	7	6	5	4	3	2	1	0	D	W	R
IN SRC	00	0	0	1	1	0	1	0	0	34	W	R
PWR-ON CFG	01	0	0	0	1	1	0	1	1	1B	W	R
CHRG C	02	0	0	0	0	0	0	0	0	00	W	R
P-CHRG/TRM C	03	0	0	0	0	0	0	0	1	01	W	R
CHRG V	04	1	0	1	1	0	0	1	0	B2	W	R
CHRG TRM/TMR	05	1	0	0	1	0	1	0	1	8A	W	R
IR CMP/T REG	06	0	0	0	0	0	0	1	1	03	W	R
MISC OP	07	0	1	0	0	1	0	1	1	4B	W	R
SYS STATUS	08	0	0	0	0	1	0	1	1	05	W	R
FAULT	09	0	0	0	0	0	0	0	0	00	W	R
V/P#/REV	0A	0	0	1	0	0	0	1	1	28	W	R

USB Bridge Connected (ver. 49.2.2)

Texas Instruments

- Electric double-layer capacitor was used instead of real battery.
- The waveform was measured with 6[F] of double-layer capacitor.
- Bq24190EVM was used with following setting.
 - IPRECHG=128mA
 - ICHG=512mA