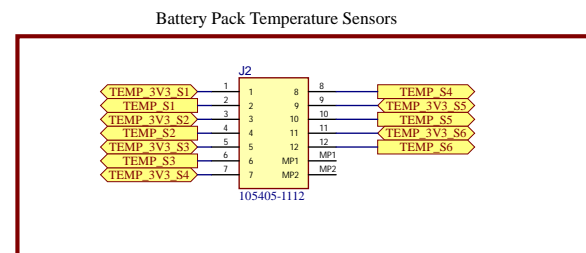
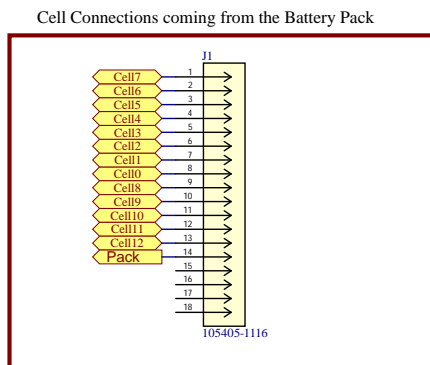
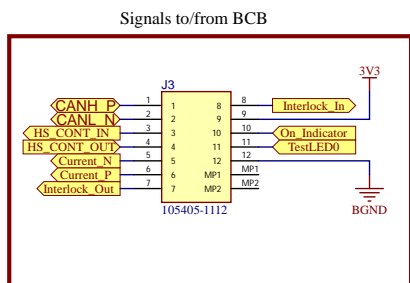
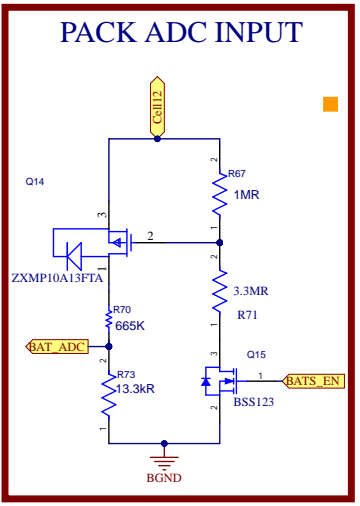
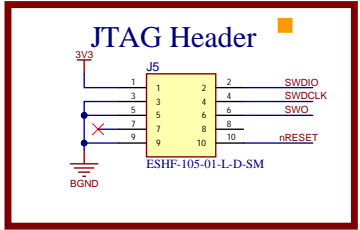
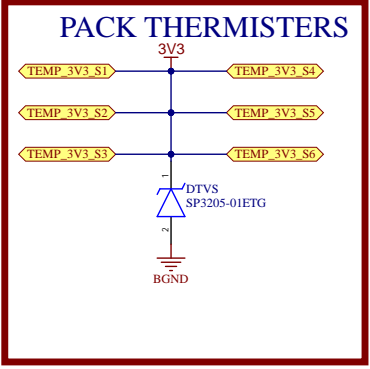
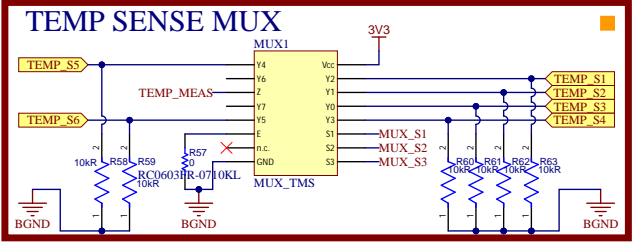
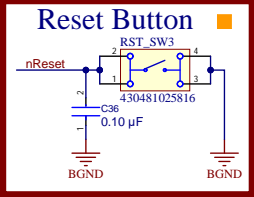
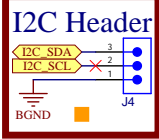
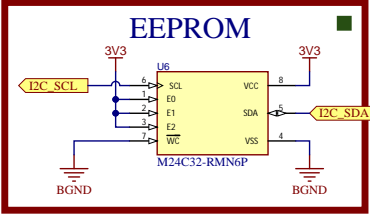
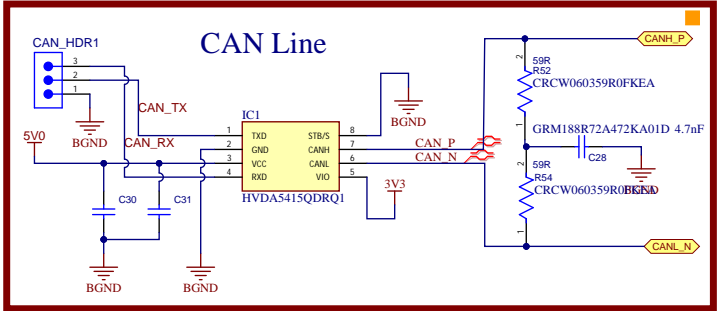
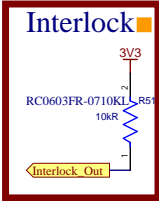
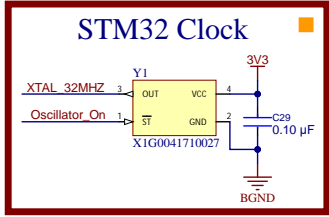
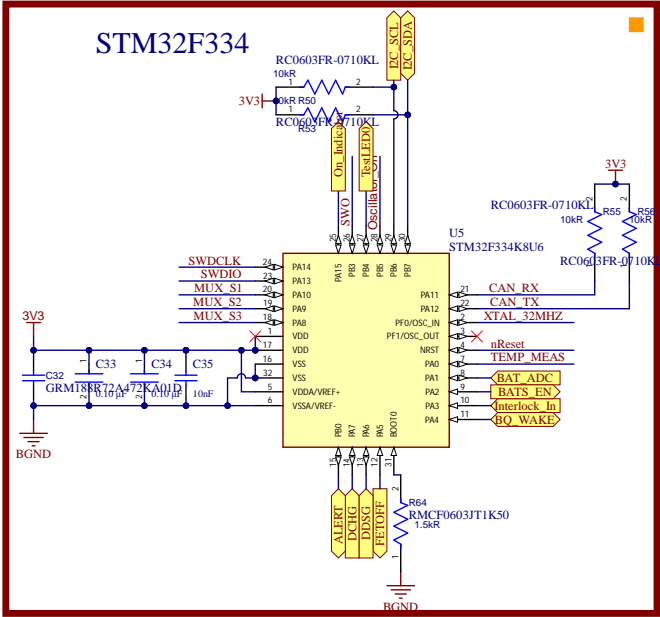


35 wires going to and from the BMS through connectors

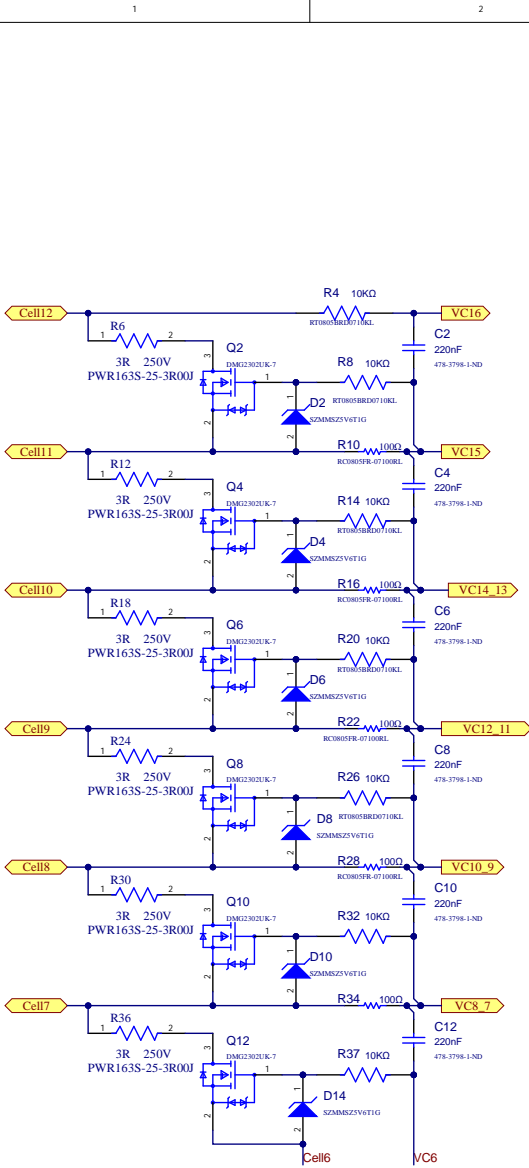


Con Desc	Part Number
Nano Fit HDR 12 Pin	105405-1112
Nano Fit Recp 12 Pin	105308-1212
Other Part	Ordered
Nano Fit HDR 16 Pin	105405-1116
Nano Fit Recp 16 Pin	105308-1216
Other Part	Ordered

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DRAWN BY: *	DATE: *	MATERIAL: N/A	
PROJECT LEAD APPROVAL: *	DATE: *	FINISH: N/A	
OFFICER APPROVAL: *	DATE: *	TITLE: connectors.SchDoc	
CAD SOFTWARE: Altium Designer			
UNLESS OTHERWISE SPECIFIED		DWG SIZE	DRAWING NO.
TOLERANCES: .XX = +/- .02 .XXX = +/- .005 ANGLES +/- 2 DEG		B	*
SCALE		Sheet	REV
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PRINTED OR GENERATED ON:		4/9/2022	3:21:03 PM



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CAD SOFTWARE: Altium Designer			
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TOLERANCES: -XX = +/- .02 -XXX = +/- .005 ANGLES +/- 2 DEG	DIMENSIONS ARE IN INCHES	B	*
SCALE		Sheet	2 of 5
PRINTED OR GENERATED ON: 4/9/2022 3:21:03 PM			



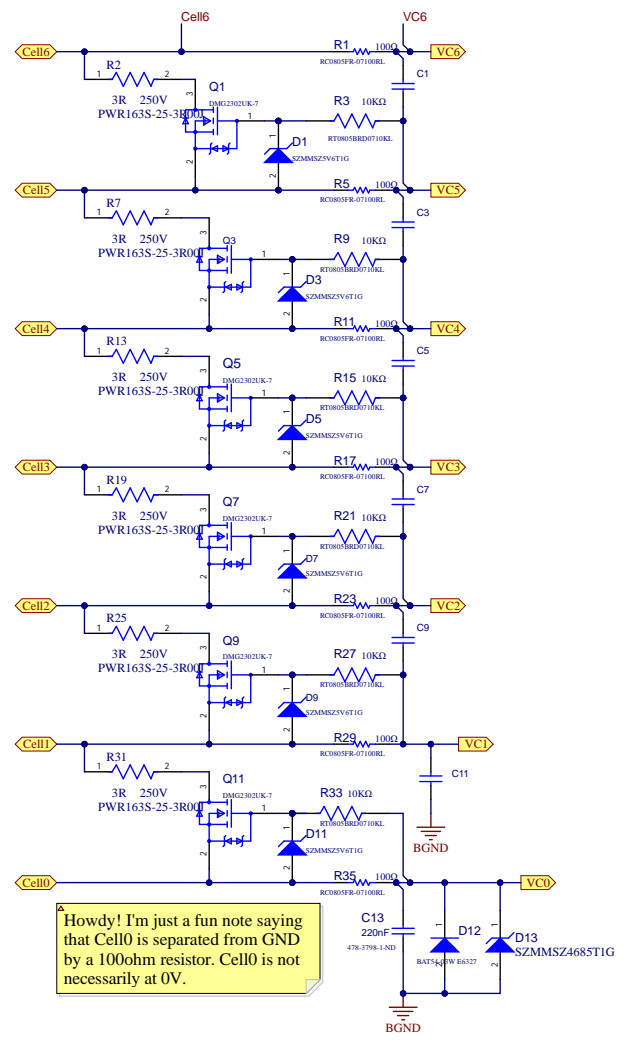
VC14_13 captures VC14 and VC13. It retains VC14 designators

VC12_11 captures VC12 and VC11. It retains VC12 designators

VC10_9 captures VC10 and VC9. It retains VC10 designators

VC8_7 captures VC8 and VC7. It retains VC8 designators

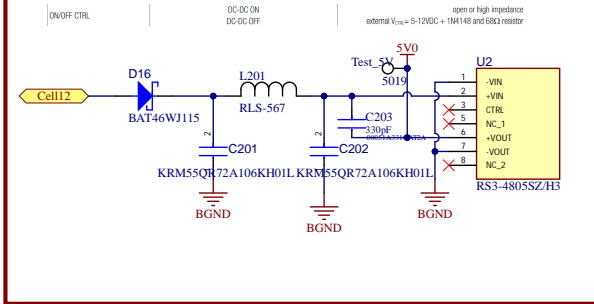
Short is on bq chip sheet



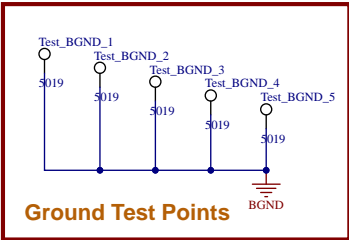
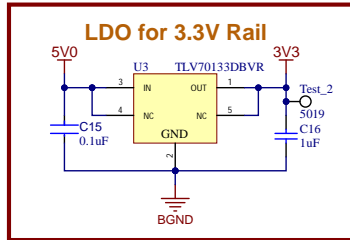
Howdy! I'm just a fun note saying that Cell0 is separated from GND by a 100ohm resistor. Cell0 is not necessarily at 0V.

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OFFICER APPROVAL	DATE	TITLE	N/A
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UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		SCALE	Sheet 3 of 5
PRINTED OR GENERATED ON: 4/9/2022 3:21:04 PM			

5V DC-DC Converter



LDO for 3.3V Rail



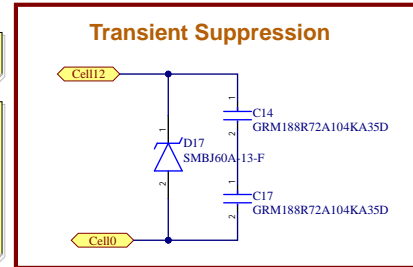
Ground Test Points

5V is from DC/DC Converter

Wide Input Voltage Range of 2.5V to 24V great for low-power microcontrollers such as MSP430

Input and output capacitors should be placed as close to the device pins as possible. To avoid interference of noise and ripple on the board, it is recommended that the board be designed with separate ground planes for VIN and VOUT, with the ground plane connected only at the device GND pin. In addition, the ground connection for the output capacitor should be connected directly to the device GND pin.

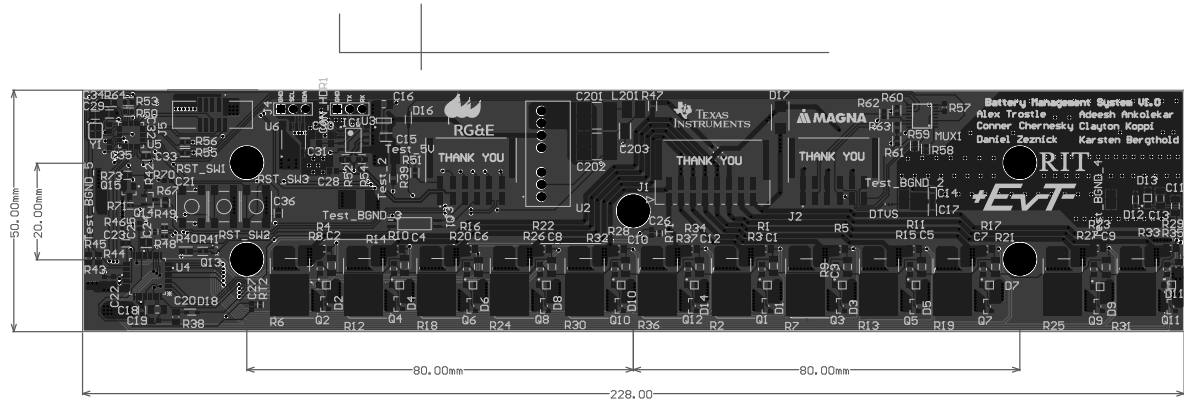
Transient Suppression



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PROJECT LEAD APPROVAL:	DATE:	FINISH:
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OFFICER APPROVAL:	DATE:	TITLE:
*	*	Aux Power.SchDoc
CAD SOFTWARE: Altium Designer		
UNLESS OTHERWISE SPECIFIED		DWG SIZE
TOLERANCES:	DIMENSIONS ARE IN INCHES	DRAWING NO.
.XX = +/- .02		*
.XXX = +/- .005		
ANGLES +/- 2 DEG	SCALE	Sheet 4 of 5
PRINTED OR GENERATED ON: 4/9/2022 3:21:04 PM		REV: X1



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OFFICER APPROVAL: *	DATE: *	TITLE:		
CAD SOFTWARE: Altium Designer		DWG RPT: B	DRAWING NO.: N/A	REV: *
UNLESS OTHERWISE SPECIFIED TOLERANCES: XX = ±.02 DIMENSIONS XXX = ±.005 ARE IN INCHES ANGLES ±2			SCALE: NONE	SHEET 1 OF 2

