



EN and PWM connected to VDD_3V3 via 10k

VDD_3V3_BUF
PWM_DIM_CTRL

VDD_3V3
R27 10k

VDD_3V3_BUF
R22 0E,DNP
C23 2.2uF,50V
PWM_DIM_CTRL

180mA max/string
R33 17.4k

400 kHz
152 Hz: 16-bit resolution
3 channels
R28 3.92K
R29 3.92K
R30 7.87k
R32 7.87K
Direct PWM and 0x2B I2C ADDRESS

R17 715K
Limit to 34V

OUT1
OUT2
OUT3