

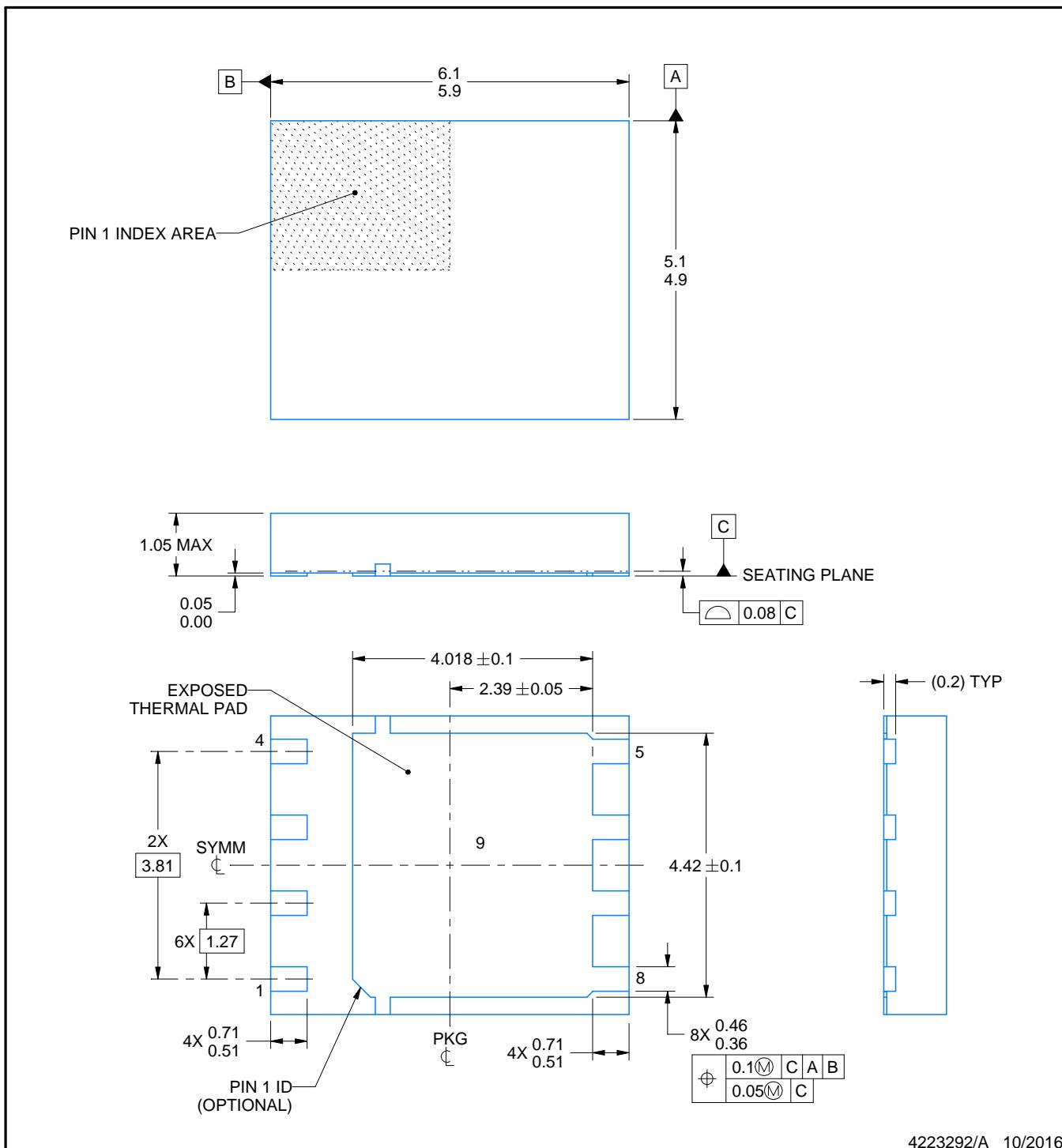
# PACKAGE OUTLINE

DQH0008A



VSON-CLIP - 1.05 mm max height

PLASTIC SMALL OUTLINE - NO LEAD



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## NOTES:

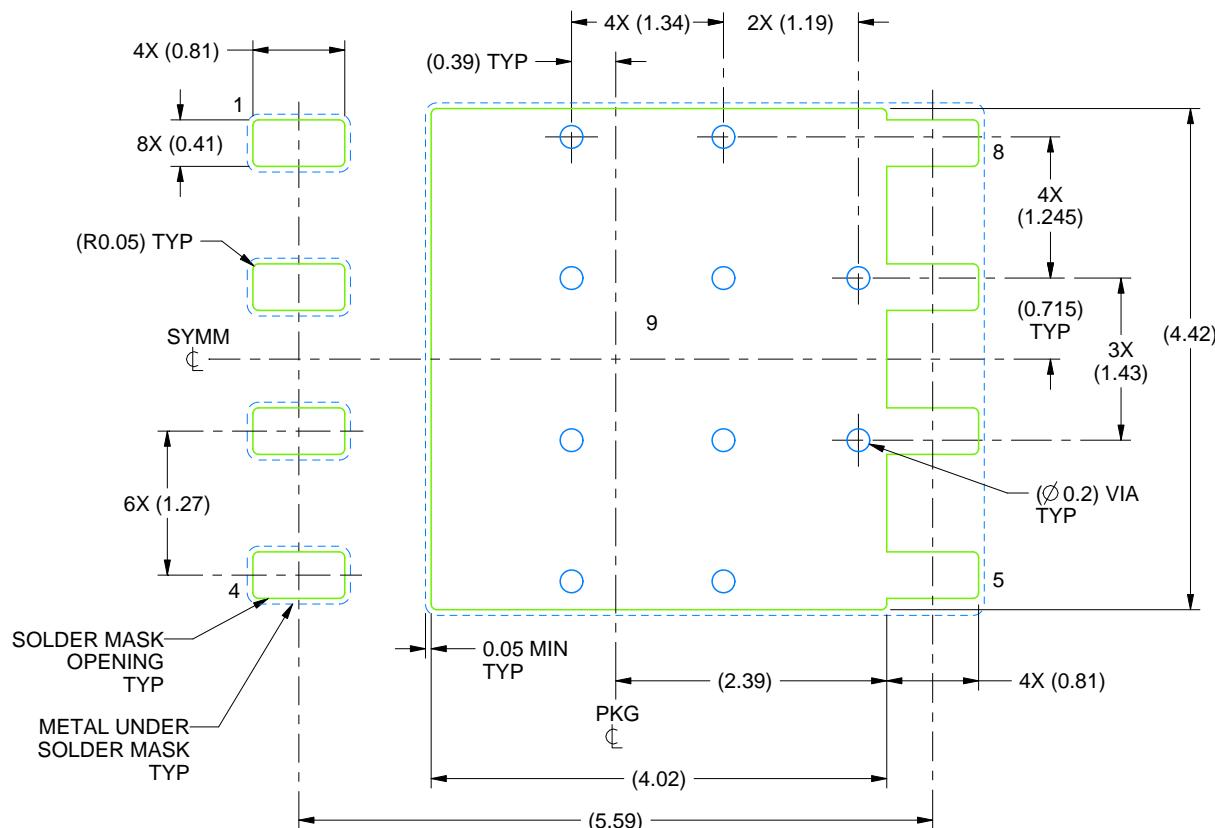
1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. The package thermal pad must be soldered to the printed circuit board for thermal and mechanical performance.

## EXAMPLE BOARD LAYOUT

**DQH0008A**

## **VSON-CLIP - 1.05 mm max height**

## PLASTIC SMALL OUTLINE - NO LEAD



**LAND PATTERN EXAMPLE  
SOLDER MASK DEFINED  
SCALE:15X**

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**NOTES: (continued)**

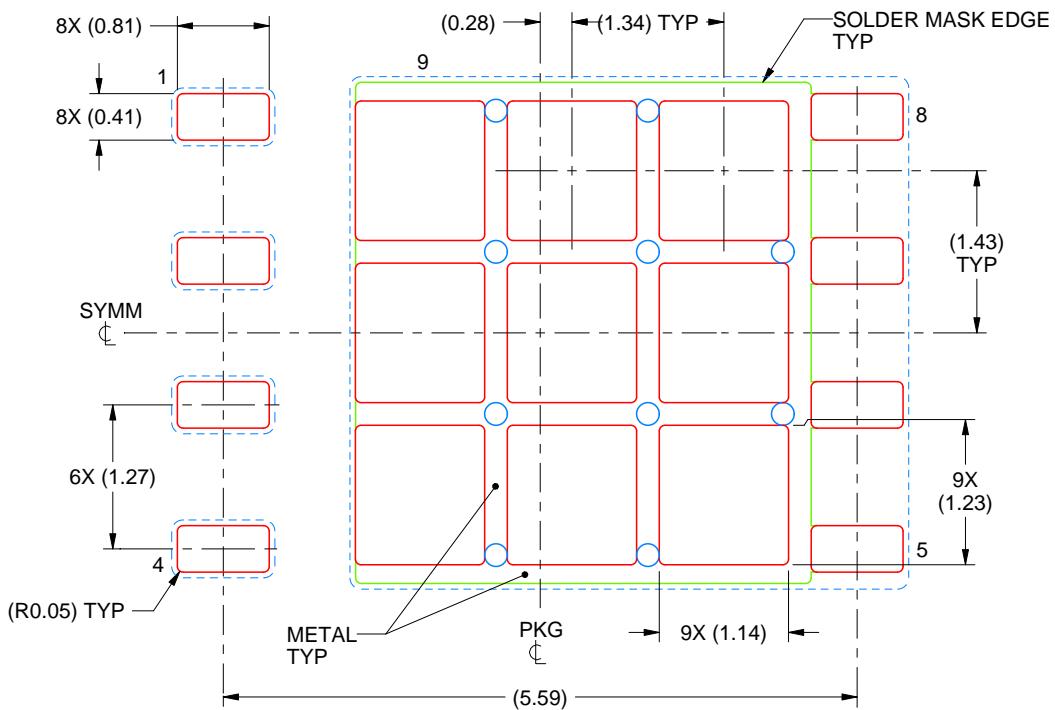
4. This package is designed to be soldered to a thermal pad on the board. For more information, see Texas Instruments literature number SLUA271 ([www.ti.com/lit/slua271](http://www.ti.com/lit/slua271)).
5. Vias are optional depending on application, refer to device data sheet. If some or all are implemented, recommended via locations are shown.

# EXAMPLE STENCIL DESIGN

DQH0008A

VSON-CLIP - 1.05 mm max height

PLASTIC SMALL OUTLINE - NO LEAD



EXPOSED PAD 9:  
71% PRINTED SOLDER COVERAGE BY AREA UNDER PACKAGE  
SCALE:15X

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NOTES: (continued)

6. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.

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