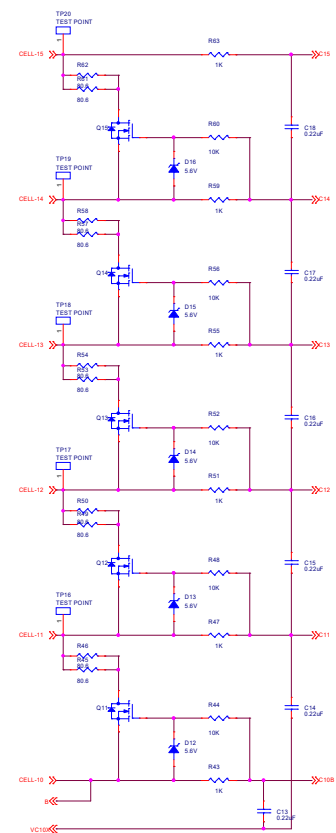
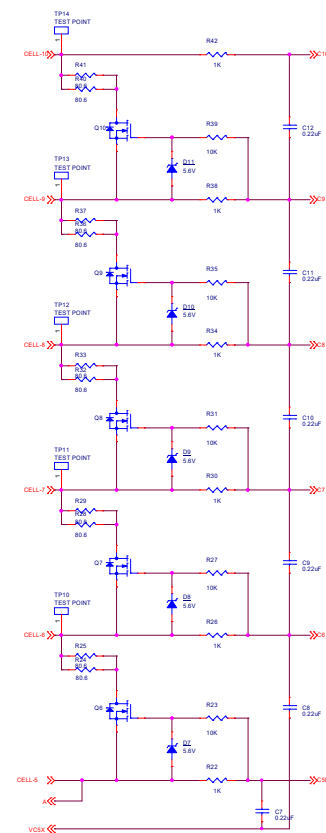
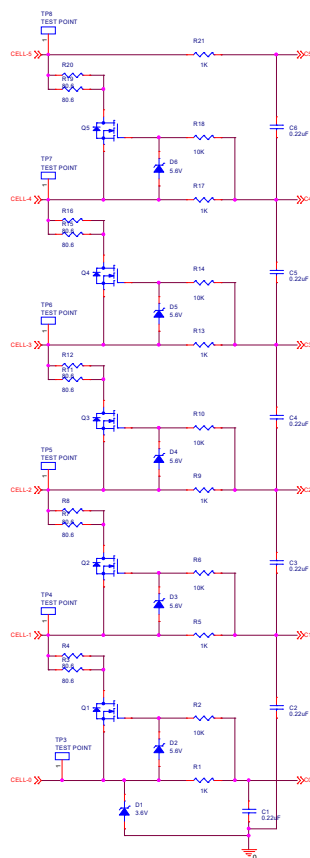
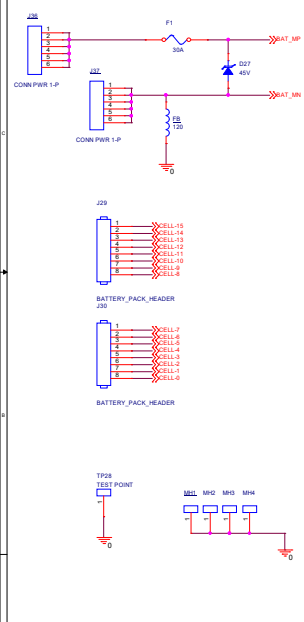
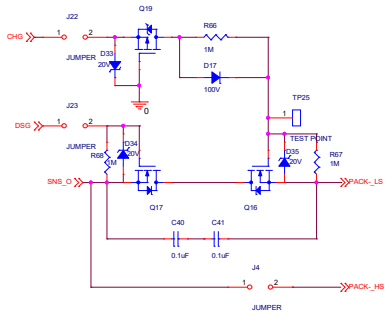


CELLS TO ANALOG FRONT END

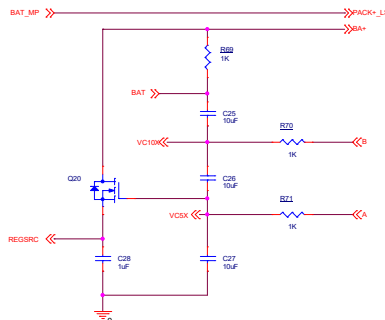


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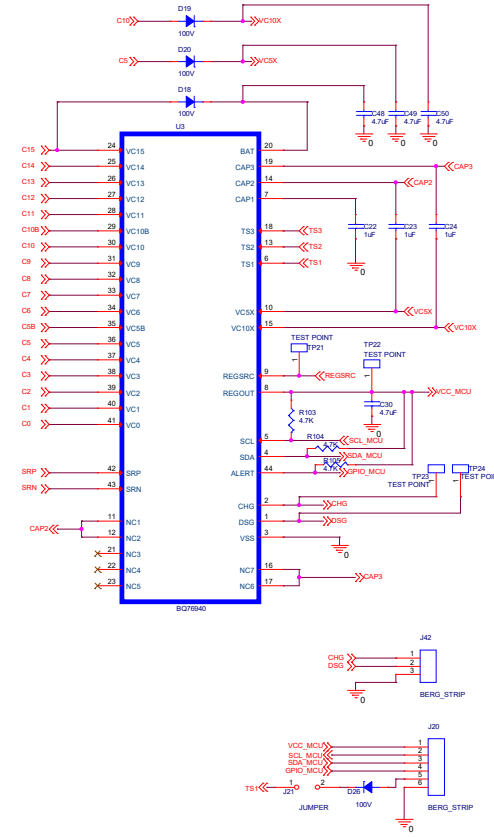
LOW SIDE CONFIGURATION



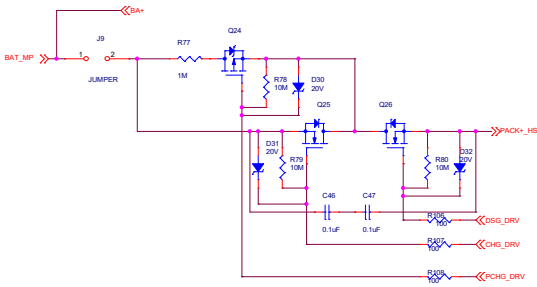
REGULATOR SOURCE



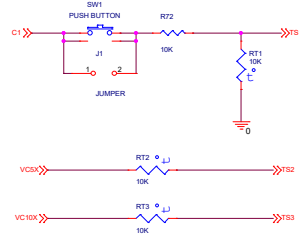
ANALOG FRONT END



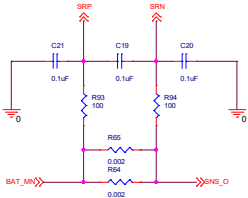
HIGH SIDE CONFIGURATION



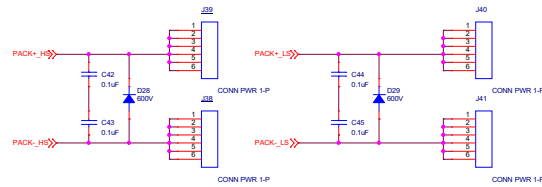
THERMISTORS



CURRENT SENSE

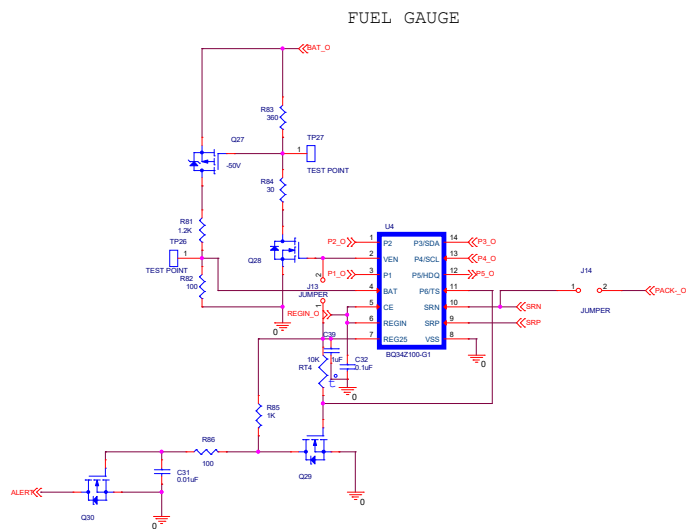
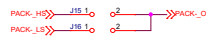


PACK+ // PACK-

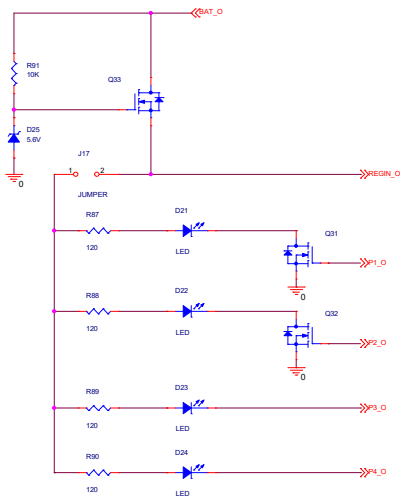


Note: PACK+ and PACK- tracks from CELL 0 and CELL 15 respectively should be capable of withstanding current flow upto 30A

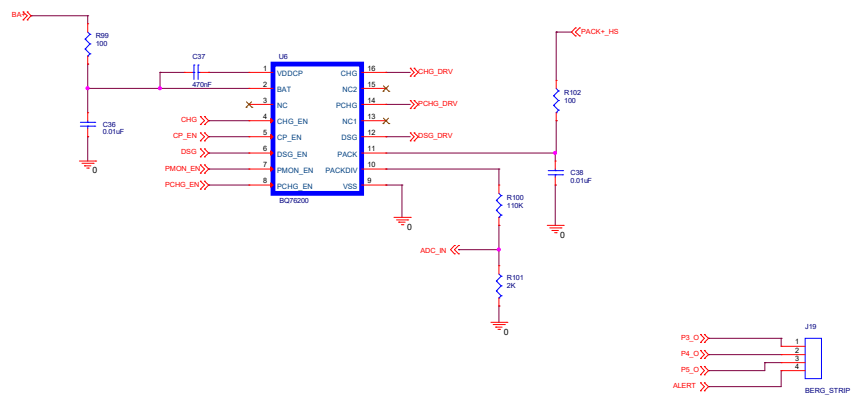
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LED INDICATORS



HIGH SIDE DRIVER



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