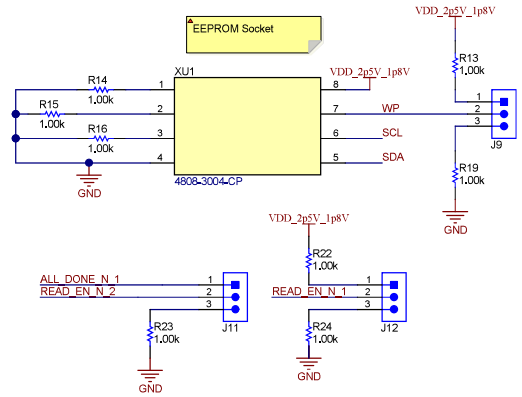
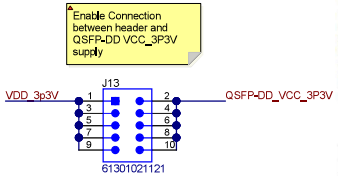
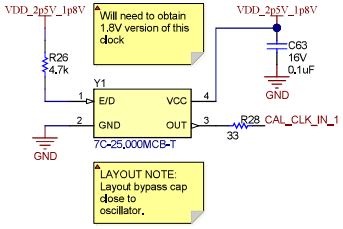
CLOCK AND POWER







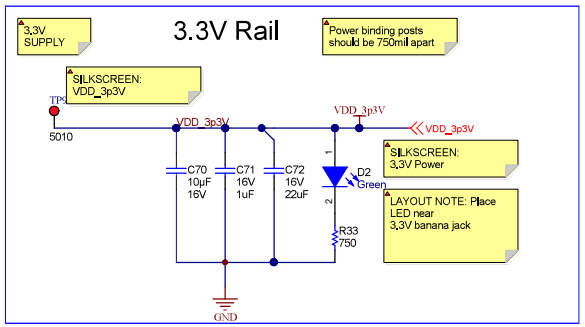
8.1.1.2 ANY-OUT Programmable Output Voltage

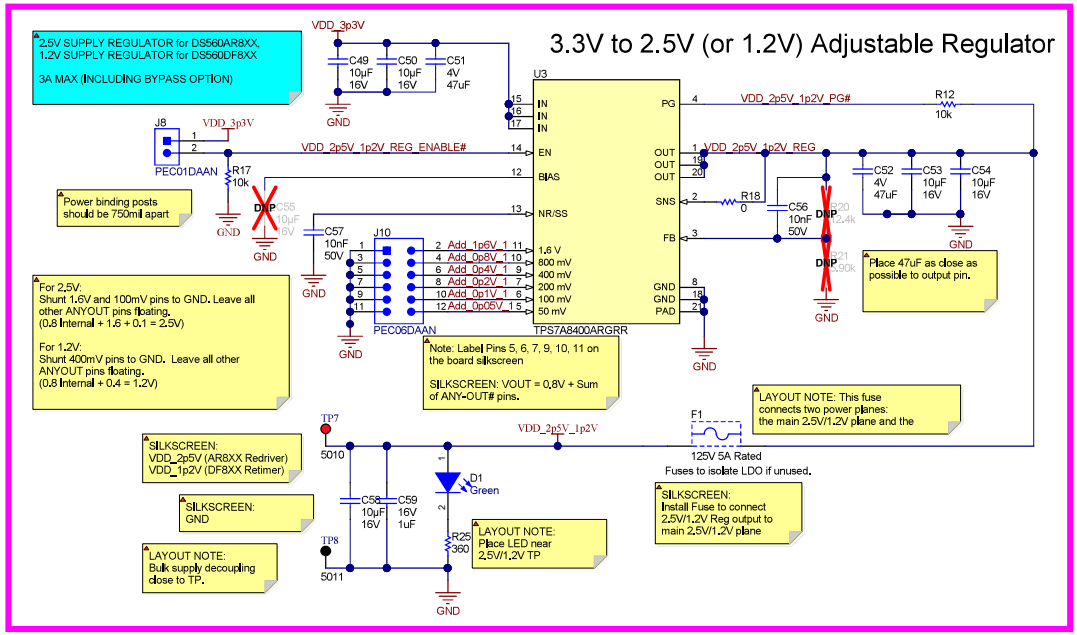
The TPS7A85A can use external resistors or the internally-matched ANY-OUT feedback resistor network to set output voltage. The ANY-OUT resistors are accessible through pin 2 and pins 5 to 11 and program the regulated output voltage. Each pin can be connected to ground (active), left open (floating), or connected to SNS. ANY-OUT programming is set by as the sum of the internal reference voltage ($V_{NR/SS}$ = 0.8 V) plus the accumulated sum of the respective voltages assigned to each active pin; that is, 50mV (pin 5), 100mV (pin 6), 200mV (pin 7), 400mV (pin 9), 800mV (pin 10), or 1.6V (pin 11). Table 5 lists the voltage values associated with each active pin setting for reference. By leaving all program pins open or floating, the output is programmed to the minimum possible output voltage equal to V_{FB}.

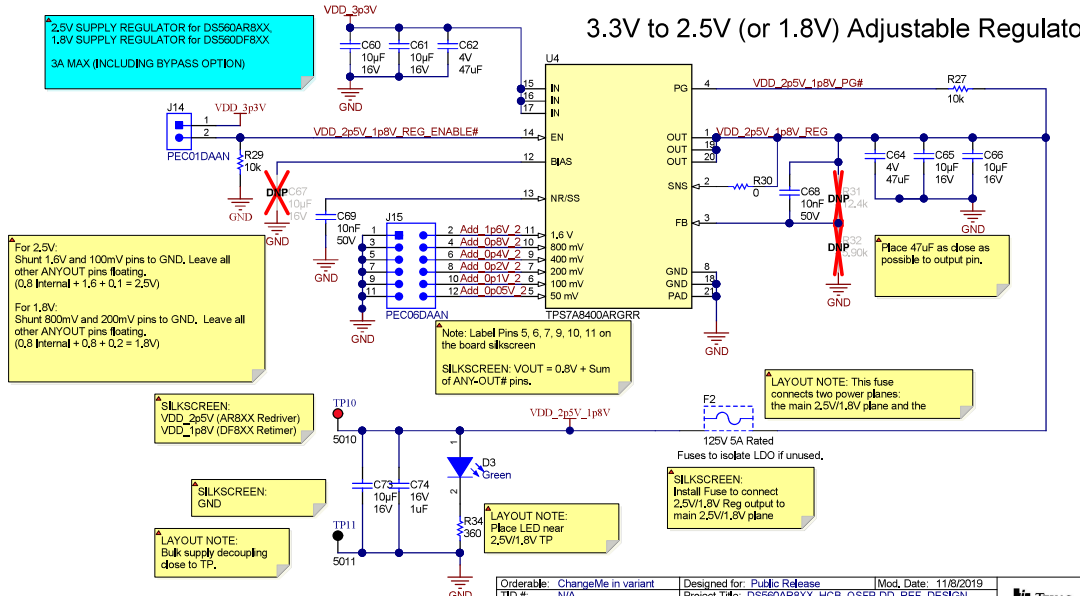
$$V_{OUT} = V_{NR/SS} + (\Sigma \text{ ANY} - \text{OUT Pins to Ground}) \quad (2)$$

Table 5. ANY-OUT Programmable Output Voltage (RGR Package)

ANY-OUT PROGRAM PINS (ACTIVE LOW)	ADDITIVE OUTPUT VOLTAGE LEVEL
Pin 5 (50mV)	50 mV
Pin 6 (100mV)	100 mV
Pin 7 (200mV)	200 mV
Pin 9 (400mV)	400 mV
Pin 10 (800mV)	800 mV
Pin 11 (1.6V)	1.6 V







Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 11/8/2019
TID #: N/A	Project Title: DS560AR8XX_HCB_QSFP-DD_REF_DESIGN	
Number: DS560AR8XX_HCB_QSFP-DD_REF_DESIGN	Sheet Title: 3.3V Regulator and CLK	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 4 of 6
Drawn By:	File: PWR_Regulator_CLK.SchDoc	Size: B
Engineer: Michael (Santa Clara) Lu	Contact: http://www.ti.com/support	

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