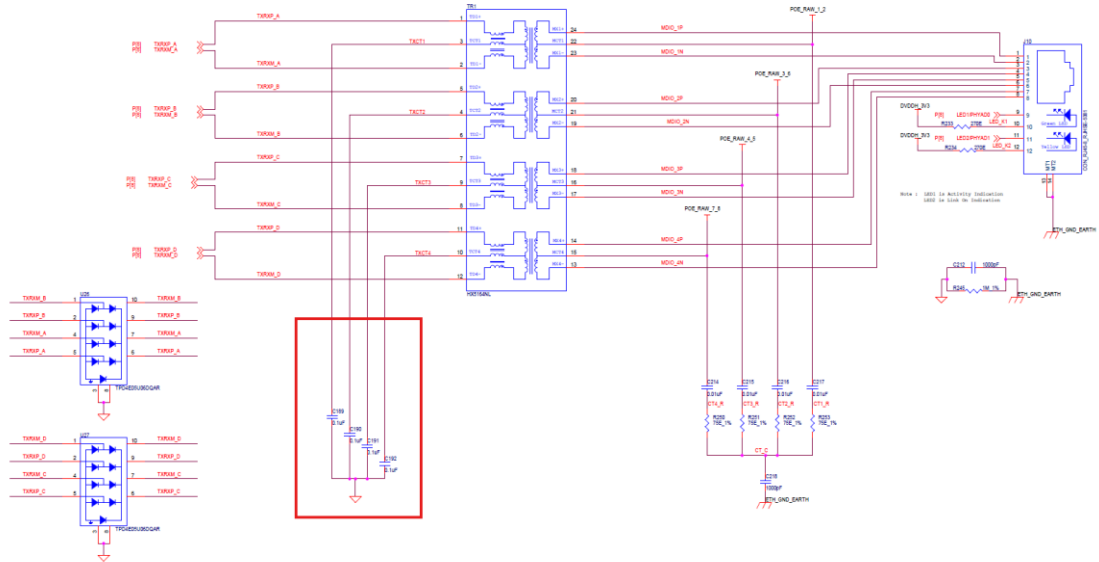


## Design differences on POE section.

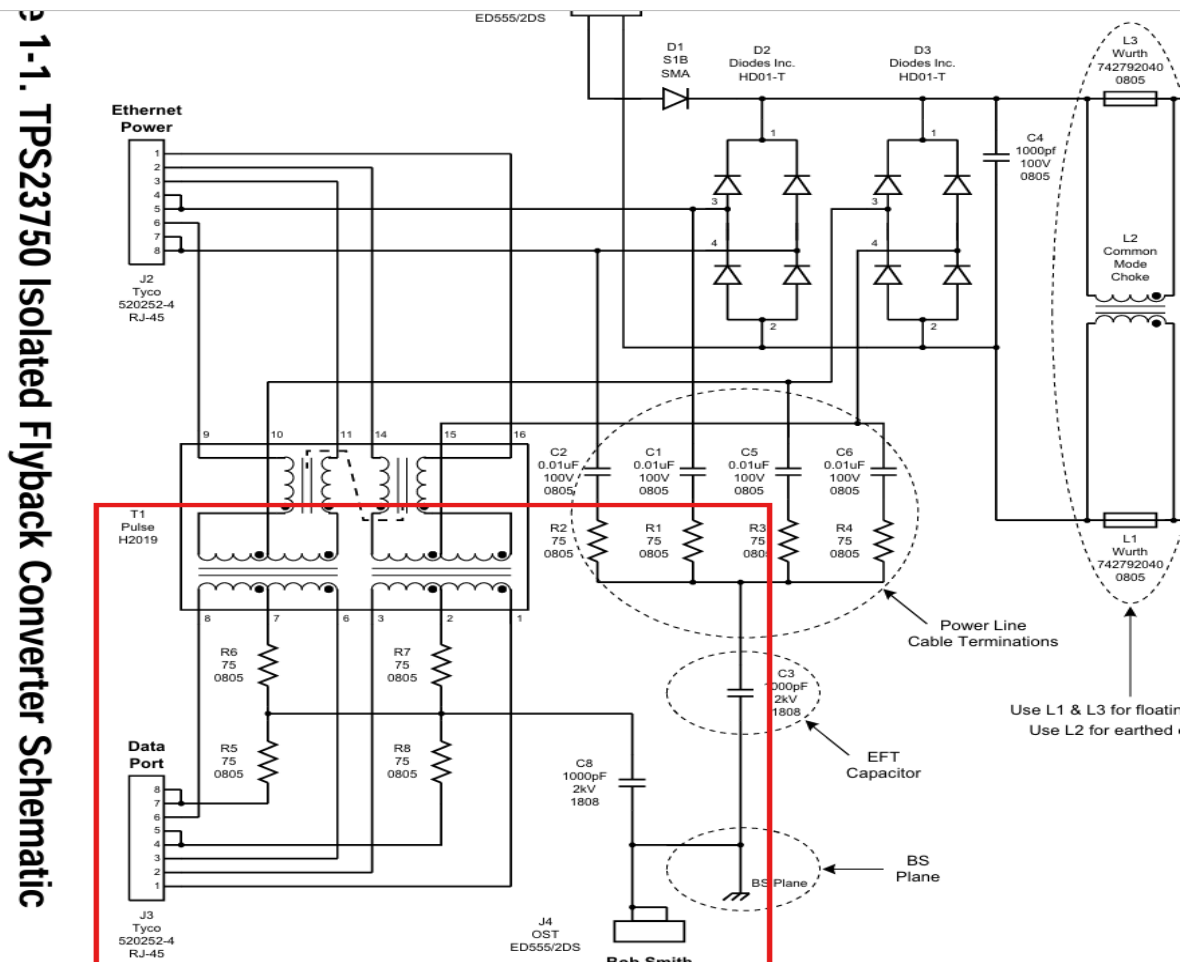
Dated-27/10/2025

- We observed that the Ethernet data lines in our design do not include the 75Ω resistors that are present in the TI reference design

### Mistral Design

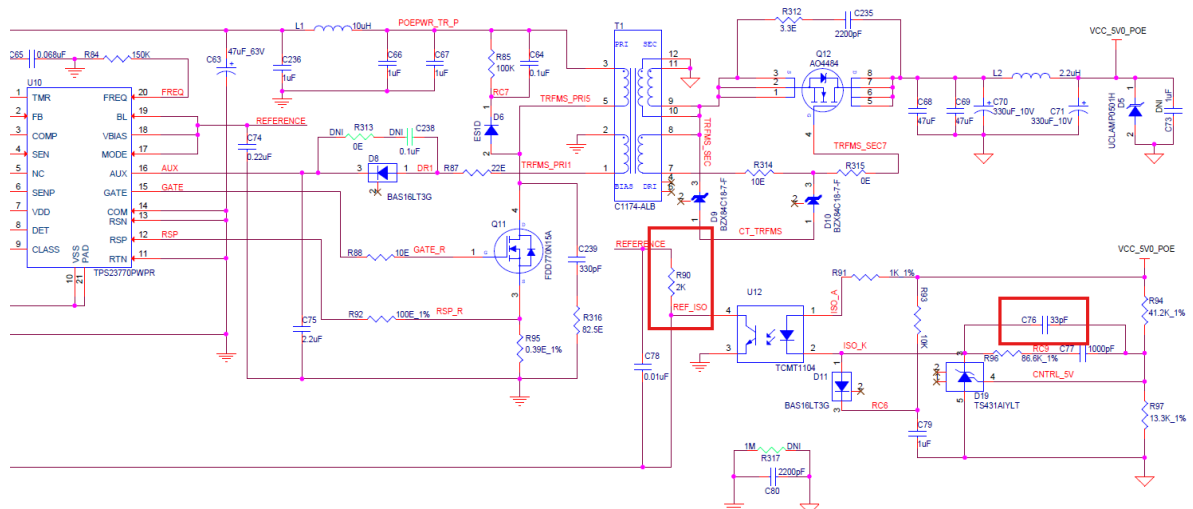


### TI Reference design



- Another difference we observed between the reference design and the Mistral design is that the snubber circuit on the collector of the U12 opto-isolator is not present in our design. Additionally, the C76 capacitor is populated in our design, whereas it is marked as DNI in the reference.

## Mistral Design



## TI Reference design

