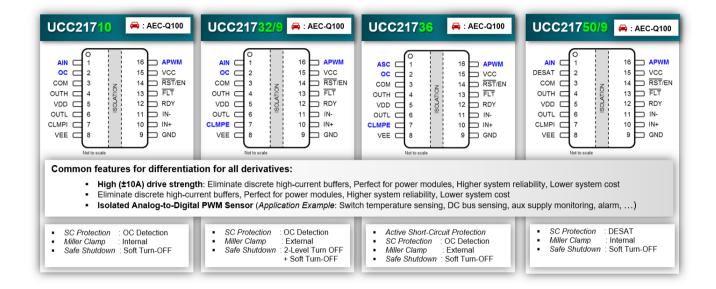
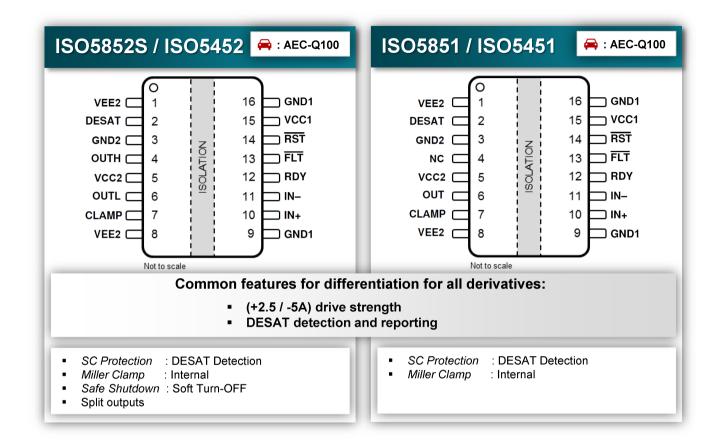
## Differences between UCC217xx and ISO5x5x

## **Pinout Differentiation**



Pins in **Black** have same pin locations as in ISO5852S or ISO5452. Pin in **Blue** are new pin definitions.



## **Device Comparison Table**

PARAMETER		UCC21710	UCC21732	UCC21736	UCC21739	UCC21759	UCC21750	ISO5451	ISO5452	ISO5851	ISO5852S	
Isolation Rating		Reinforced Basic					Reinforced					
Isolation	СМТІ			> 15	0V/ns			>50V/ns		>100V/ns		
	VIOWM: Max Working (DC)		2121V		990V		2121V	14:	20V	21	2121V	
	VIOTM: Max Transient		8000V		4242V			8000V				
	VISO (RMS, 60s): Max Surge	5700V			3000V			5700V				
Output Driver	Max Drive Current	+10A/-10A						+2.5A/-5A				
	Max Differential Voltage	33V						30V				
	(VDD-VEE)											
Driver Specs	Prop Delay	90ns						76 ns				
	Part-to-part Delay Matching	<30ns										
Gate driver	Split Output	Y			es			No	Yes	No	Yes	
enhanced	Active Miller Clamp (AMC)	Internal (4A) External				Internal (4A)		Internal (2.5A)				
features	VEE UVLO	No	No	Yes (-3V)	No			Yes (-2.25V)				
	Isolated Analog Channel	Yes No			Yes			No				
Protection	Active Short Circuit (ASC)	No Yes			No			No				
	Short-circuit Detection		Over-0	Current		DESAT		DESAT				
	Safe Shutdown Scheme	Soft Turn-Off (400mA)	2-Level Turn-Off (9V)	Soft Turn-Off (900mA)	2-Level Turn-Off (9V)	Soft Turn-0	Off (400mA)	No	Soft Turn-Off (130mA)	No	Soft Turn OFF (130mA)	
	Fault Feedback	Yes (600ns)						Yes (<1.4us)				
	Fault RST & RDY	Yes										
Package	Package Options	16-pin SOIC DW 10.3mm x 7.5mm										
Device Benefit	Product Differentiation	Internal AMC +	External AMC +	ASC Protection	Basic-ISO	Basic-ISO	DESAT + STO	Single output		Single output	Split output	
		STO	2LTO	VEE UVLO	version of UCC21732	version of UCC21750			STO		STO	
← : AEC-Q100 AEC-Q100 Version		Yes										