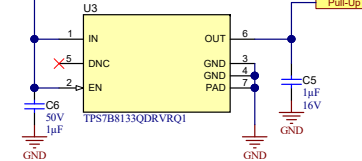


Vin 3V to 40V max
IN (+)
GND

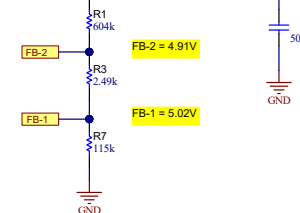
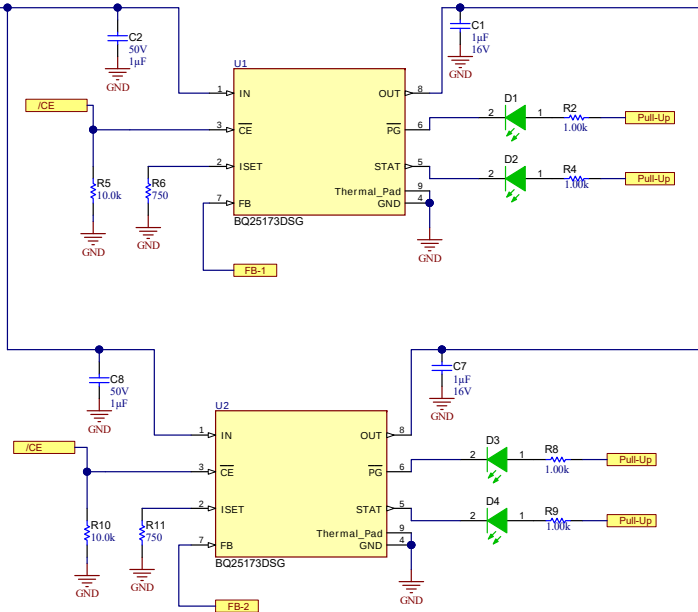


3.3V regulator optional -- Supply LED Voltage

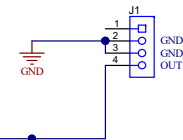
Note -- Max Voltage to /PG, STAT and /CE is 5.5V

Output current set by R6 and R11, 750 ohms = 400mA.
Calculation $300 / R-ISET = I_{out}$

Thermal plane area should be 20mm X 20mm area for each regulator



FeedBack voltage is 4.9V on follower (U2) unit and 5.0V on leader (U1) unit. This will prevent oscillation near charge complete.



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable:	Designed for:	Mod. Date: 11/3/2022
TID #:	Project Title:	
Number:	Rev:	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 1 of 2
Drawn By:	File: BQ25173 Dual Nov 2022.SchDoc	Size: B
Engineer:	Contact:	