THIS ATTACHMENT IS TI UCC28070 EXCEL WORKSHEET SHOWING ALL VALUES USED IN THIS PROJECT. INCLUDED ARE YOR SCHEMATIG ALONG WITH THE TWO CHARTS.
/cfs-file/__key/communityserver-components-multipleuploadfilemanager/cf47f6f6_2D00_8cdc_2D00_44e2_2D00_a87d_2D00_0ec26eae9ba4-78544-complete/REVIEW-UCC28070-DESIGN-TOOL-SAVED-28-JUL-19.oxps

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WHERE I NEED GUIDANCE IS HOW TO GET THE GDA & GDB OUTPUTS TO WORK.
Hello John, Hope you had a great weak end.
This message is in regard to the answer you sent the other day.

Answer Suggested: UCC28070: 7.3.17 ZERO-POWER DETECTION
I SENT A QUESTION AND BELOW IS THE REPLY.
TI E2E Community - Automated Email
ToEddie Loy
Show more
Jul 26 at 1:07 AM

RE: UCC28070: 7.3.17 ZERO-POWER DETECTION **John Griffin1**

Hello Eddie,
In order to view a GDA or GDB signal you will need VSENSE> 0.75V and VINAC>0.7V
This will cause the error amplifier output to go to its maximum value and the multiplier to go to one of its quantized levels.
With VSENSE< 0.7V the controller shuts down due to an open loop protection error and with VINAC<0.7V the multiplier output is reset to zero.
You will also need VCC> V_UVLO = 10.2V
The effect you are seeing right now is not really due to a zero power detection.
Regards
John

Below you will see that pin 3 (vao) never rises above 230 mV. Pin 3 never goes any higher.
As per DS 7.3.17 Zero-Power Detection pin 3 should rise up to a min of .750V.

I do not know why I cannot get any output signal your help will be greatly appreciated.

Waiting patiently for your reply.

EDDIE LOY

CH1 – PIN 13, CH2 – PIN 3, CH3 – PIN 4, CH4 - 5