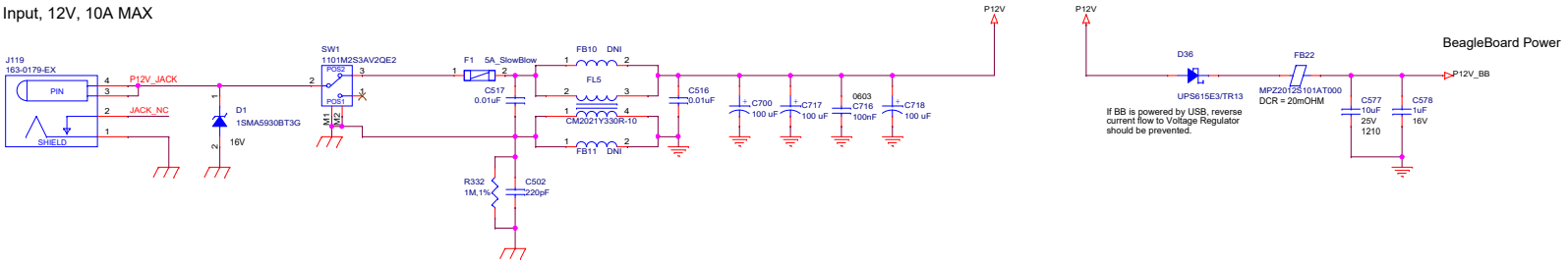
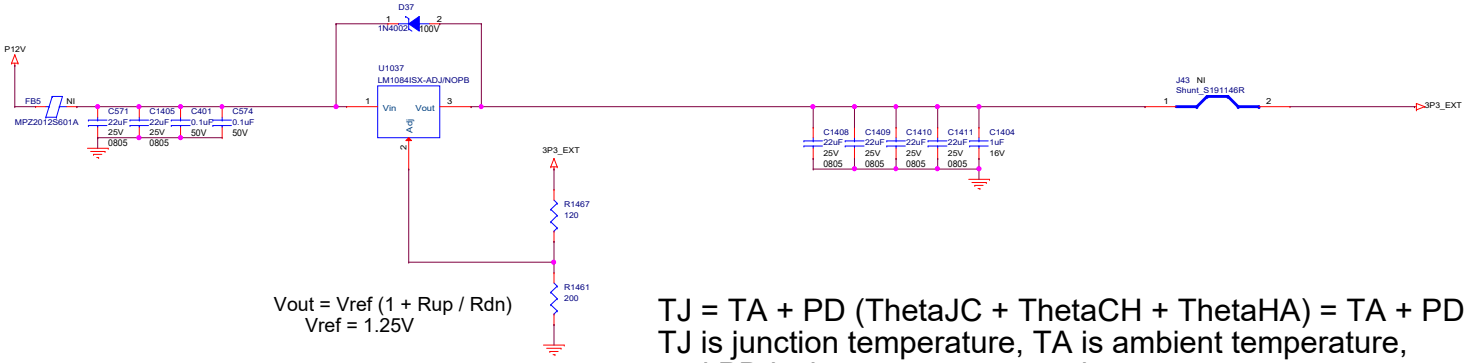
DC Input, 12V, 10A MAX

12V POWER INPUT



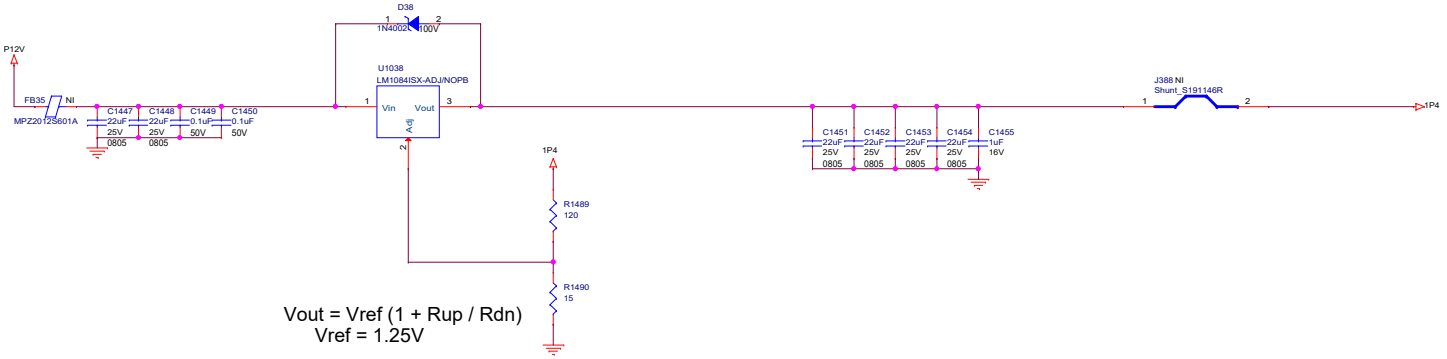
Generating 3.3V for onboard components and Edison



Vout = Vref (1 + Rup / Rdn)
Vref = 1.25V

TJ = TA + PD (ThetaJC + ThetaCH + ThetaHA) = TA + PD ThetaJA
TJ is junction temperature, TA is ambient temperature,
and PD is the power consumption
ThetaJA (max) = TR(max)/PD = TJ(max) - TA(max)/PD

Generating 1.4V for Input of LDOs



Vout = Vref (1 + Rup / Rdn)
Vref = 1.25V

<Variant Name>

Scale = Ethernovia

Tuesday, December 22, 2020

Size	CAGE Code	DWG NO	Rev
C			
Scale	Edison Schematics	Sheet	of 21