

SCH		ERC Errors			
Type		Sheet	Module		
Consistency not checked (no board loaded)					
4 Errors (28)					
	More than one OUTPUT pin on net CELL4+				1
	More than one OUTPUT pin on net CELL4+				1
	More than one OUTPUT pin on net CELL4+				1
	More than one OUTPUT pin on net CELL4+				1
	More than one OUTPUT pin on net V-				1
	More than one OUTPUT pin on net V-				1
	More than one OUTPUT pin on net V-				1
	More than one OUTPUT pin on net V-				1
	OUTPUT and SUPPLY pins mixed on net V- (out)				1
	OUTPUT and SUPPLY pins mixed on net V- (out)				1
	OUTPUT and SUPPLY pins mixed on net V- (out)				1
	OUTPUT and SUPPLY pins mixed on net V- (out)				1
	OUTPUT and SUPPLY pins mixed on net V- (sup)				1
	OUTPUT and SUPPLY pins mixed on net V- (sup)				1
	OUTPUT and SUPPLY pins mixed on net V- (sup)				1
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	OUTPUT and SUPPLY pins mixed on net V- (sup)				1
	OUTPUT and SUPPLY pins mixed on net V- (sup)				1
	Only INPUT pins on net CS				1
	Only INPUT pins on net SCLK				1
	Only INPUT pins on net SDI				1
	SUPPLY pin V- overwritten with more than one signal (V-, REG50, TAB)				1
	SUPPLY pin V- overwritten with more than one signal (V-, REG50, TAB)				1
4 Warnings (80)					
	Only one pin on net ALERT				1
	Only one pin on net CS				1
	Only one pin on net DRDY				1
	Only one pin on net FAULT				1
	Only one pin on net GPIO				1
	Only one pin on net SCLK				1
	Only one pin on net SDI				1
	Only one pin on net SDO				1
	POWER pin U1 AGND connected to V-				1
	POWER pin U1 BAT1 connected to CELL4+				1
	POWER pin U1 BAT2 connected to CELL4+				1
	POWER pin U1 EPAD connected to TAB				1
	POWER pin U1 LDOD1 connected to LDOD2				1
	POWER pin U1 VREF connected to NS12				1
<div><input type="checkbox"/> Centered<div>Processed</div></div> <div><div>Clear all</div><div>Approve</div></div>					