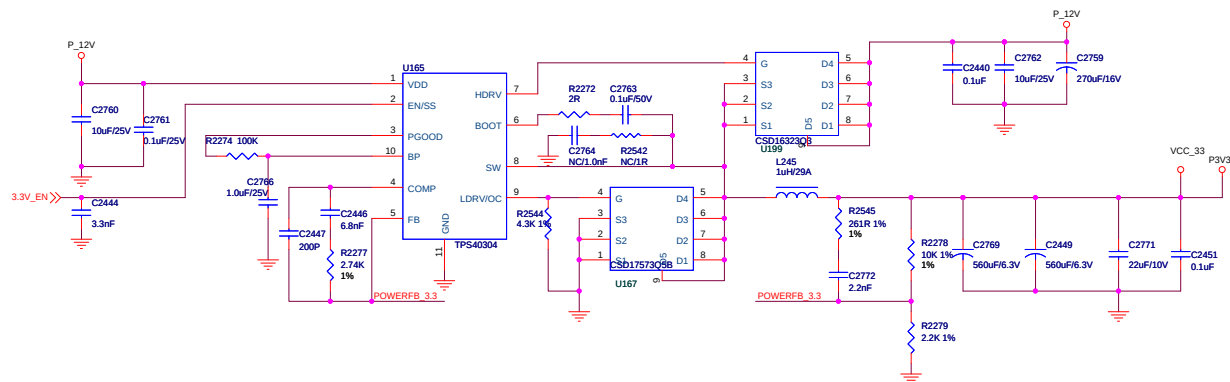
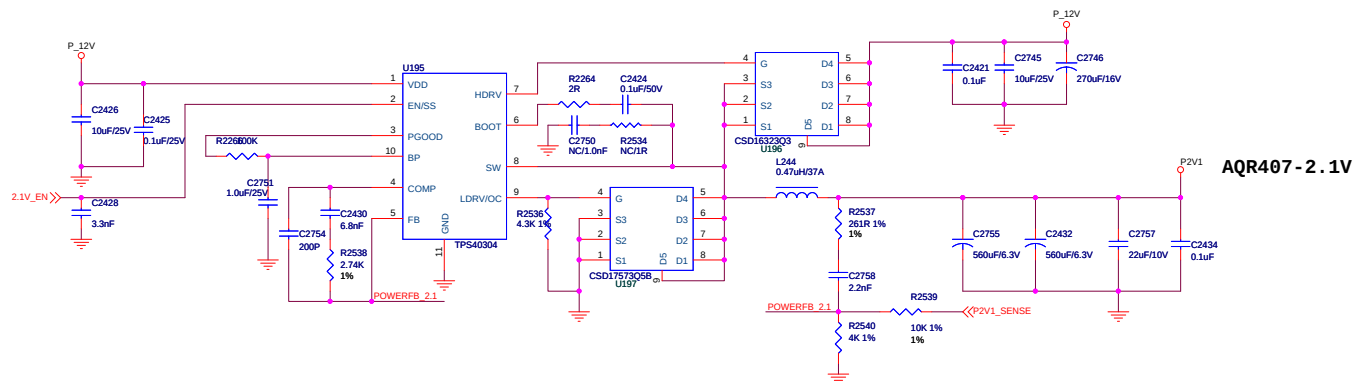
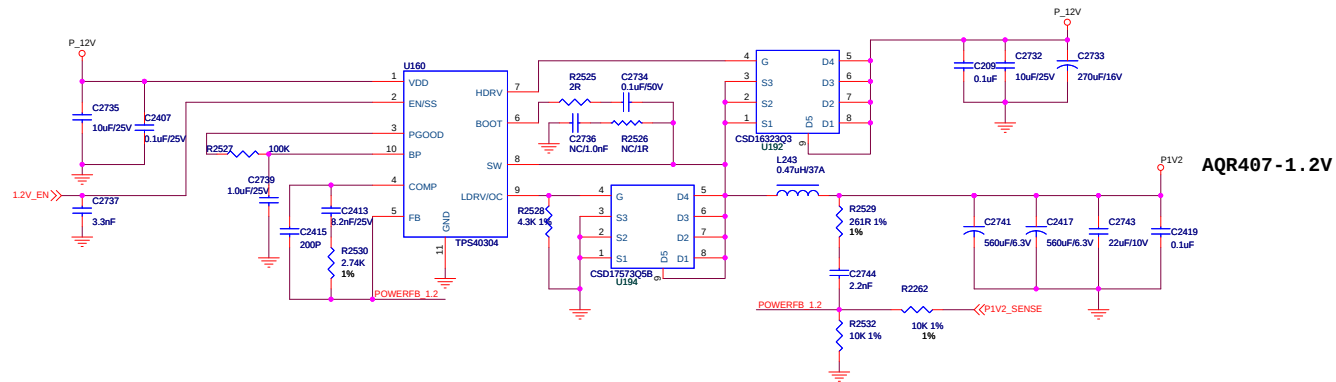




Cameo Communication INC.

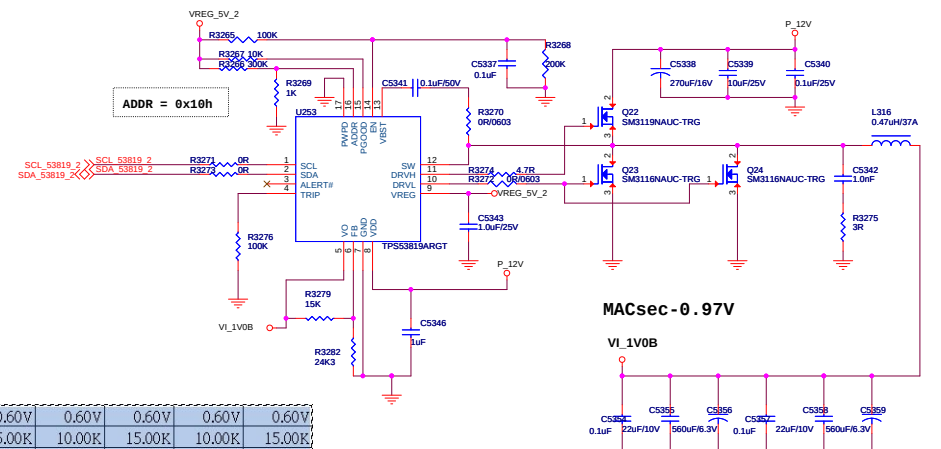
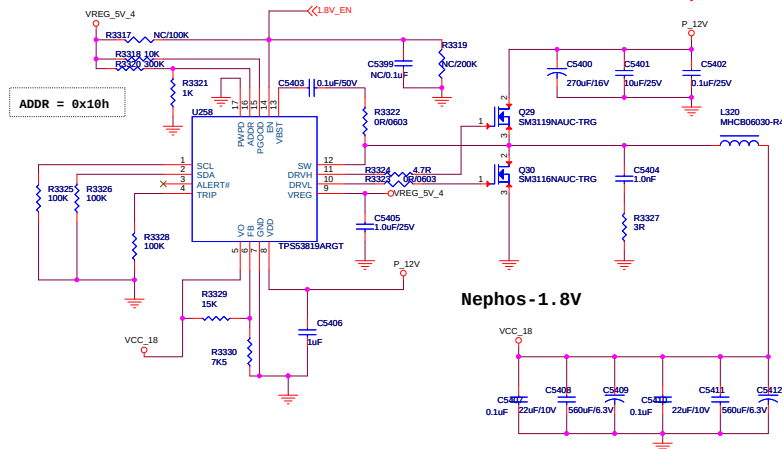
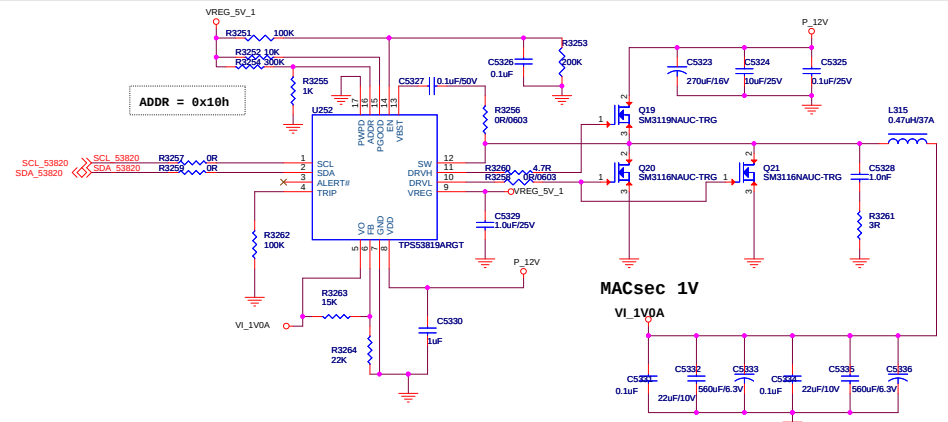
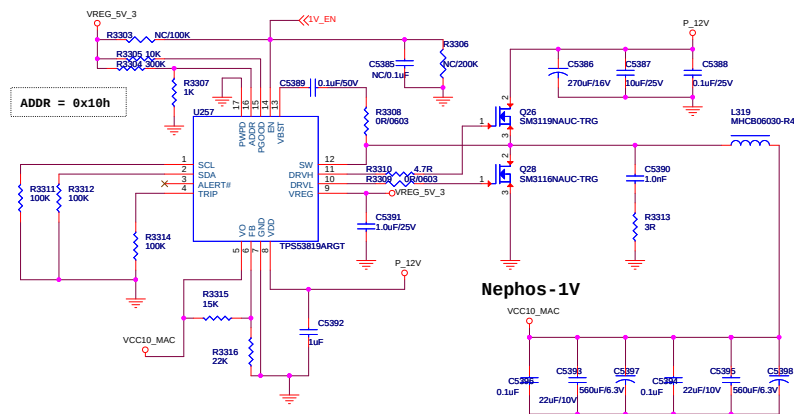
Name: ESX601-54RQ

Other POWER_01

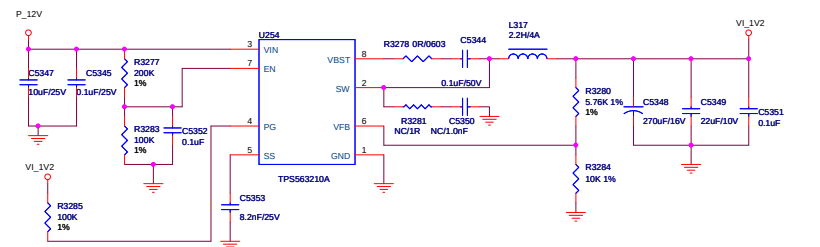
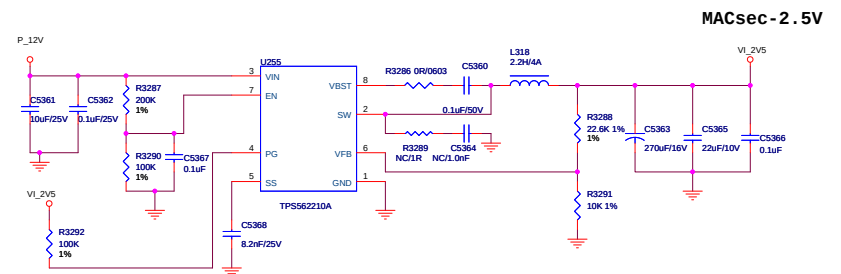
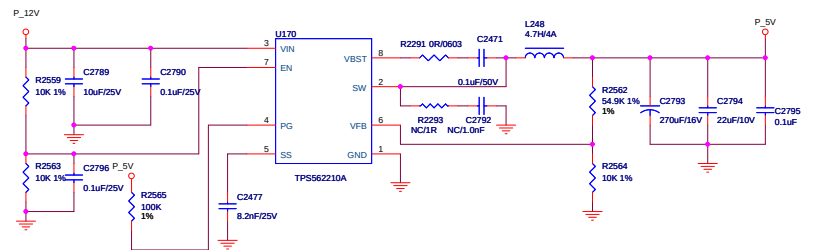
Date: Wednesday, May 31, 2017

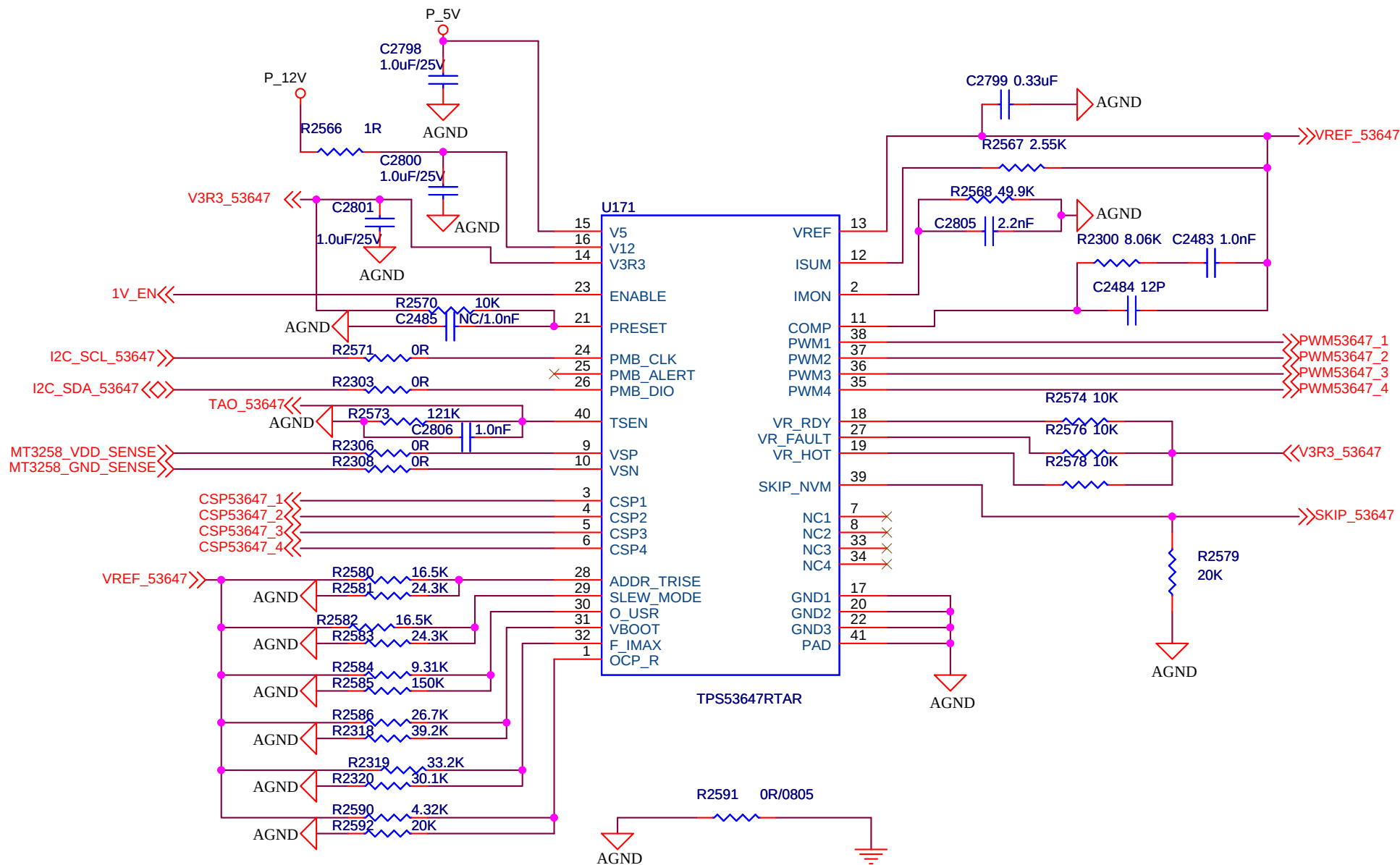
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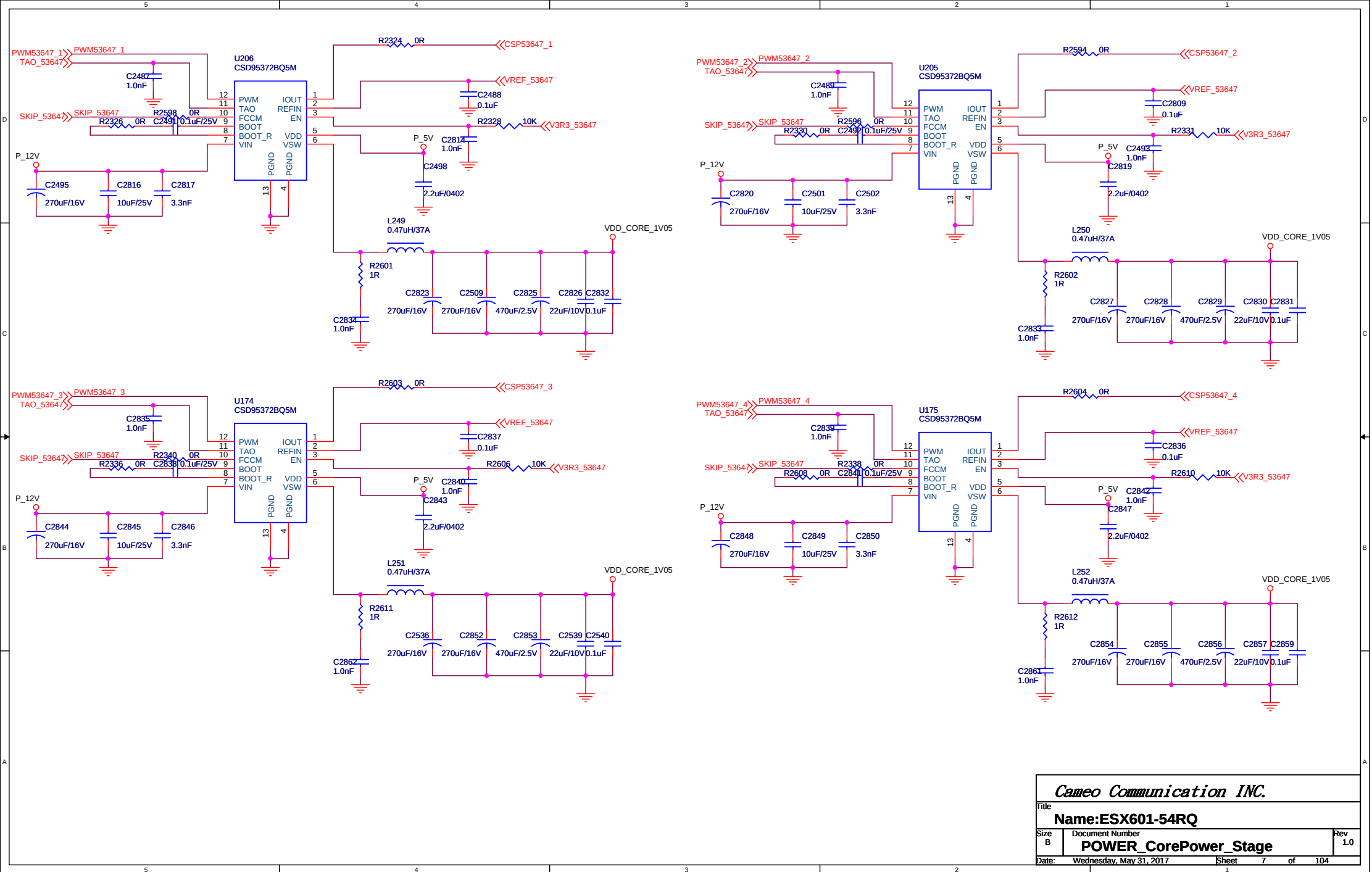
Vref	0.60V	0.60V	0.60V	0.60V	0.60V	0.60V
RH	10.00K	15.00K	10.00K	15.00K	10.00K	15.00K
RL	5.00K	7.41K	14.50K	21.80K	16.20K	24.30K
OUT	1.80V	1.81V	1.01V	1.01V	0.97V	0.97V

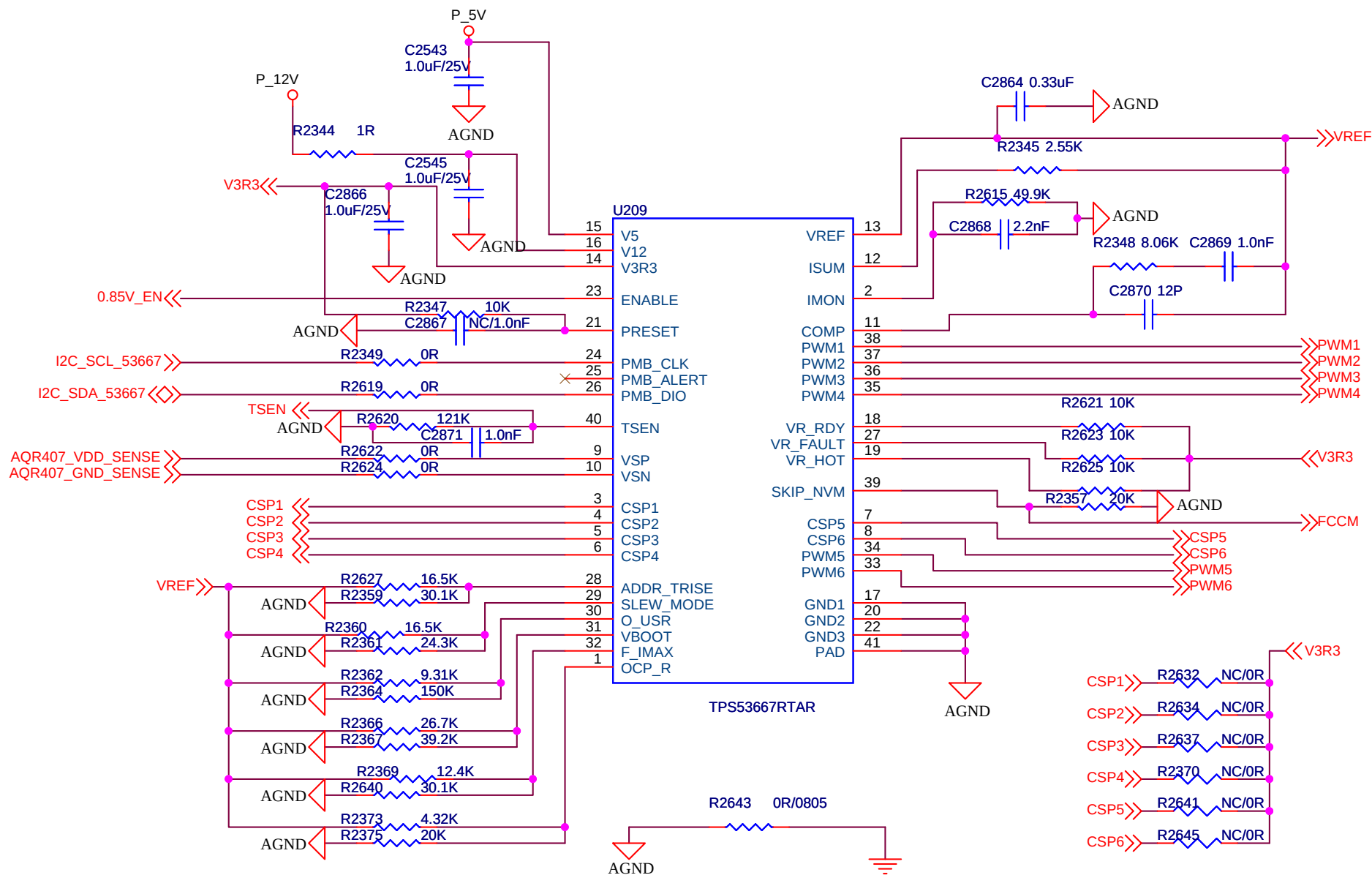




PMBus Address: 1110001 (bin)
PMBus Address: E1(hex)

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Title		
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PMBus Address: 1110010 (bin)
PMBus Address: E2(hex)

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Title

Name:ESX601-54RQ

Size
A

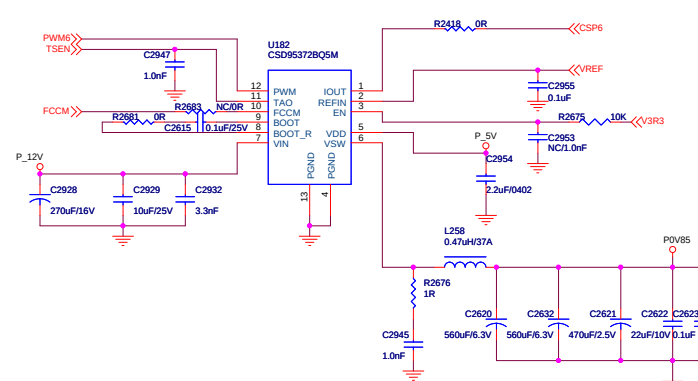
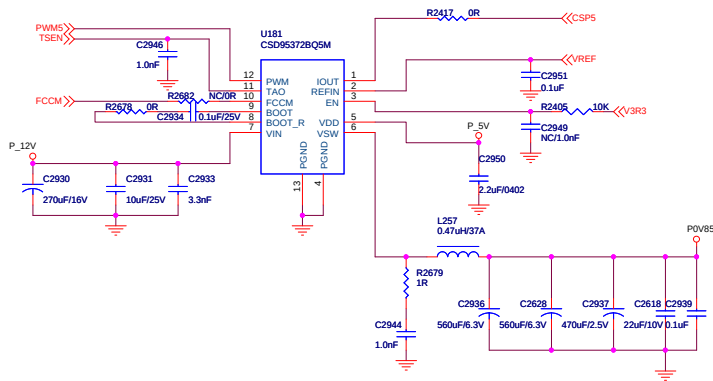
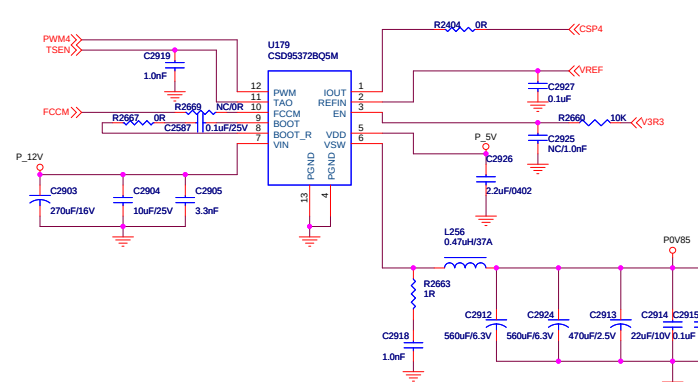
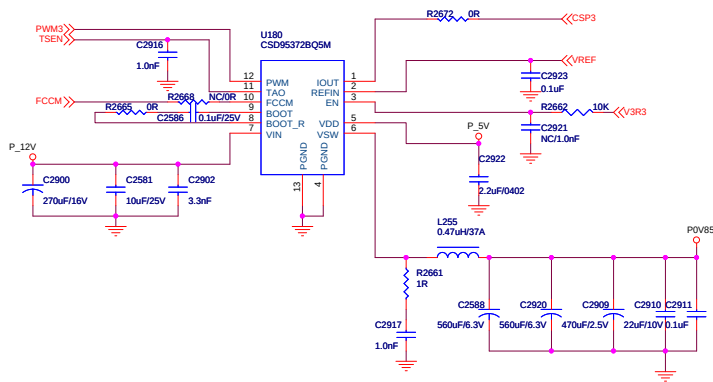
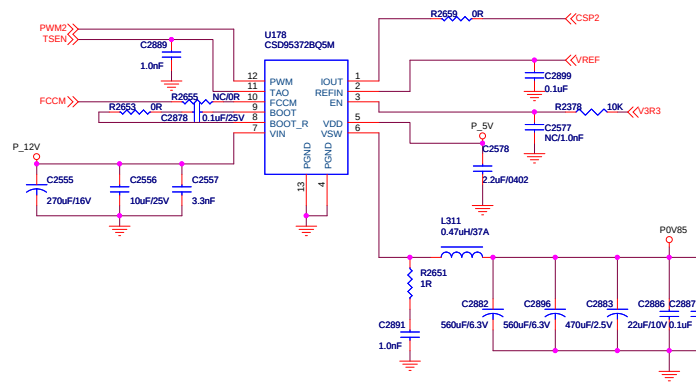
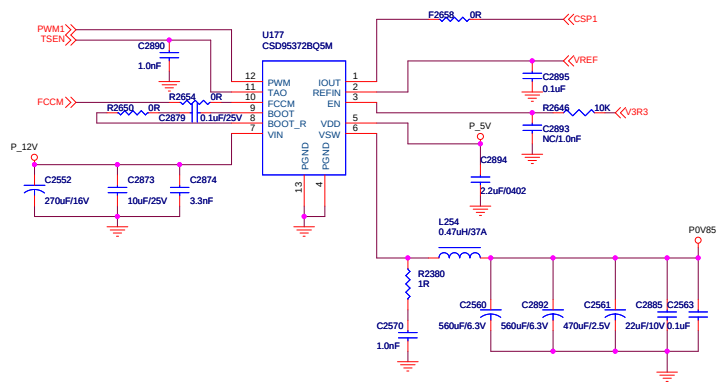
Document Number

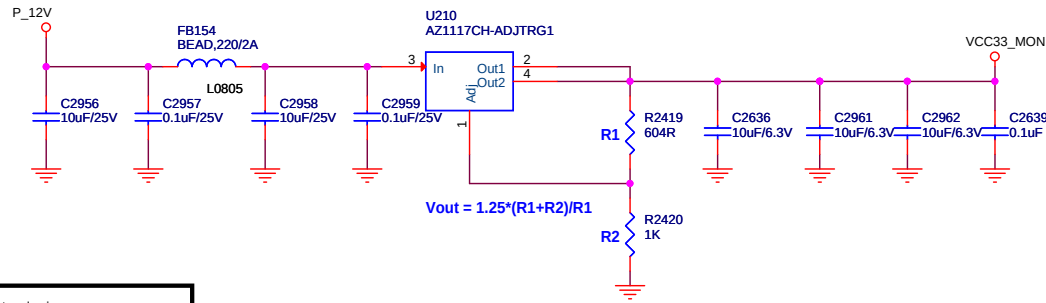
PHY_POWER

Rev
1.0

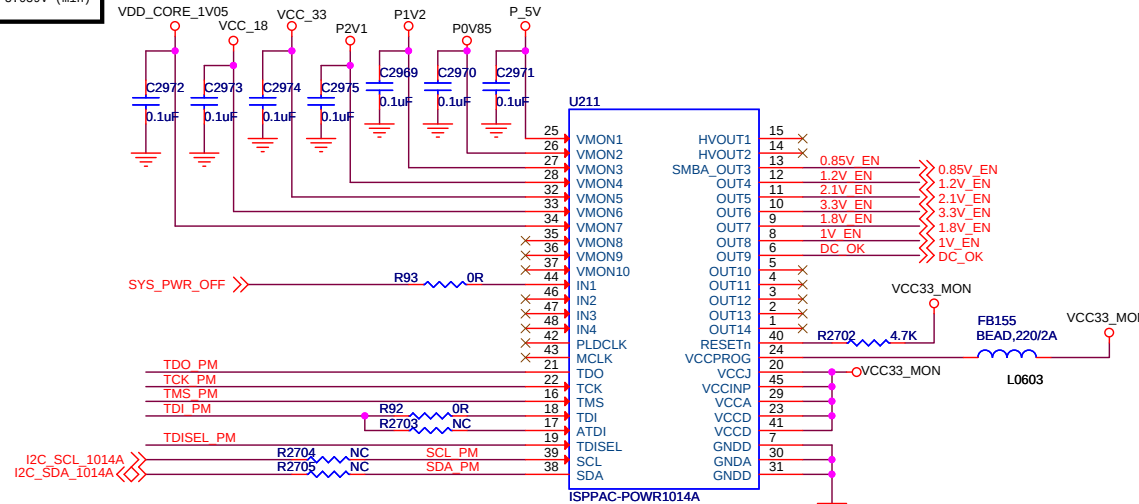
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VMON_12V, -10% to design
under voltage threshold =1.97V
Normal voltage threshold = 3.039V (min)



undefine ,default ADDR = 0x0h

TDISEL = 1, standard TDI pin is enabled
TDISEL = 0, ATDI is enabled.

AQR407Requirement :

VDD(0.85V) to DVDD1.2(1.2V) to DVDD2.1(2.1V) to DVDD33 (3.3V) <100ms

Power sequence :

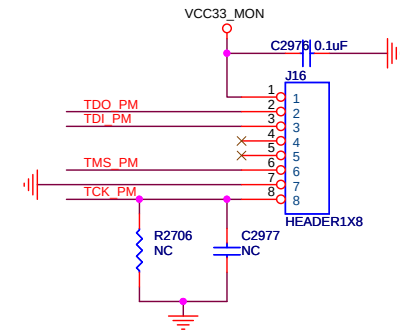
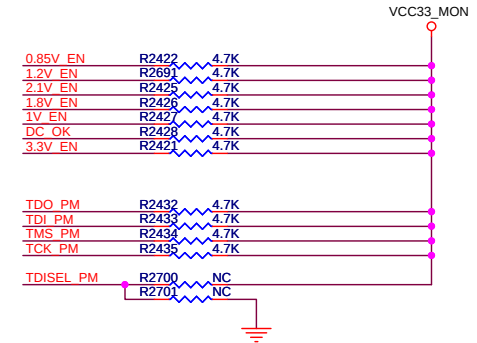
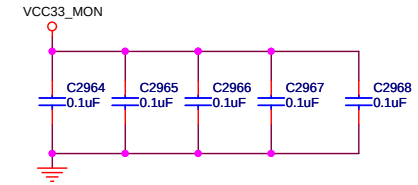
0.85V-->1.2V-->2.1V and 3.3.V

MT3257Requirement :

DVDD33 (3.3V) to DVDD18 (1.8V) to VDDK(1.05V) and Analog PWR (1V) < 5ms

Power sequence :

3.3V-->1.8V-->1.05V and 1.0V



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