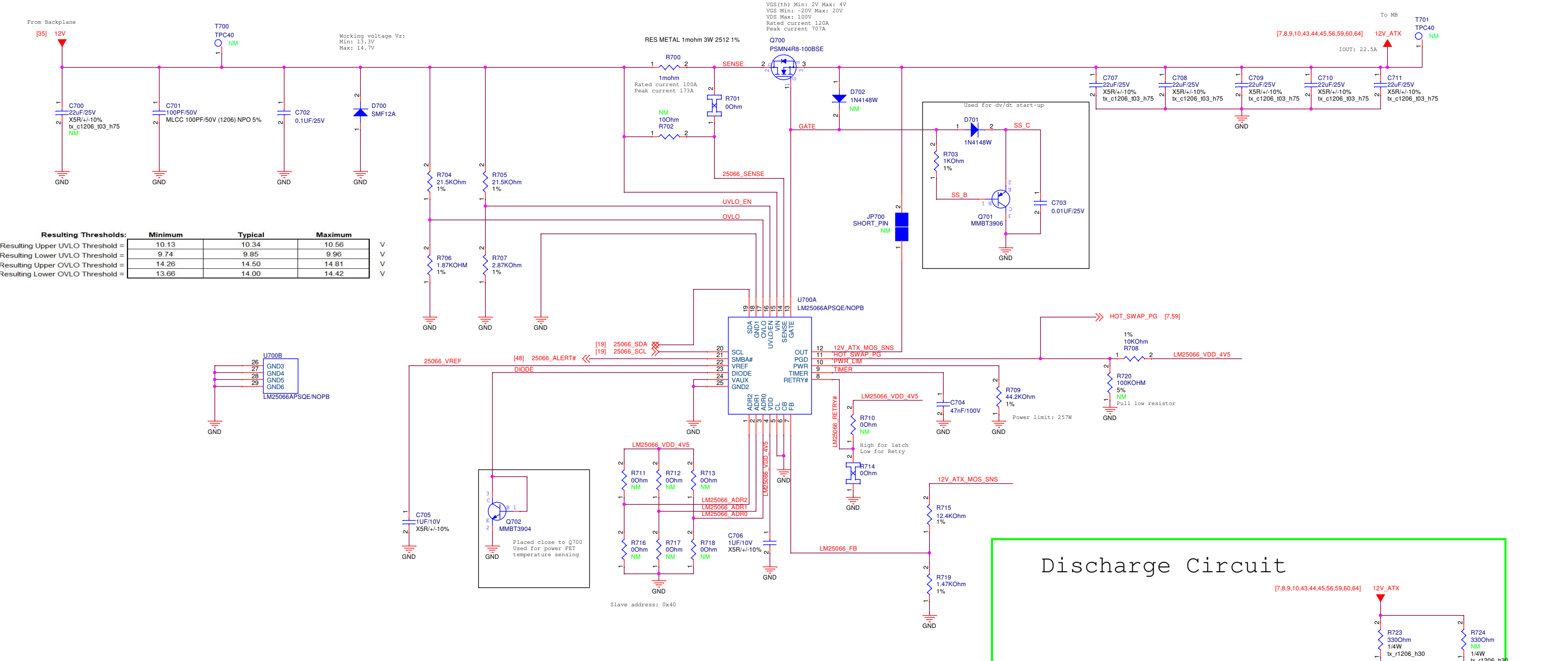
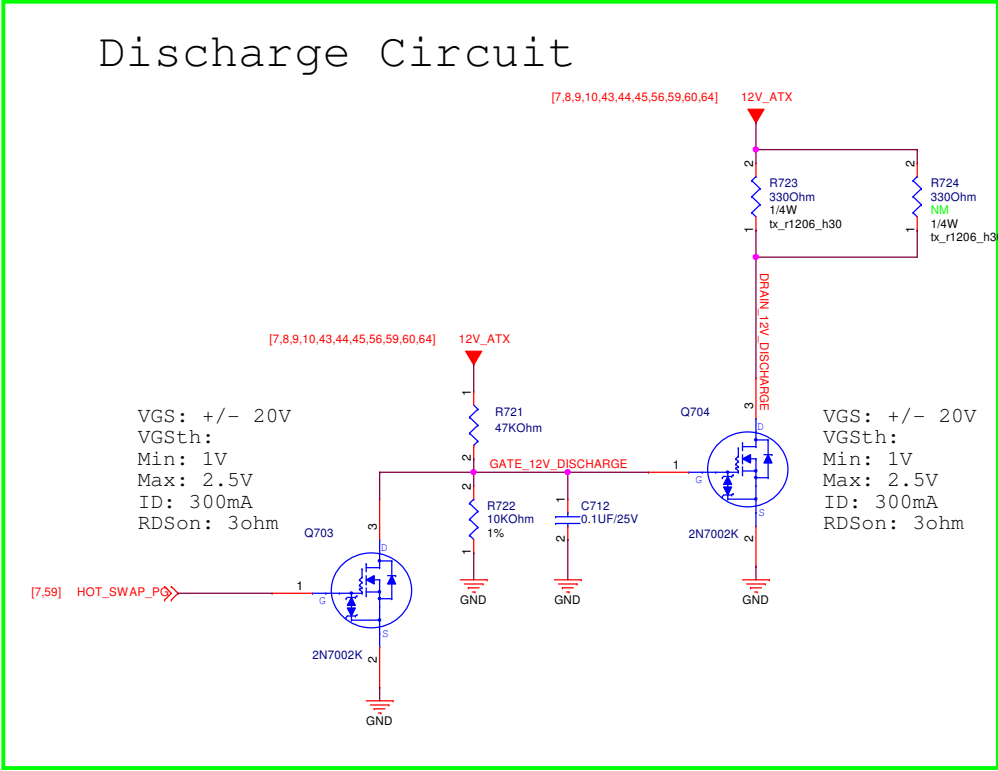


PWR HOT SWAP PROTECTION



Resulting Thresholds:

	Minimum	Typical	Maximum	
Resulting Upper UVLO Threshold =	10.13	10.34	10.56	V
Resulting Lower UVLO Threshold =	9.74	9.85	9.96	V
Resulting Upper OVLO Threshold =	14.26	14.50	14.81	V
Resulting Lower OVLO Threshold =	13.66	14.00	14.42	V



Design Summary

R_S	=	1	m Ω
R_{CL1}	=	Open	Ω
R_{CL2}	=	0	Ω
R_{PWR}	=	44.2	k Ω
C_T	=	47.00	nF
R_1	=	21.5	k Ω
R_2	=	2.87	k Ω
R_3	=	21.5	k Ω
R_4	=	1.87	k Ω
R_{FB1}	=	12.4	k Ω
R_{FB2}	=	1.5	k Ω
R_{PD}	=	10	k Ω
C_{IN}	=	0.01	μ F
C_{VDD}	=	1	μ F
C_{VREF}	=	1	μ F
D_1, D_3	=	1N4148W-7-F	
D_2	=	SK153-TP	
$C_{dv/dt}$	=	10	nF
Connect CL Pin to		GND	μ F
Connect CB Pin to		GND	μ F
Connect /Retry to		GND	μ F
Q_1	=	PSMN4R8-100BSE	
Q_2	=	MMBT3904	
Q_3	=	MMBT3906	
Z_1	=	5.0SMDJXXX	

	Typical	Units
Current limit	25.0	A
Power Limit	257	W
Circuit Breaker Current	45.0	A
Startup Time	6.36	ms
Insertion Delay	14.5	ms
Fault Timeout	0.888	ms
Restart Time During Fault	108.309	ms
Upper UVLO Threshold	10.344	V
Lower UVLO Threshold	9.850	V
Upper OVLO Threshold	14.497	V
Lower OVLO Threshold	14.002	V
PGD Threshold	11.0110816	V
PGD Hysteresis	297.6	mV