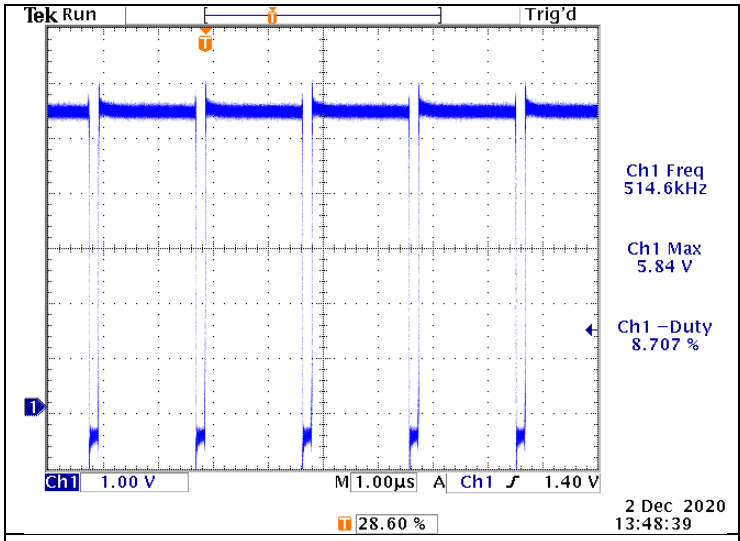

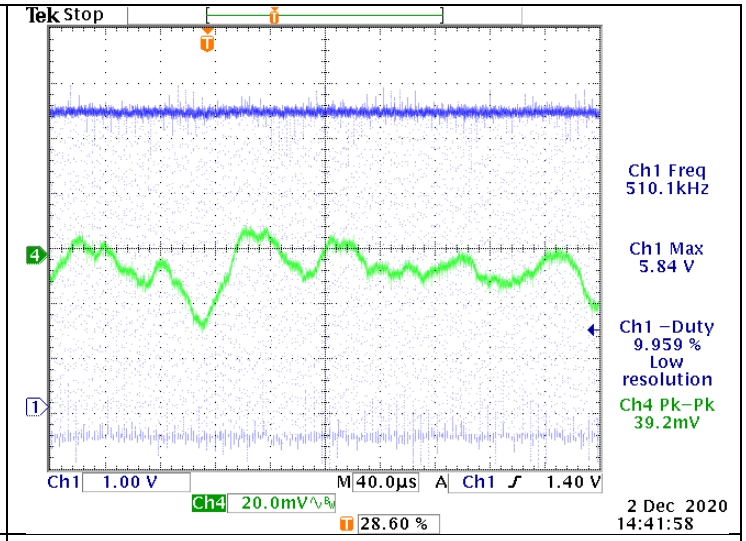

I have now removed C243 to disconnect the 90 KHz clock from TPS611781 U8. This is the switchnode waveform at J424. Input current to the board is 317 mA. Vout (U8_VOUT_5V5) = 5V66. TPS611781 FB pin (C182) is at 1.252 VDC. I calculate an "on" time of 169 nSec.	Same as on left but slower timebase and Ch4 shows the boost output. The noisy output seems to makes sense if the IC is not regulating due to minimum pulse width (which seems to be the case.)