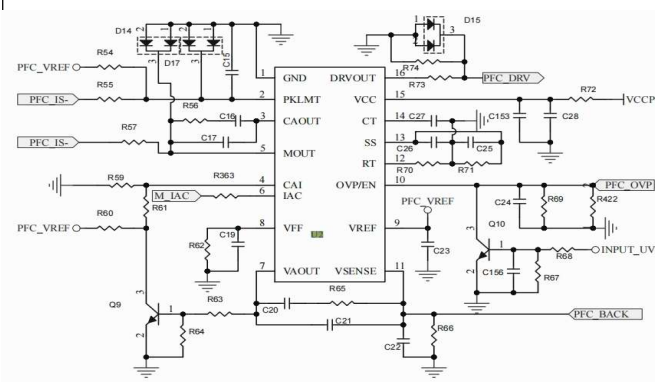


GIP UCC3818A FA test waveform

Schematic

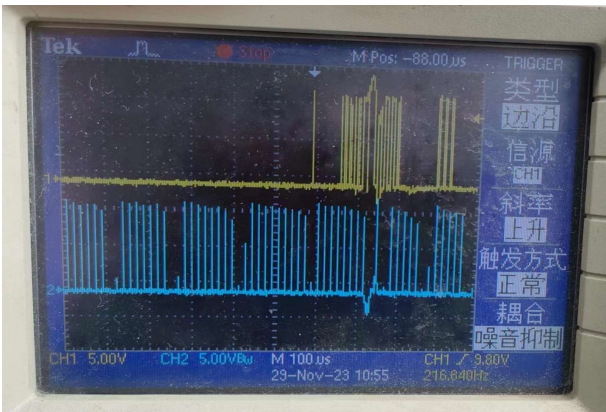


IC Top Mark

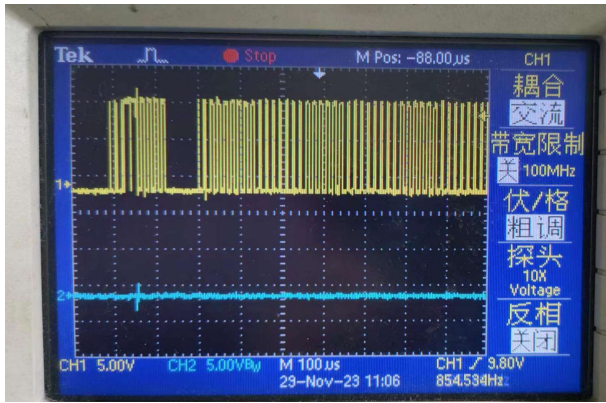


Test Point: PIN16 DRVOUT, PIN15 VCC

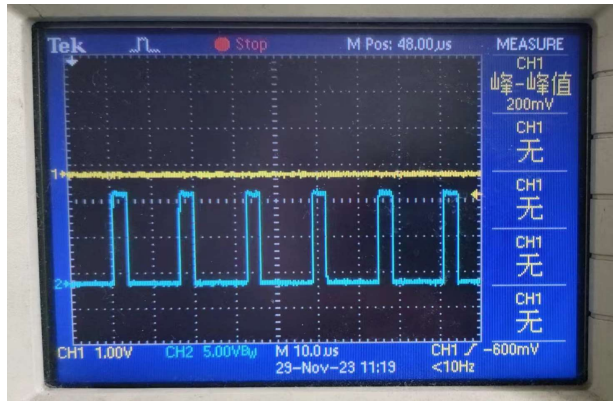
1. No load-PIN16. CH1: NG IC Board have abnormal noise , CH2: OK IC Board no abnormal noise



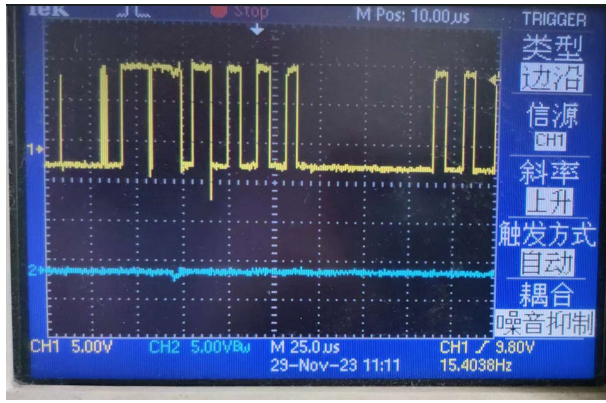
2. 56V 1.5A load-PIN 16 NG IC Board have abnormal noise



56V 1.5A load-PIN 16 OK IC Board no abnormal noise



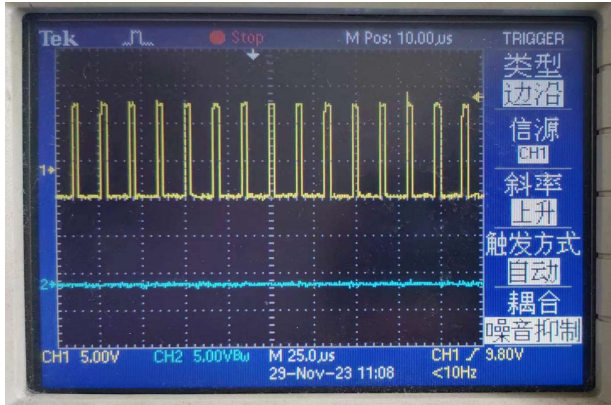
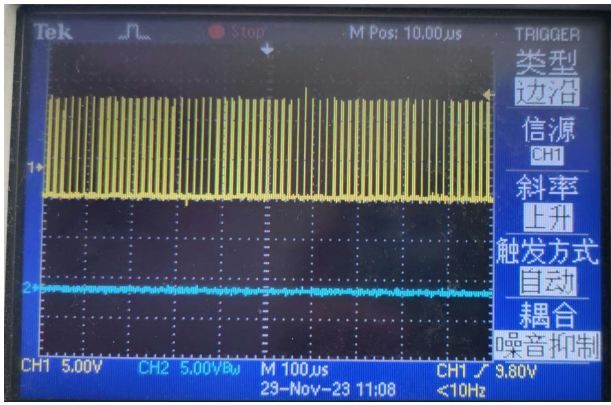
3. 56V 2.5A load-PIN16 NG IC Board have abnormal noise



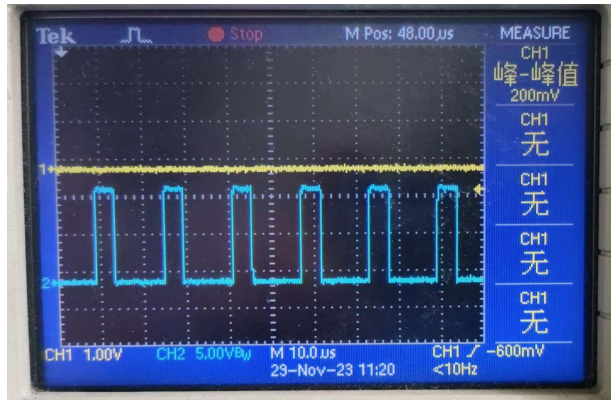
56V 2.5A load-PIN 16 OK IC Board no abnormal noise



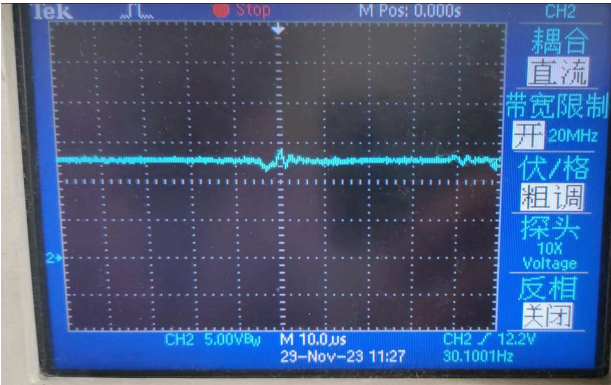
4. 56V 3A load-PIN16 NG IC Board no abnormal noise



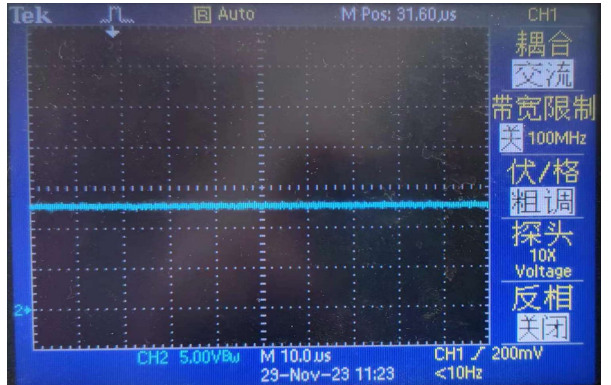
6V 3A load-PIN16 OK IC Board no abnormal noise



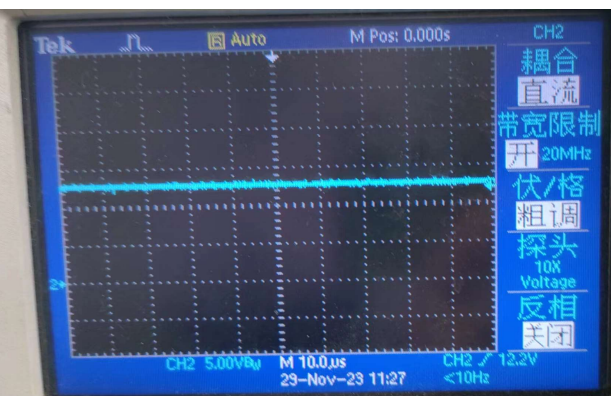
5. PIN15-VCC: NG Board with 1.5A Load have abnormal noise



PIN15-VCC: OK Board with 1.5A Load no abnormal noise



6. PIN15-VCC: NG Board with 3A Load no abnormal noise



PIN15-VCC: OK Board with 3A Load no abnormal noise

