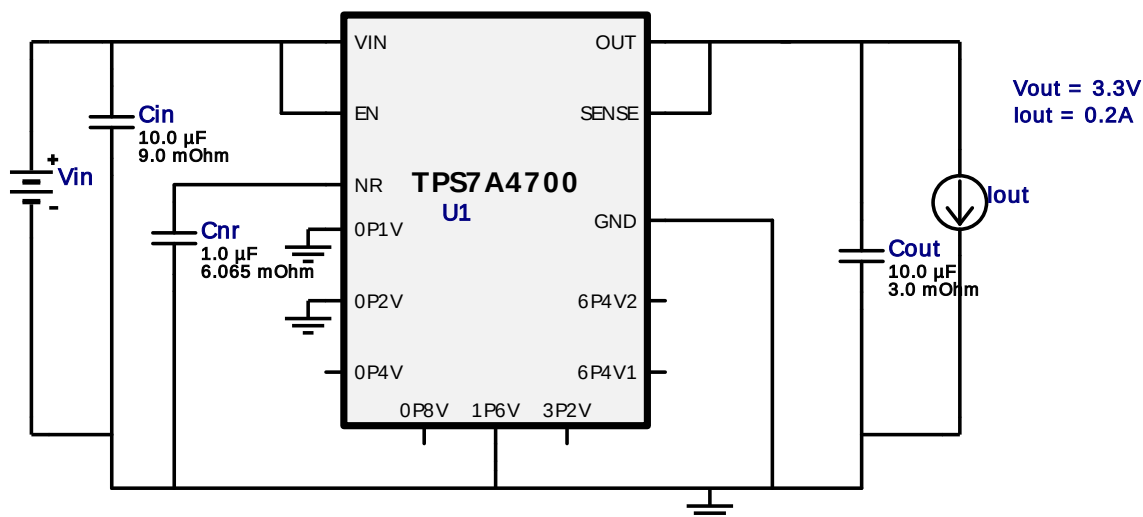


WEBENCH® Design Report

Design : 1228111/898 TPS7A4700RGWR
TPS7A4700RGWR 4.5V-5.2V to 3.30V @ 0.2A



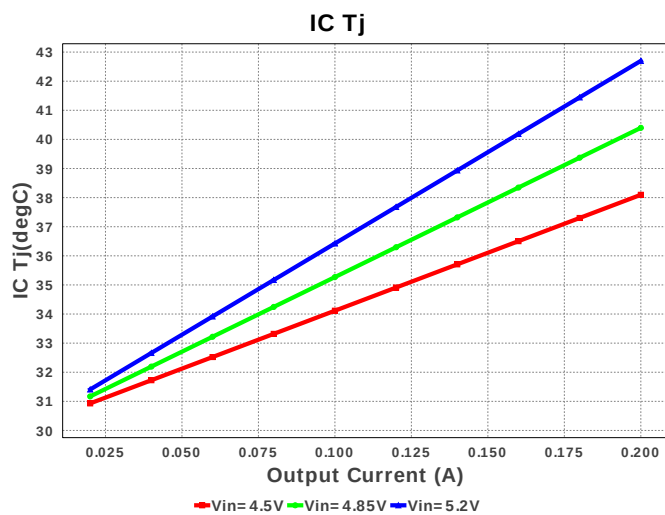
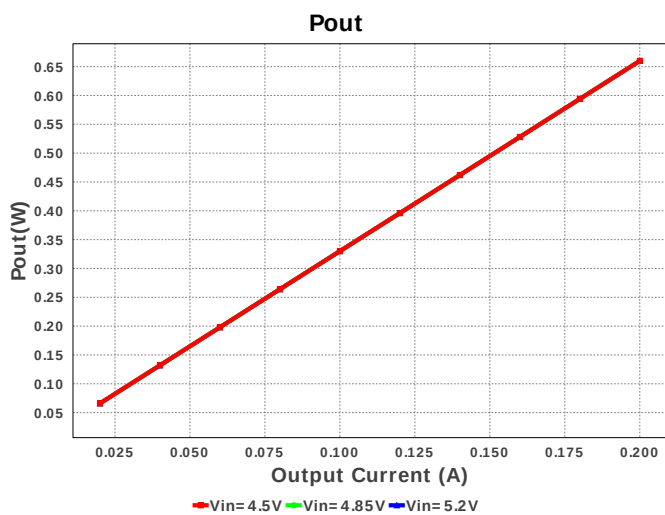
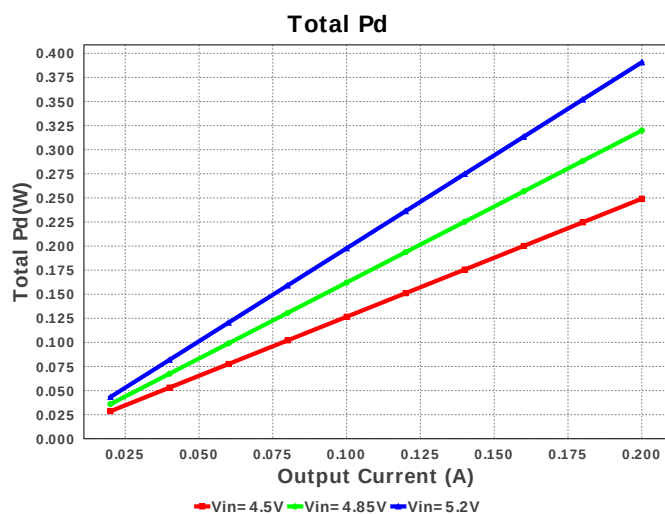
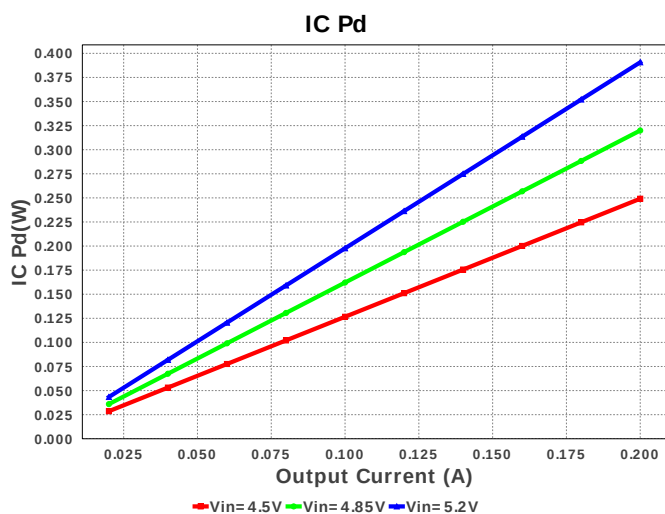
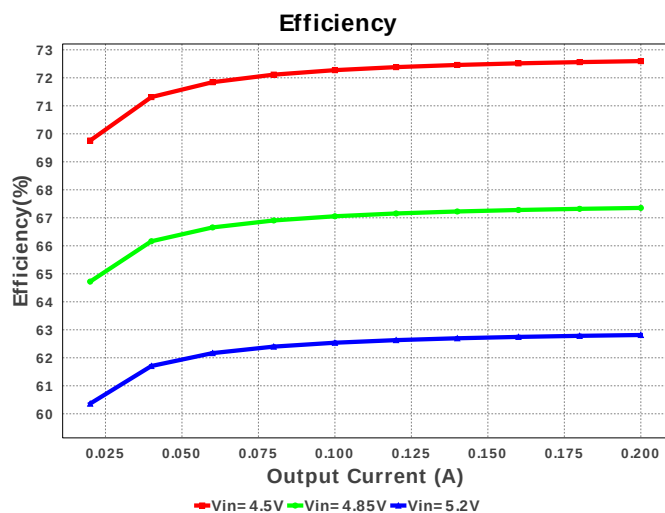
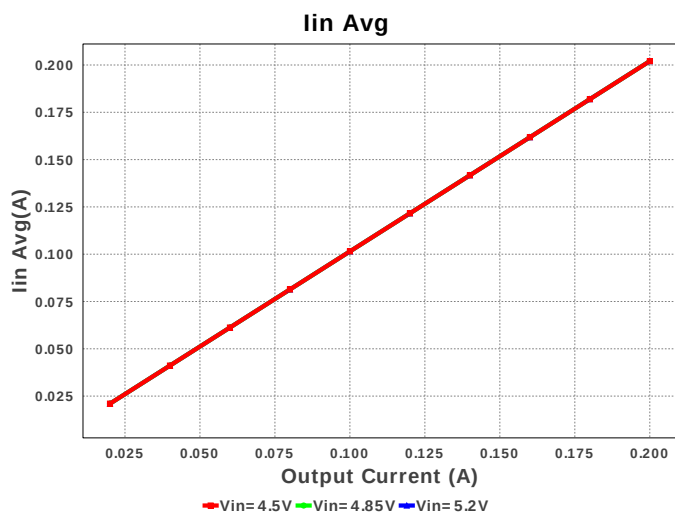
1. This regulator device is qualified for Automotive applications. All passives and other components selected in this design may not be qualified for Automotive applications. The user is required to verify that all components in the design meet the qualification and safety requirements for their specific application. View WEBENCH(R) Disclaimer.

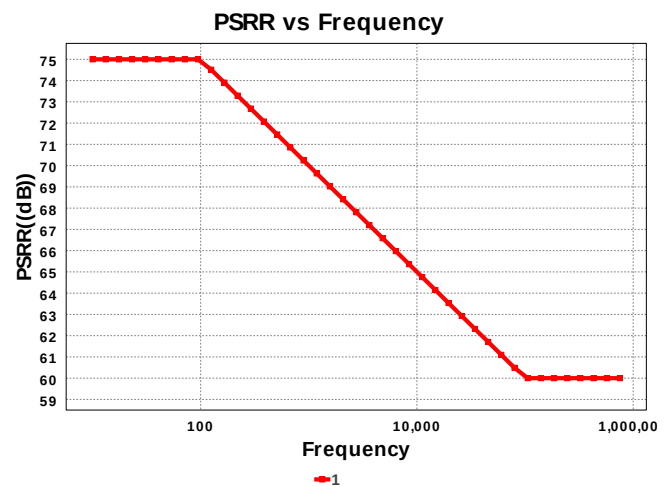
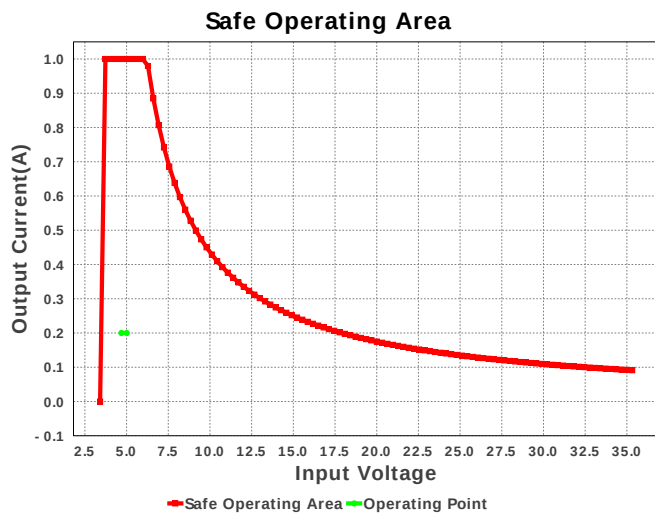
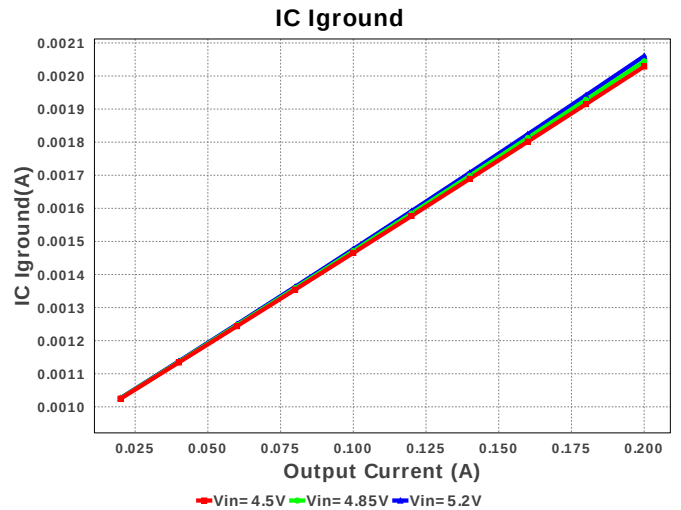
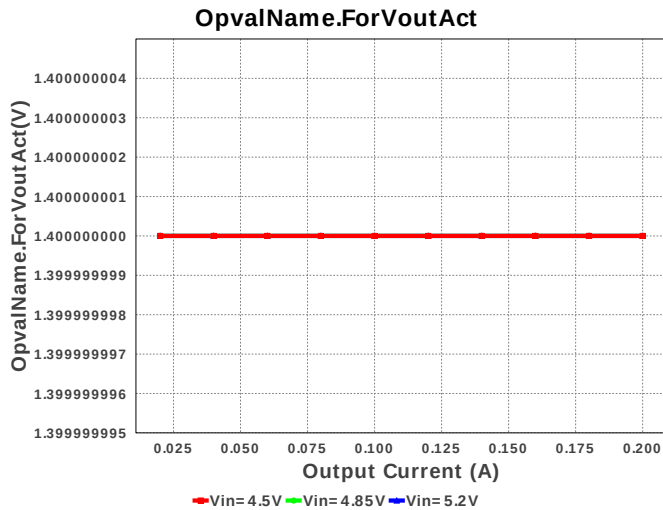
My Comments

No comments

Electrical BOM

#	Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
1.	Cin	MuRata	GRM188R60J106ME47D Series= X5R	Cap= 10.0 uF ESR= 9.0 mOhm VDC= 6.3 V IRMS= 2.74 A	1	\$0.02	0603 5 mm ²
2.	Cnr	MuRata	GRM188R60J105KA01D Series= X5R	Cap= 1.0 uF ESR= 6.065 mOhm VDC= 6.3 V IRMS= 1.36934 A	1	\$0.01	0603 5 mm ²
3.	Cout	MuRata	GRM188R60G106ME47D Series= X5R	Cap= 10.0 uF ESR= 3.0 mOhm VDC= 4.0 V IRMS= 3.0 A	1	\$0.02	0603 5 mm ²
4.	U1	Texas Instruments	TPS7A4700RGWR	Switcher	1	\$2.10	RGW0020A 49 mm ²





Operating Values

#	Name	Value	Category	Description
1.	IC Igroud	2.06 mA	Current	IC ground current
2.	Iin Avg	202.06 mA	Current	Average input current
3.	BOM Count	4	General	Total Design BOM count
4.	FootPrint	63.0 mm ²	General	Total Foot Print Area of BOM components
5.	IC Tolerance	35.0 mV	General	IC Feedback Tolerance
6.	Output Noise RMS	4.273 μ V	General	Noise RMS
7.	Pout	660.0 mW	General	Total output power
8.	Total BOM	\$2.15	General	Total BOM Cost
9.	Vin p-p	52.0 mV	Op_Point	Input Source ripple voltage
10.	Vout OP	3.3 V	Op_Point	Operational Output Voltage
11.	Efficiency	62.814 %	Op_point	Steady state efficiency
12.	IC Tj	42.698 degC	Op_point	IC junction temperature
13.	ICThetaJA	32.5 degC/W	Op_point	IC junction-to-ambient thermal resistance
14.	IOUT_OP	200.0 mA	Op_point	Iout operating point
15.	Input Ripple Frequency	100.0 kHz	Op_point	Input Source Ripple Frequency for PSRR Calculation
16.	PSRR est.	-60.0 dB	Op_point	Power Supply Rejection Ratio estimated
17.	VIN_OP	5.2 V	Op_point	Vin operating point
18.	Vout p-p	52.0 μ V	Op_point	Peak-to-peak output ripple voltage
19.	IC Pd	390.713 mW	Power	IC power dissipation
20.	Total Pd	390.713 mW	Power	Total Power Dissipation
21.	Vout Act	3.3 V		Achieved Vout with feedback resistor pair
22.	Vout Tolerance	1.061 %		Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable

Design Inputs

#	Name	Value	Description
1.	Iout	200.0 m	Maximum Output Current
2.	VinMax	5.2	Maximum input voltage
3.	VinMin	4.5	Minimum input voltage
4.	Vout	3.3	Output Voltage

#	Name	Value	Description
5.	base_pn	TPS7A4700	Base Product Number
6.	source	DC	Input Source Type
7.	Ta	30.0	Ambient temperature

Design Assistance

1. **TPS7A4700** Product Folder : <http://www.ti.com/product/tps7a47> : contains the data sheet and other resources.

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You should completely validate and test your design implementation to confirm the system functionality for your application prior to production.

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