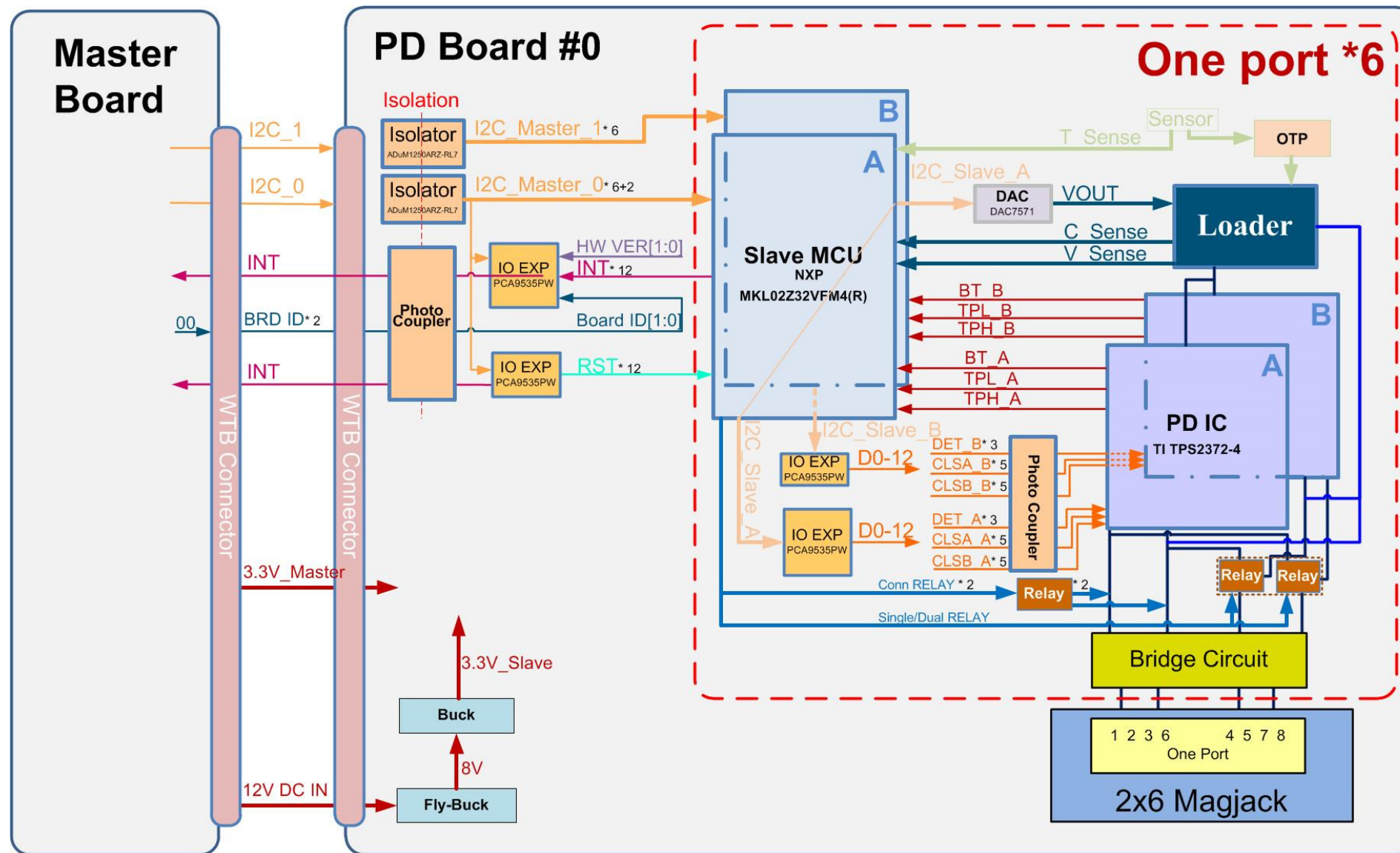
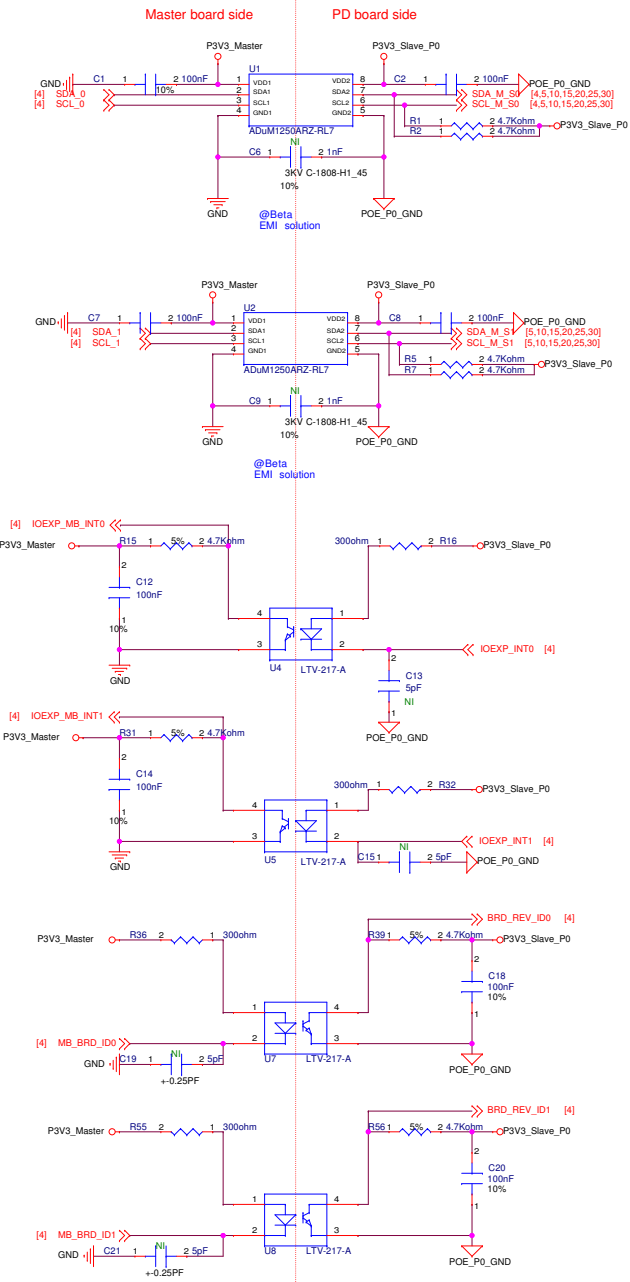




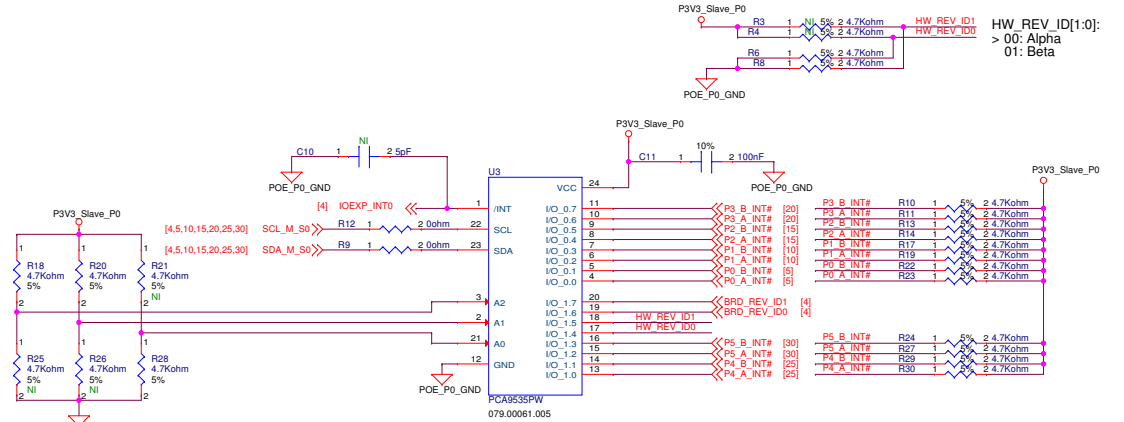
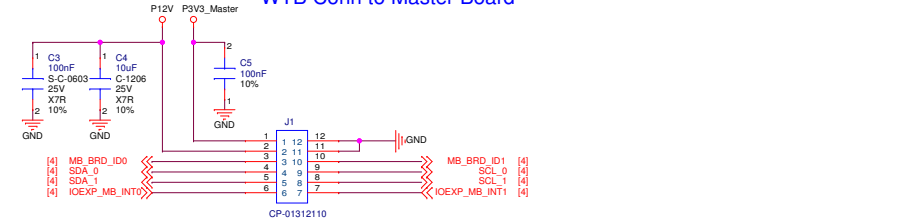
02. Block Diagram

Title		Rev	
02. Block Diagram		00	
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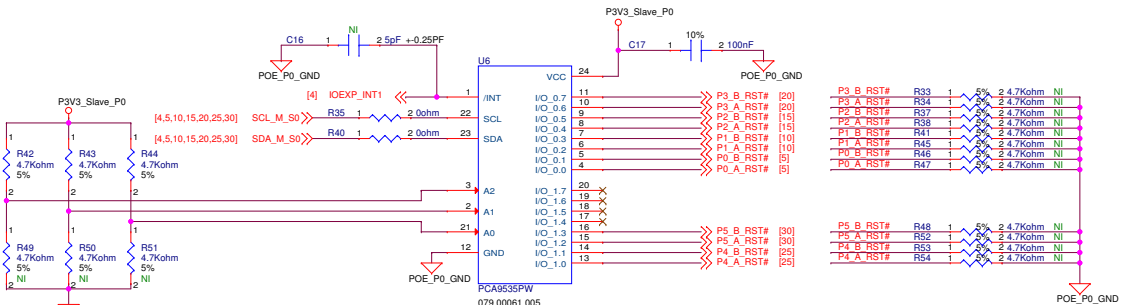
Isolation



WTB Conn to Master Board



I2C ADDRESS 0x4C: 0100 110x

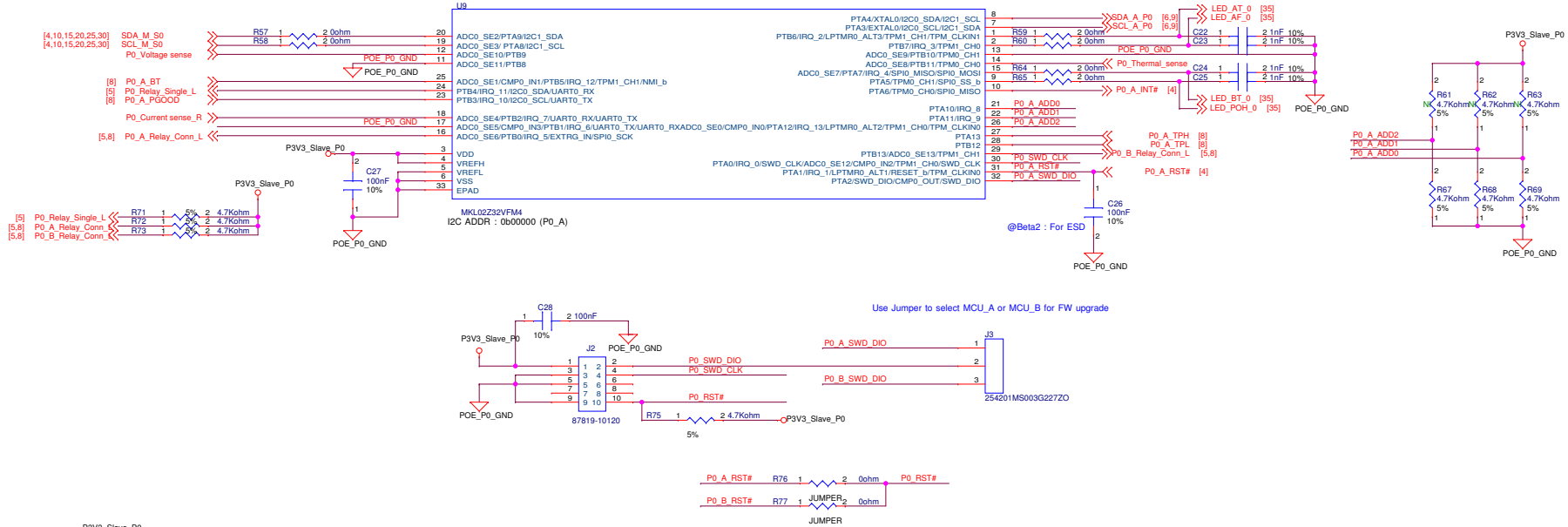


I2C ADDRESS 0x4E: 0100 111x

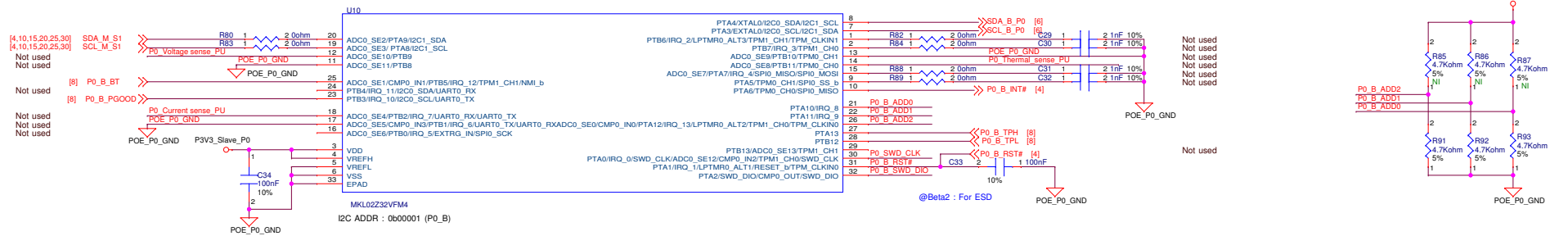
04. Isolation Circuit

Title		04. Isolation Circuit		Rev	A
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Port0_MCU_A



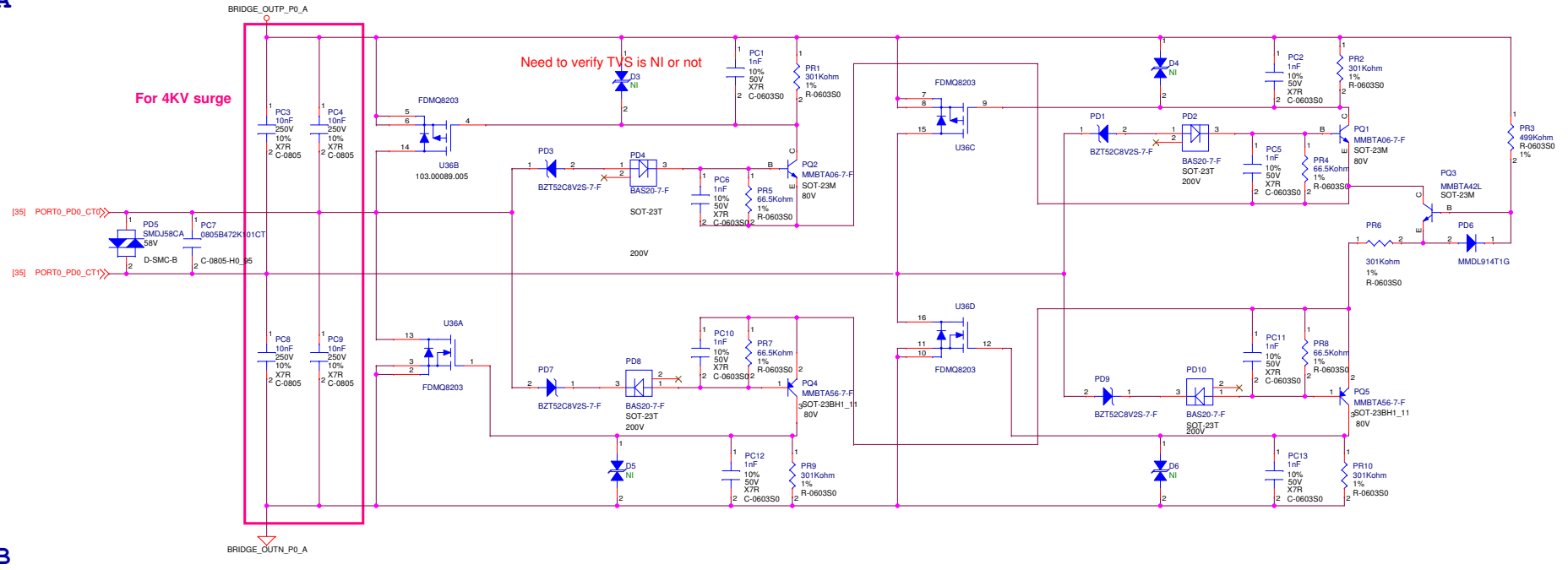
Port0_MCU_B



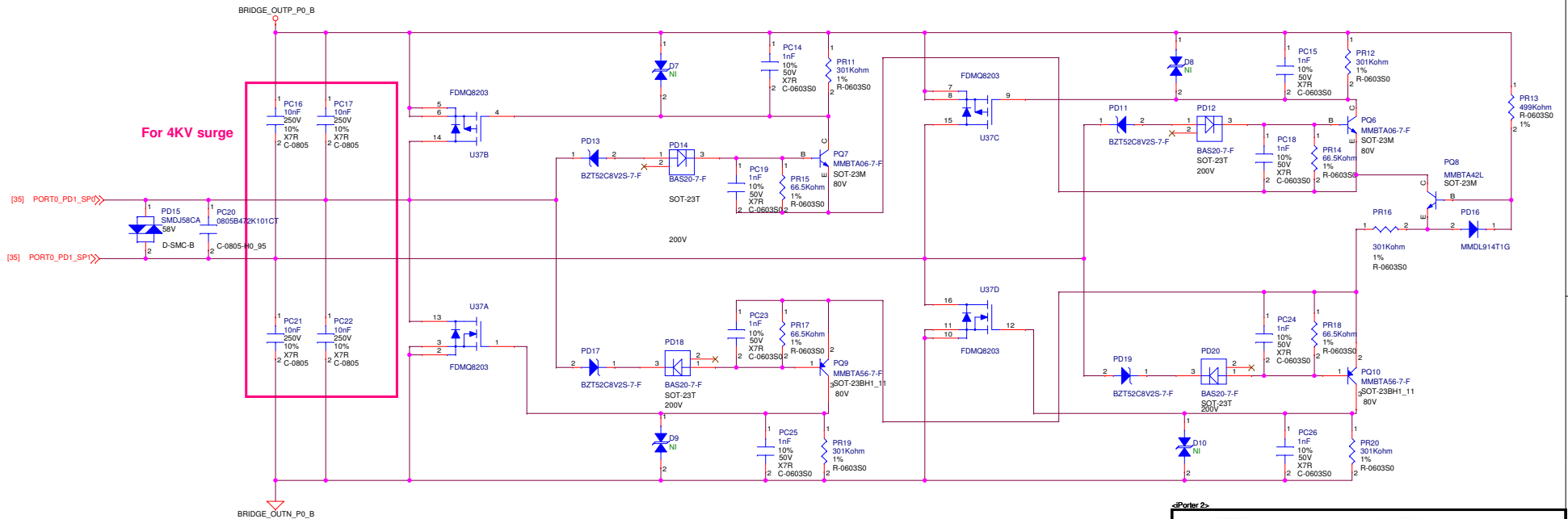
		Rev		A	
Title		05.Slave MCU_P0		00	
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				C	

POE Input

PORT0_A



PORT0_B

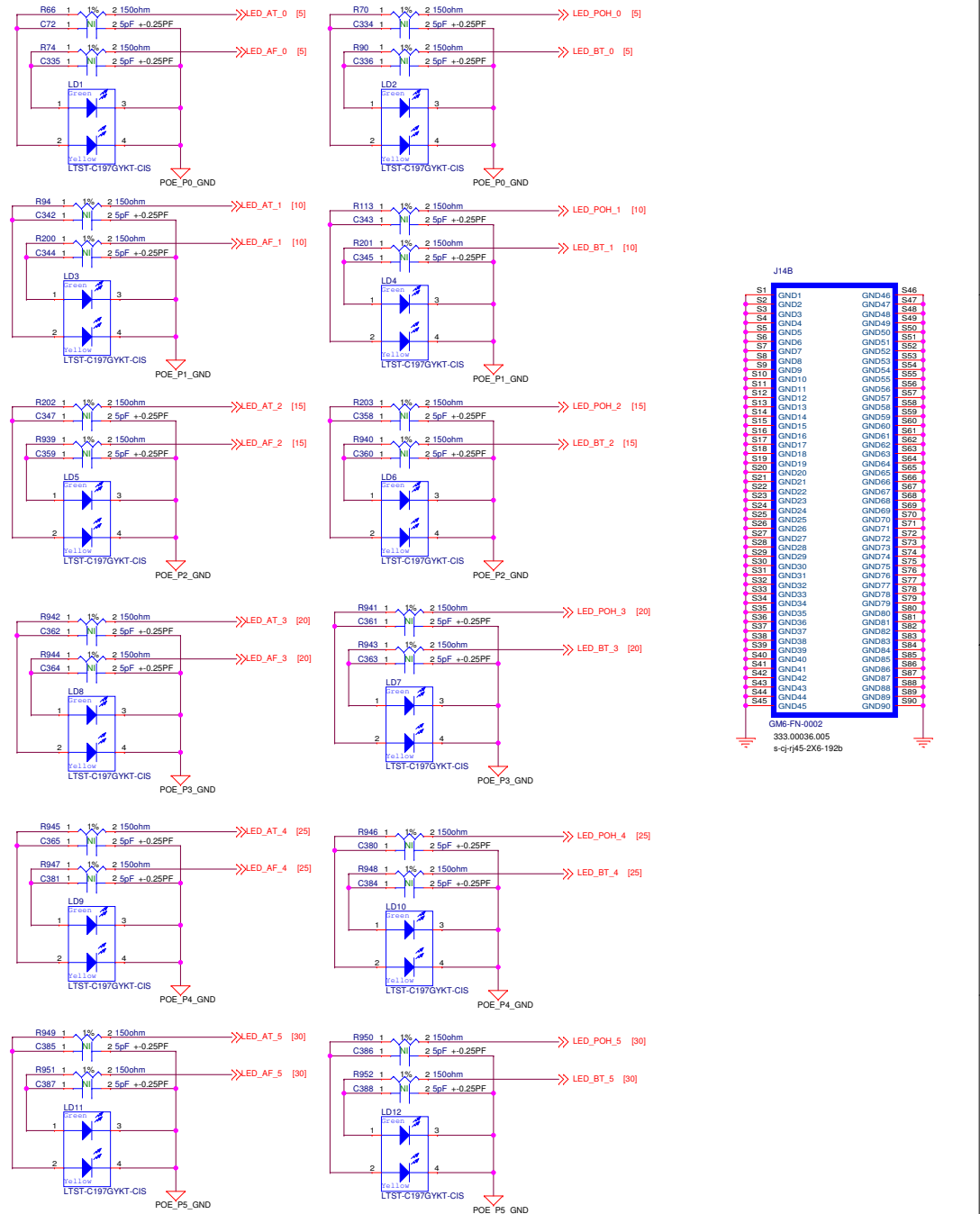
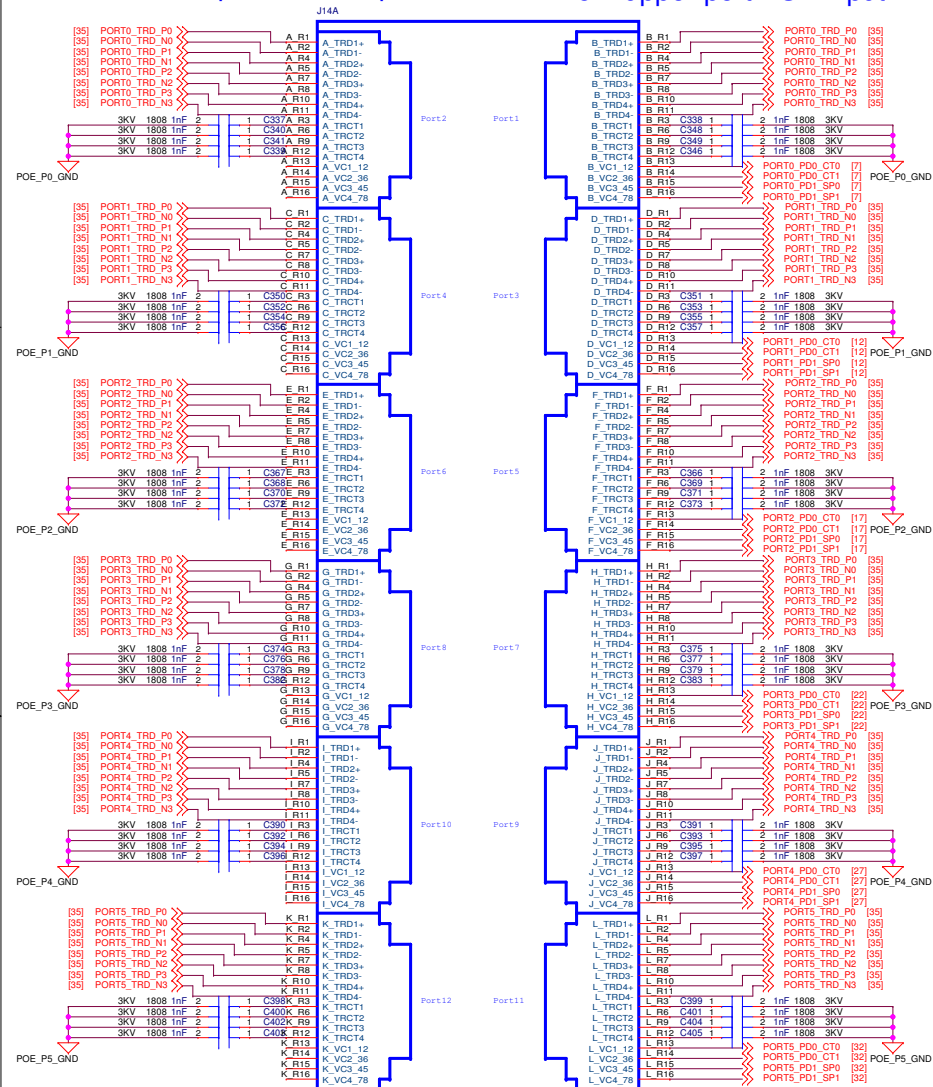


07. Bridge FEM_P0

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To bottom port data output

From upper port PSE input



35. Magjack

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