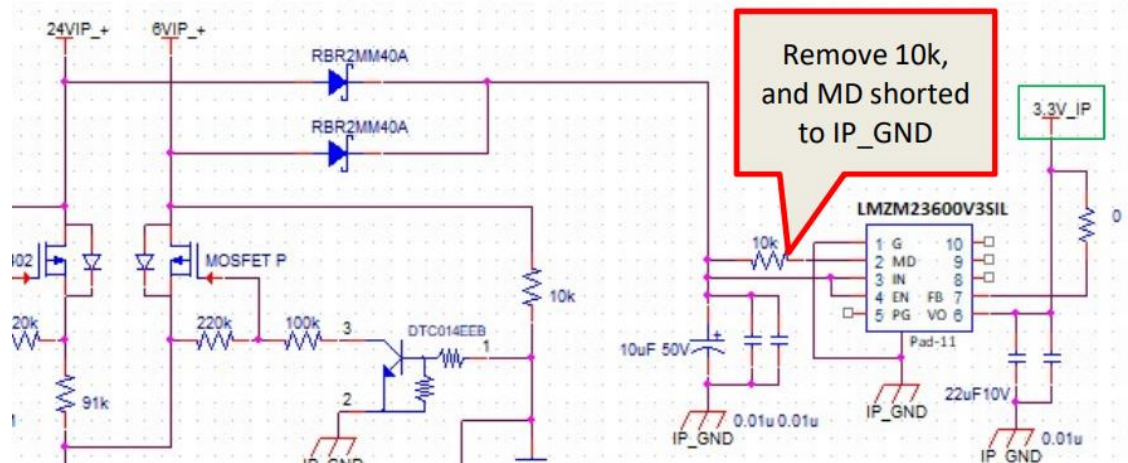
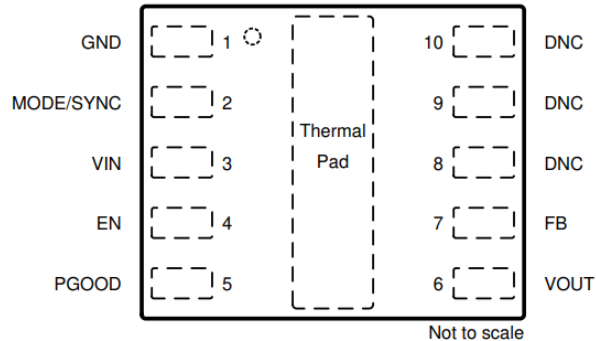


**SIL Package
10-Pin MicroSiP
Top View**



Schematic/PCB Layout Review:

1. Input capacitor placed close to VIN pin (v)
2. Output capacitor is in the minimum output capacitance 22uF, recommend increasing to recommended output capacitance 33uF
3. 10uF electrolytic bulk cap should help suppress over/undershoot of input voltage rail from 24VIP | 6VIP
4. Schematic connection looks correct, I assume the 0 ohm resistor is a placeholder for an injection resistor to measure frequency response?
5. Highlighted resistor in yellow shows the feedback pin getting pulled to GND. What is the purpose of this resistor?

