Hello Jim,

Please see my comments below.

1) I am using 220uA for Ivsl(run), is this correct? Yes

PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNITS
PROTECTION						
V_{OVP}	Over-voltage threshold	At VS input, T_J = 25°C	4.52	4.6	4.68	V
V_{OCP}	Over-current threshold	At CS input	1.4	1.5	1.6	
$I_{VSL(run)}$	VS line-sense run current	Current out of VS pin – increasing	190	220	260	µA
$I_{VSL(stop)}$	VS line-sense stop current	Current out of VS pin – decreasing	70	80	95	
K_{VSL}	VS line-sense ratio	$I_{VSL(run)}$ / $I_{VSL(stop)}$	2.5	2.8	3.05	A/A
$T_{J(stop)}$	Thermal shut-down temperature	Internal junction temperature		165		°C

2) I am using 4.05 for Vvsr, is this correct? Yes

VS INPUT						
V_{VSR}	Regulating level	Measured at no-load condition, T_J = 25°C	4.01	4.05	4.09	V
V_{VSNC}	Negative clamp level	I_{VS} = -300 µA, volts below ground	190	250	325	mV
I_{VSB}	Input bias current	V_{VS} = 4 V	–0.25	0	0.25	µA

3) I am dividing auxilliary turns by secondary turns for Nas, is this correct? Yes
   (Vout+Vf)*Na/Ns = Vaux
4) I am designing a 24V output, is Vocv 24?
   Vocv is defined as the converter regulated output voltage so you are correct.

Regards,

Mike