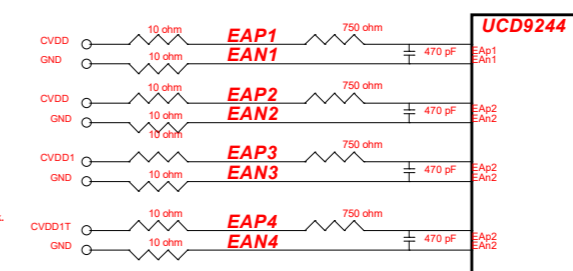


### SOC DSP Vcore @20A



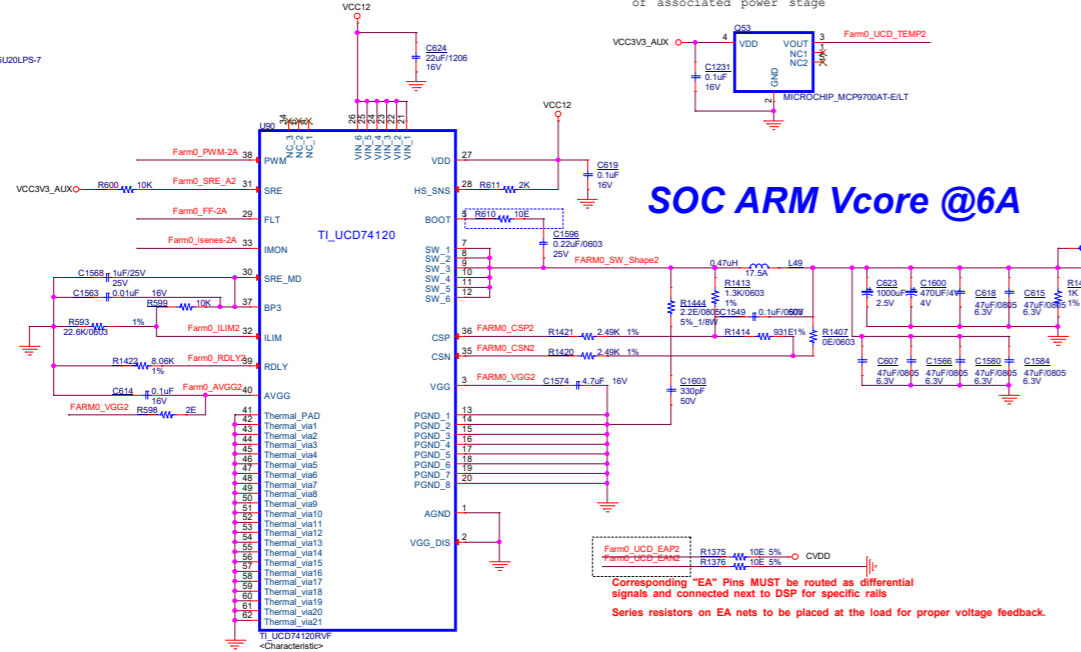
Series resistors on EA nets to be placed at the load for proper voltage feedback.

### 0.95V @5A DSP Fixed Core Supply

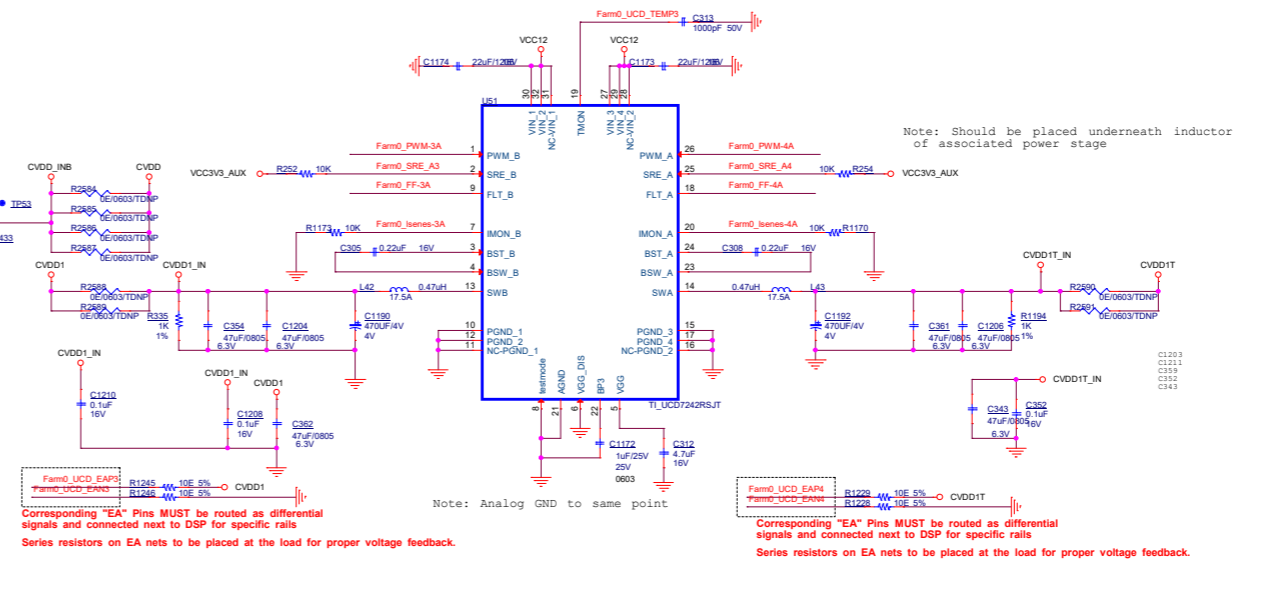
### 0.95V @5A ARM Fixed Core Supply

### PMBus Address Bins

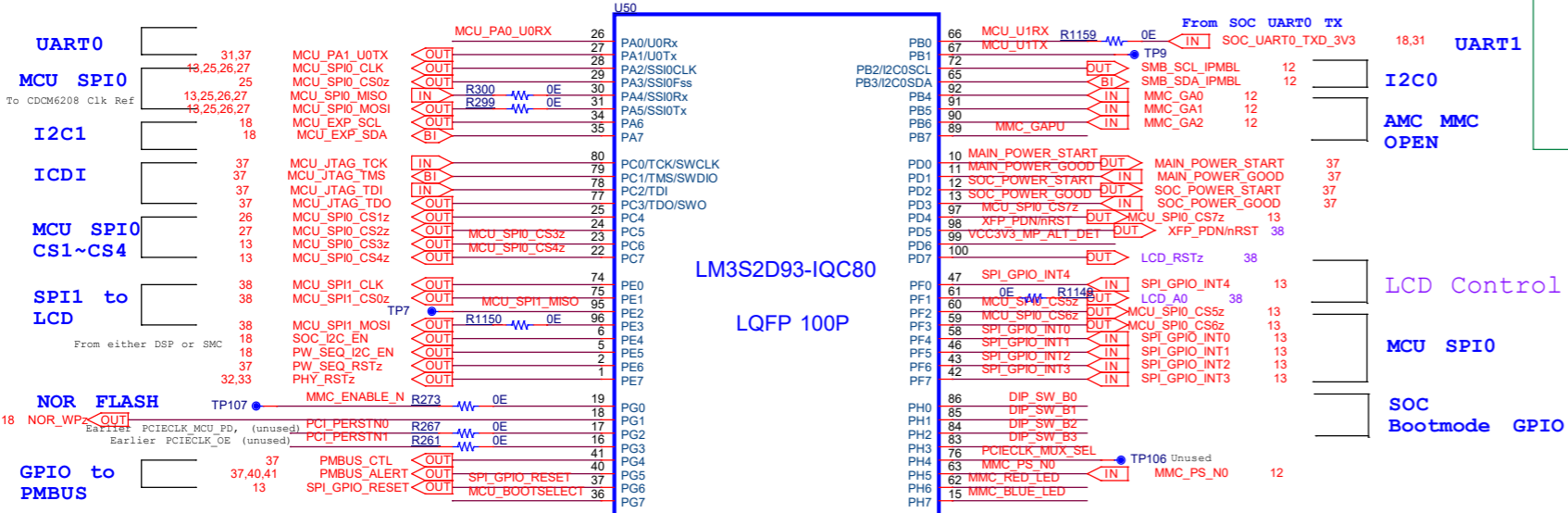
PMBus Address	PMBus RESISTANCE ( K ohm )
OPEN	--
11	205
10	178
9	154
8	133
7	115
6	100
5	86.6
4	75
3	64.9
2	56.2
1	48.7
0	42.2
SHORT	--



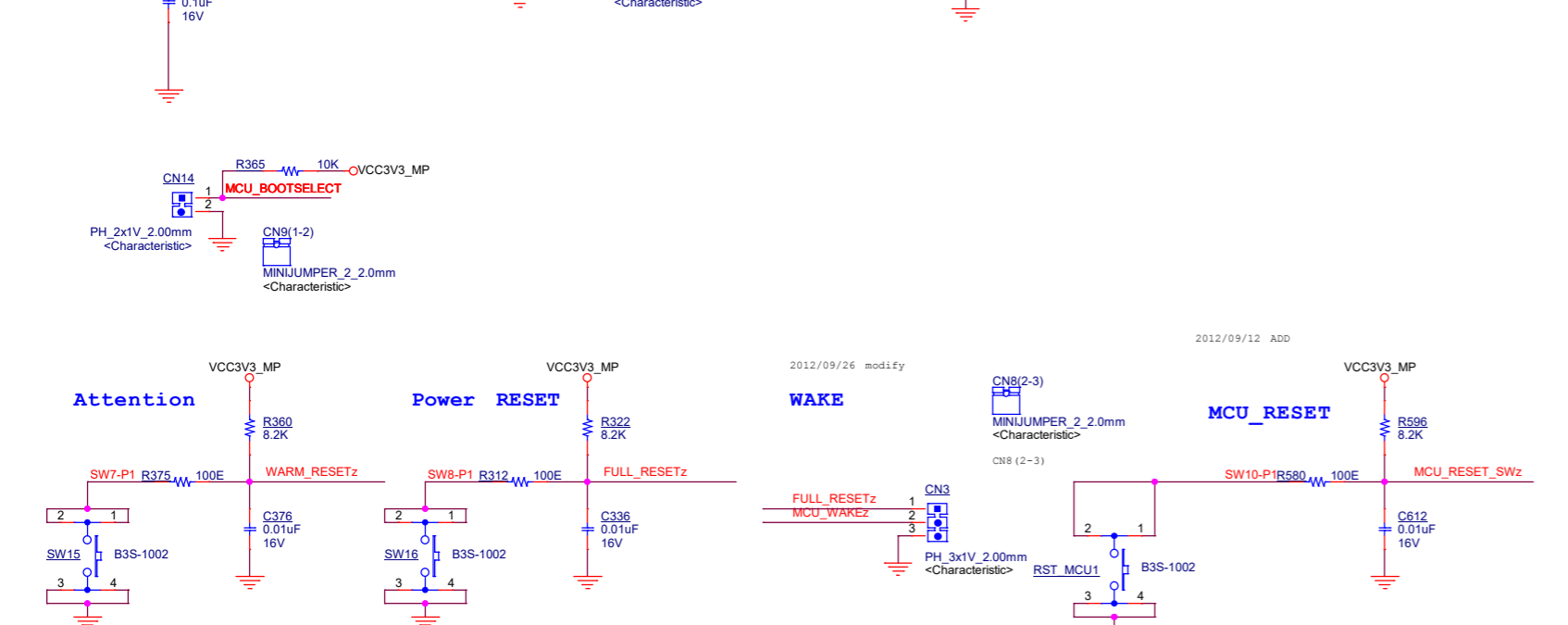
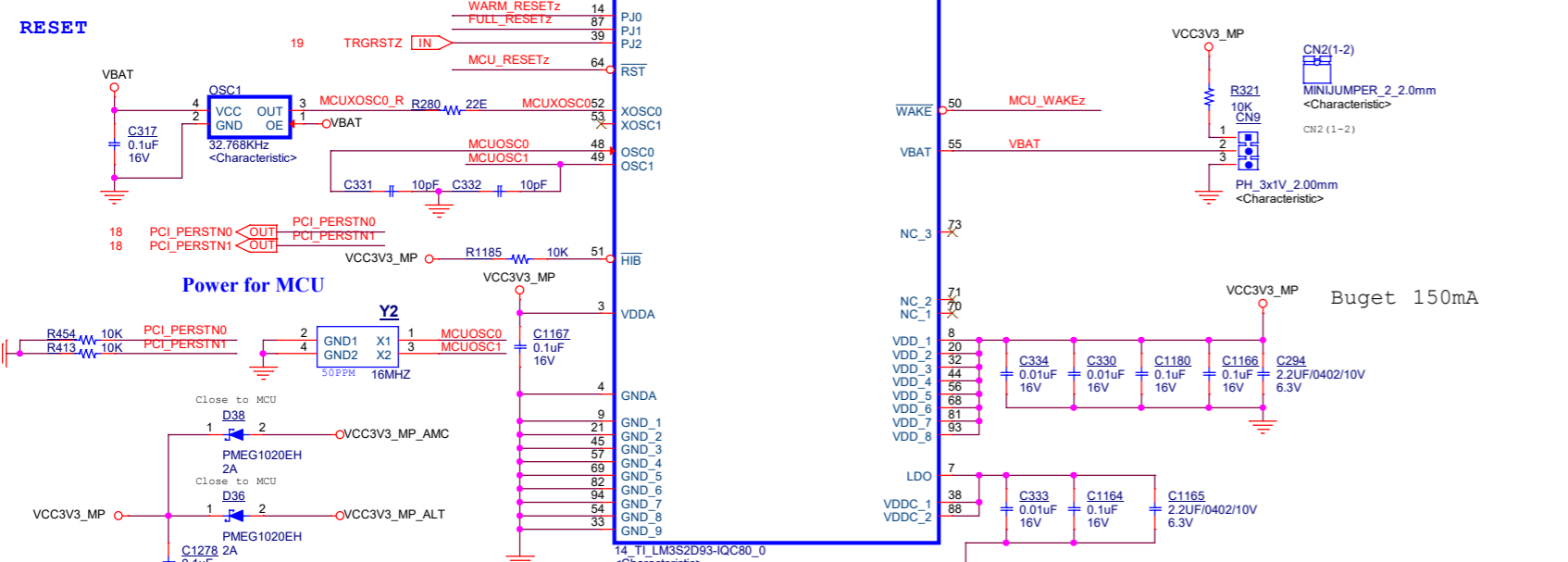
### SOC ARM Vcore @6A



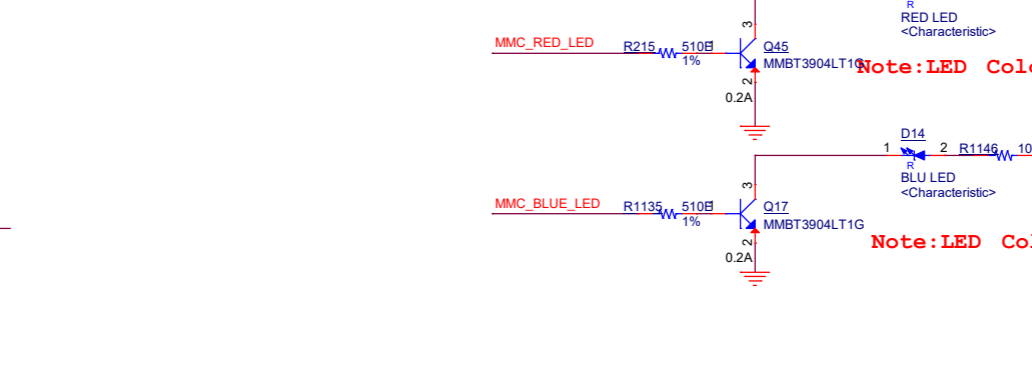
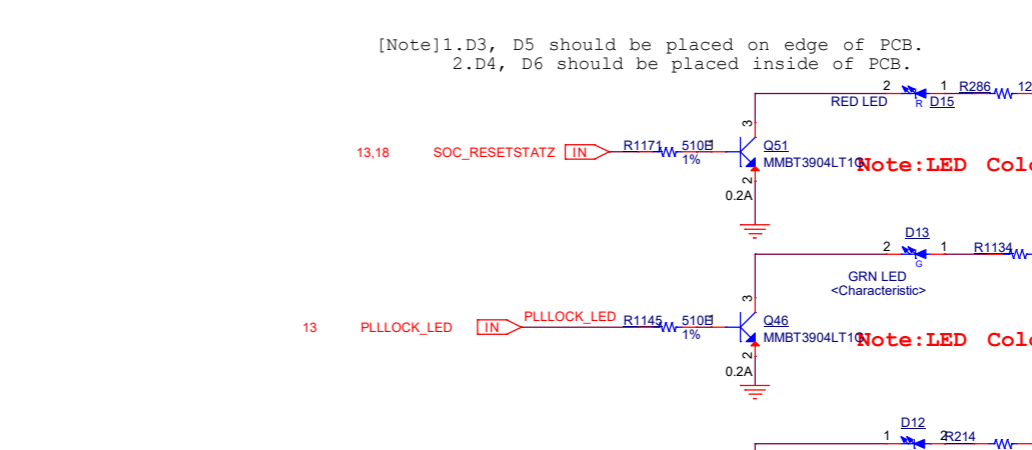
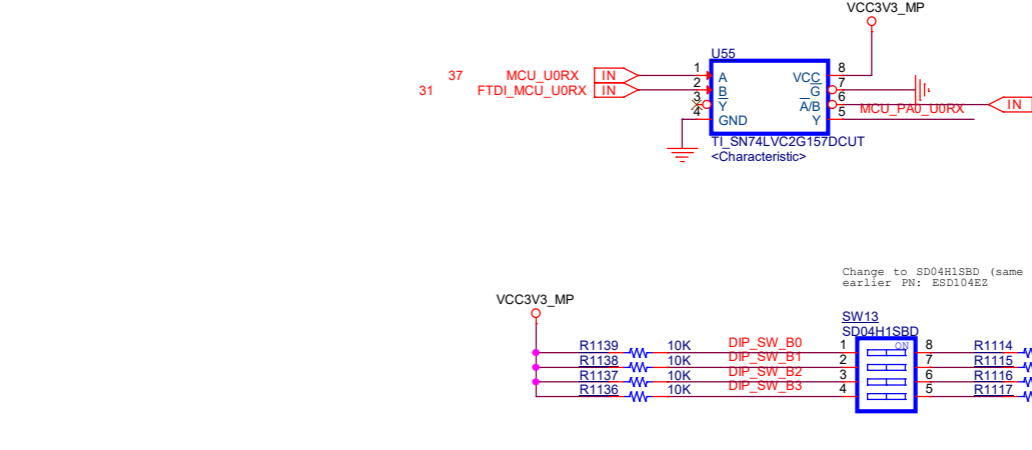
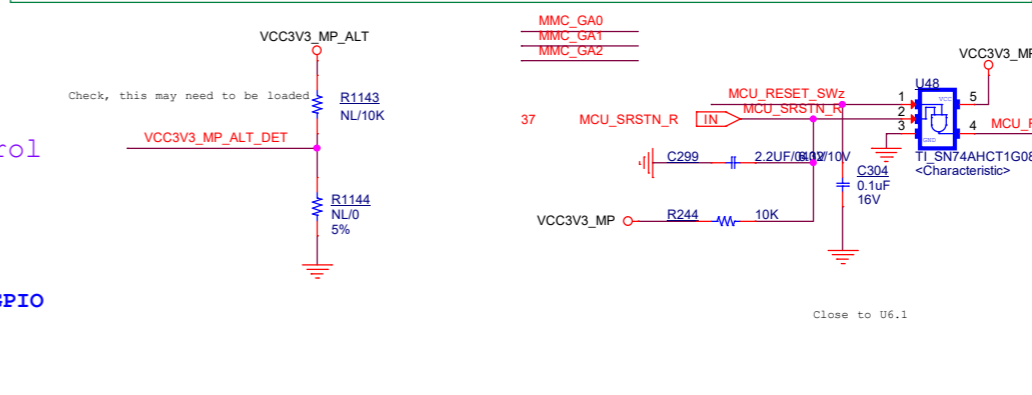
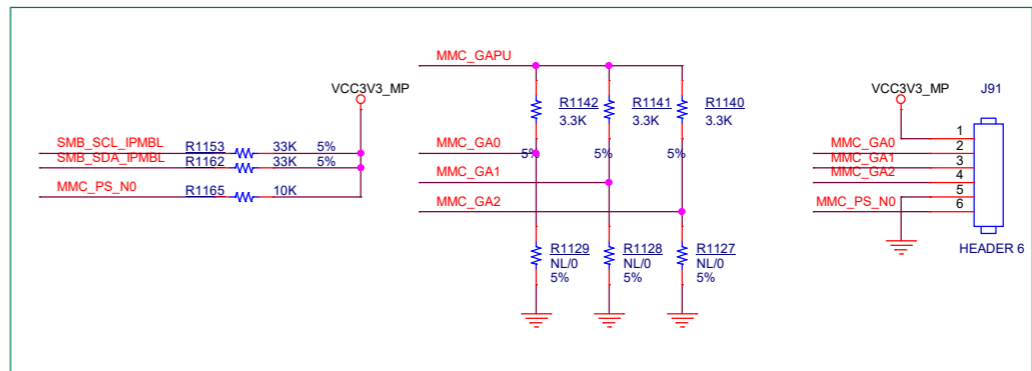
### SOC ARM Vcore @6A



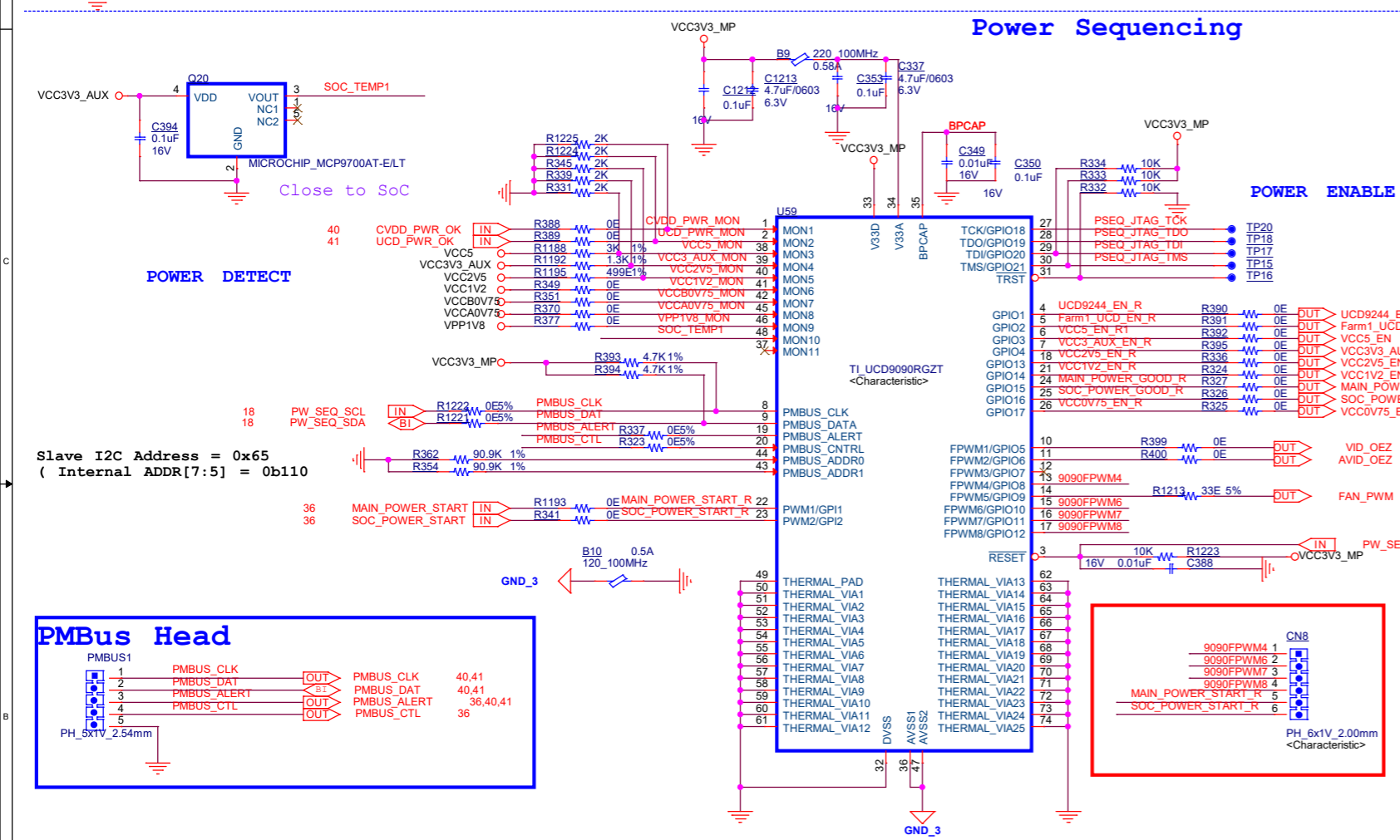
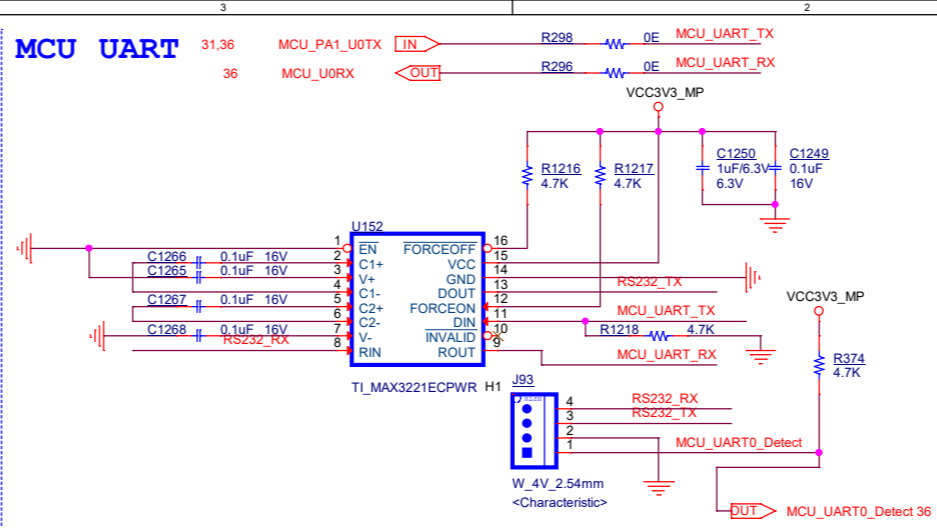
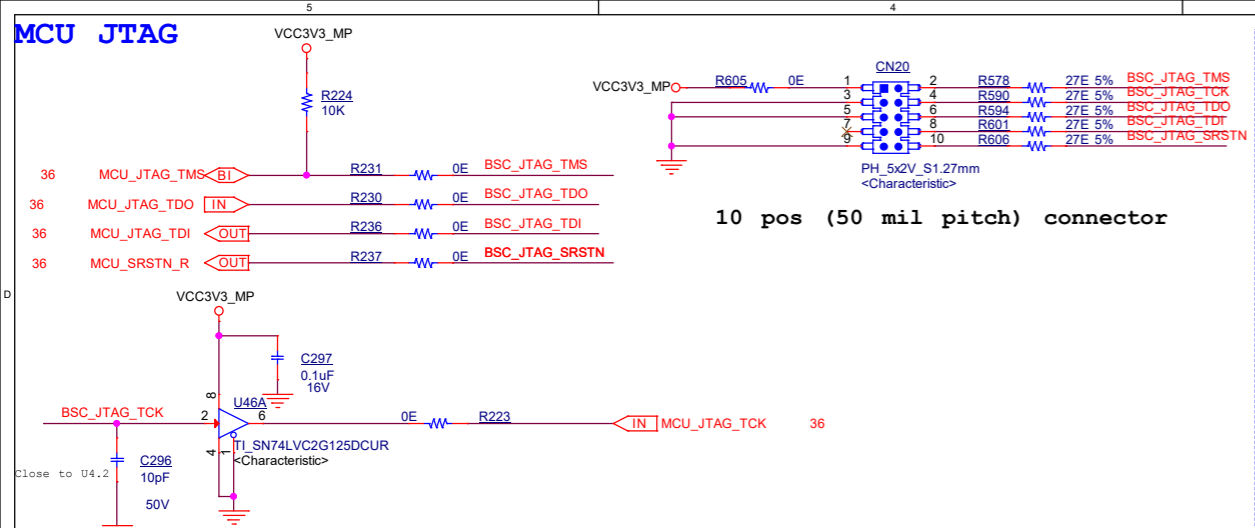
LM3S2D93-IQC80  
LQFP 100P



[Note]RST\_MCU1 should be placed inside of PCB



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### PMBus Address Bins

PMBus Address	PMBus RESISTANCE ( K ohm )
OPEN	--
11	200
10	154
9	118
8	90.9
7	69.8
6	53.6
5	41.2
4	31.6
SHORT	--

