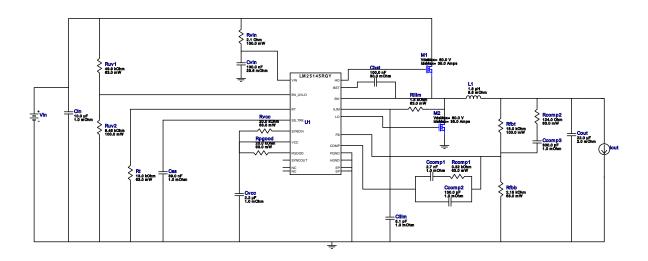
VinMin = 10.0V VinMax = 26.0V Vout = 7.5V Iout = 8.0A Device = LM25145RGYR Topology = Buck Created = 2022-02-15 03:15:32.221 BOM Cost = \$3.15 BOM Count = 25 Total Pd = 3.48W

WEBENCH® Design Report

Design: 14 LM25145RGYR LM25145RGYR 10V-26V to 7.50V @ 8A

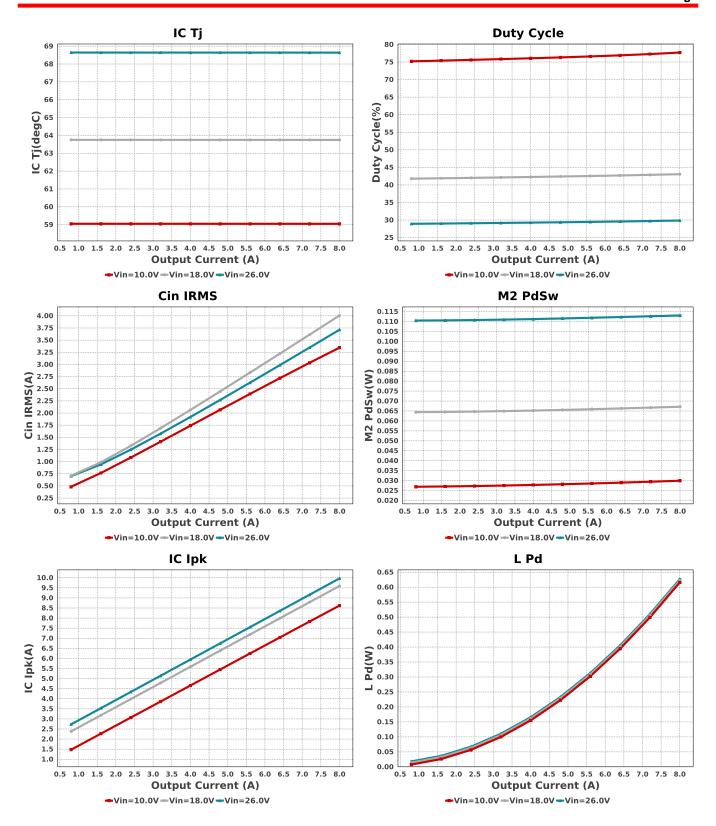


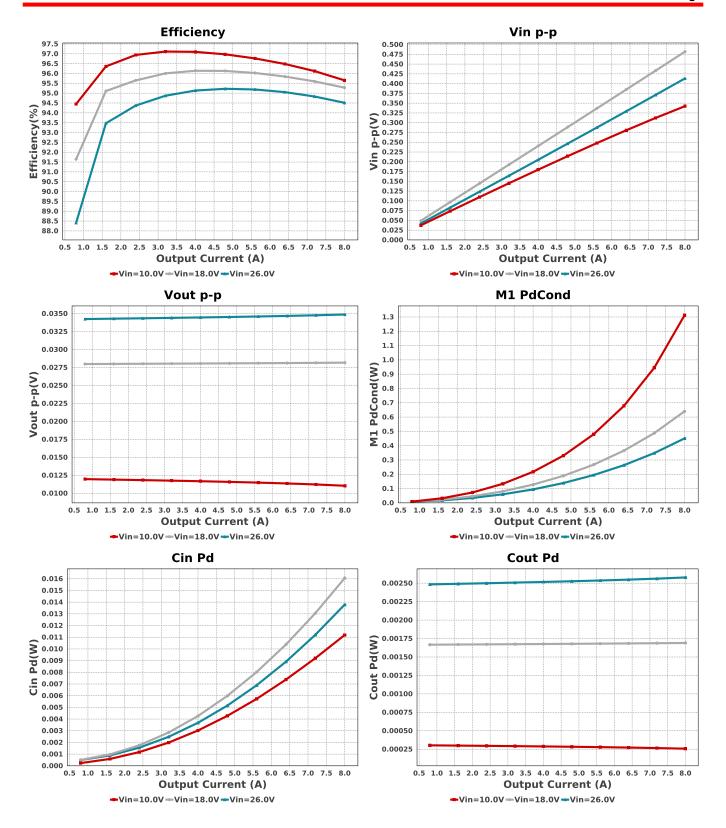
Electrical BOM

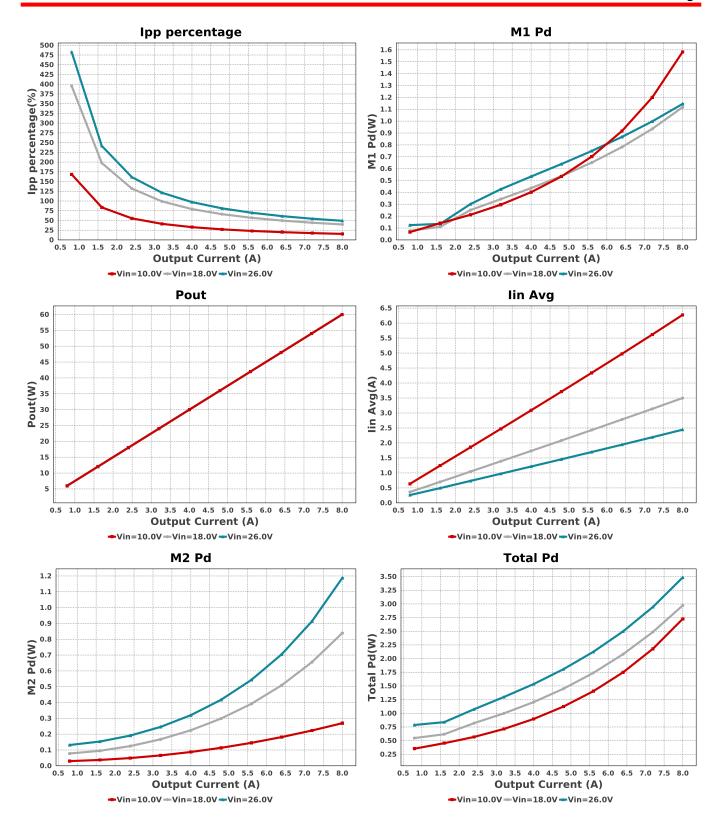
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cbst	MuRata	GRM188R61E104KA01D Series= X5R	Cap= 100.0 nF ESR= 30.0 mOhm VDC= 25.0 V IRMS= 0.0 A	1	\$0.01	0603 5 mm ²
Ccomp1	MuRata	GRM155R71H272KA01D Series= X7R	Cap= 2.7 nF ESR= 1.0 mOhm VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	0402 3 mm ²
Ccomp2	MuRata	GRM1555C1H131JA01D Series= C0G/NP0	Cap= 130.0 pF ESR= 1.0 mOhm VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	0402 3 mm ²
Ccomp3	MuRata	GRM1555C1H301JA01D Series= C0G/NP0	Cap= 300.0 pF ESR= 1.0 mOhm VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	0402 3 mm ²
Cilim	MuRata	GQM2195C2A5R1CB01D Series= C0G/NP0	Cap= 5.1 pF ESR= 1.0 mOhm VDC= 100.0 V IRMS= 0.0 A	1	\$0.15	0805 7 mm ²
Cin	TDK	C3225X7R1H106M250AC Series= X7R	Cap= 10.0 uF ESR= 1.0 mOhm VDC= 50.0 V IRMS= 5.0 A	1	\$0.28	1210 15 mm ²
Cout	MuRata	GRM32ER61C226ME20L Series= X5R	Cap= 22.0 uF ESR= 2.0 mOhm VDC= 16.0 V IRMS= 3.68 A	1	\$0.55	1210 15 mm ²
Css	MuRata	GRM155R61A393KA01D Series= X5R	Cap= 39.0 nF ESR= 1.0 mOhm VDC= 10.0 V IRMS= 0.0 A	1	\$0.01	0402 3 mm ²

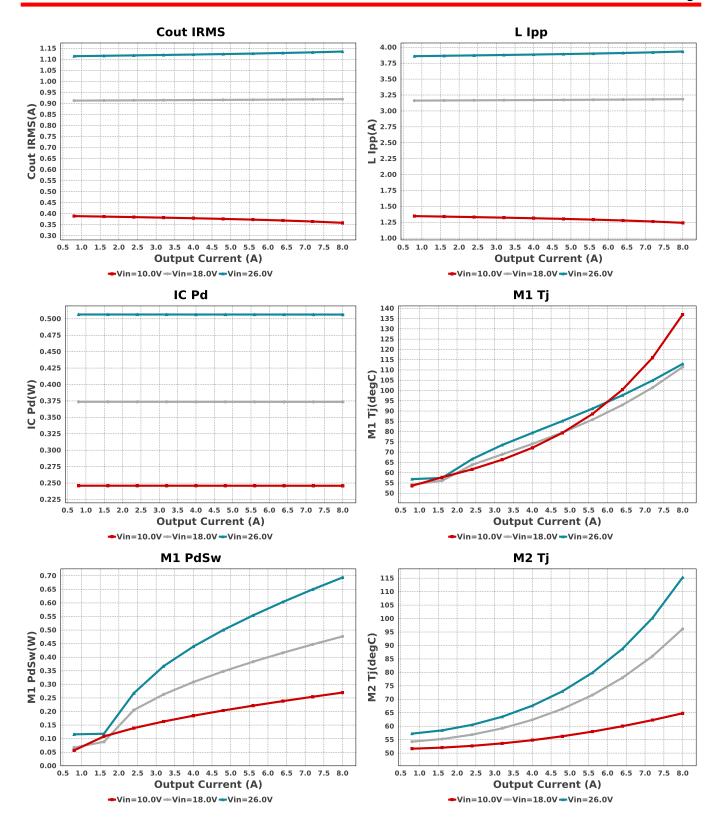
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cvcc	TDK	C1005X5R1V225K050BC Series= X5R	Cap= 2.2 uF ESR= 1.0 mOhm VDC= 35.0 V IRMS= 0.0 A	1	\$0.07	0402_065 3 mm ²
Cvin	TDK	CGA3E2X7R1H104K080AA Series= X7R	Cap= 100.0 nF ESR= 29.6 mOhm VDC= 50.0 V IRMS= 971.99 mA	1	\$0.01	0603 5 mm ²
L1	Coilcraft	XAL6030-182MEB	L= 1.8 μH 9.6 mOhm	1	\$0.65	XAL6030 72 mm ²
M1	Texas Instruments	CSD18543Q3A	VdsMax= 60.0 V IdsMax= 35.0 Amps	1	\$0.26	DNH0008A 18 mm ²
M2	Texas Instruments	CSD18543Q3A	VdsMax= 60.0 V IdsMax= 35.0 Amps	1	\$0.26	DNH0008A 18 mm ²
Rcomp1	Vishay-Dale	CRCW04023K32FKED Series= CRCWe3	Res= 3.32 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
Rcomp2	Vishay-Dale	CRCW0402124RFKED Series= CRCWe3	Res= 124.0 Ohm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
Rfbb	Vishay-Dale	CRCW04022K15FKED Series= CRCWe3	Res= 2.15 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
Rfbt	Yageo	RC0603FR-0718KL Series= ?	Res= 18.0 kOhm Power= 100.0 mW Tolerance= 1.0%	1	\$0.01	0603 5 mm ²
Rilim	Vishay-Dale	CRCW04021K50FKED Series= CRCWe3	Res= 1.5 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
Rpgood	Vishay-Dale	CRCW040220K0FKED Series= CRCWe3	Res= 20.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
Rt	Vishay-Dale	CRCW040213K0FKED Series= CRCWe3	Res= 13.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
Ruv1	Vishay-Dale	CRCW040249K9FKED Series= CRCWe3	Res= 49.9 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
Ruv2	Yageo	RC0603FR-078K45L Series=?	Res= 8.45 kOhm Power= 100.0 mW Tolerance= 1.0%	1	\$0.01	0603 5 mm ²
Rvcc	Vishay-Dale	CRCW040220K0FKED Series= CRCWe3	Res= 20.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
Rvin	Vishay-Dale	CRCW06032R10FKEA Series= CRCWe3	Res= 2.1 Ohm Power= 100.0 mW Tolerance= 1.0%	1	\$0.01	0603 5 mm ²
U1	Texas Instruments	LM25145RGYR	Switcher	1	\$0.76	

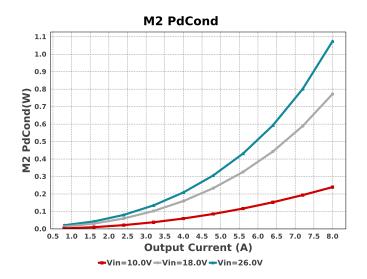
RGY0020B 25 mm²

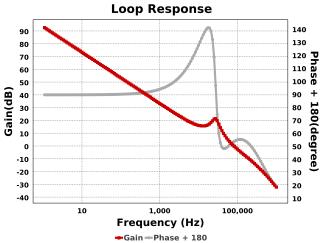












Operating Values

#	Name	Value	Category	Description
1.	Cin IRMS	3.713 A	Capacitor	Input capacitor RMS ripple current
2.	Cin Pd	13.789 mW	Capacitor	Input capacitor power dissipation
3.	Cout IRMS	1.135 A	Capacitor	Output capacitor RMS ripple current
4.	Cout Pd	2.578 mW	Capacitor	Output capacitor power dissipation
5.	IC lpk	9.967 A	IC	Peak switch current in IC
6.	IC Pd	506.47 mW	IC	IC power dissipation
7.	IC Tj	67.625 degC	IC	IC junction temperature
8.	IC Tolerance	8.0 mV	IC	IC Feedback Tolerance
9.	ICThetaJA	34.8 degC/W	IC	IC junction-to-ambient thermal resistance
10.	lin Avg	2.442 A	IC	Average input current
11.	lpp percentage	49.165 %	Inductor	Inductor ripple current percentage (with respect to average inductor current)
12.	L lpp	3.933 A	Inductor	Peak-to-peak inductor ripple current
	L Pd	626.78 mW	Inductor	Inductor power dissipation
	M1 Pd	1.144 W	Mosfet	M1 MOSFET total power dissipation
	M1 PdCond	450.99 mW	Mosfet	M1 MOSFET conduction losses
16.		693.53 mW	Mosfet	M1 MOSFET switching losses
17.		112.95 degC	Mosfet	M1 MOSFET junction temperature
18.	•	1.187 W	Mosfet	M2 MOSFET total power dissipation
19.		1.074 W	Mosfet	M2 MOSFET conduction losses
	M2 PdSw	113.02 mW	Mosfet	M2 MOSFET switching losses
	M2 T dow	115.28 degC	Mosfet	M2 MOSFET junction temperature
22.	Cin Pd	13.789 mW	Power	Input capacitor power dissipation
	Cout Pd	2.578 mW	Power	Output capacitor power dissipation
-	IC Pd	506.47 mW	Power	IC power dissipation
	L Pd	626.78 mW	Power	Inductor power dissipation
26.	M1 Pd	1.144 W	Power	M1 MOSFET total power dissipation
27.	M1 PdCond	450.99 mW	Power	M1 MOSFET conduction losses
28.		693.53 mW	Power	M1 MOSFET switching losses
20. 29.	M2 Pd	1.187 W	Power	M2 MOSFET total power dissipation
30.	M2 PdCond	1.074 W	Power	M2 MOSFET conduction losses
	M2 PdSw	1.074 W 113.02 mW	Power	M2 MOSFET conduction losses M2 MOSFET switching losses
31. 32.	Total Pd	3.484 W	Power	3
-				Total Power Dissipation
33.	BOM Count	25	System	Total Design BOM count
2.4	Duty Cycle	20.06.6/	Information	Duty avala
34.	Duty Cycle	29.86 %	System	Duty cycle
25	Γ <i>t</i> :-:	04.540.0/	Information	Chandy state officionary
35.	Efficiency	94.512 %	System	Steady state efficiency
00	E(D-)-/	2	Information	Total Foot Print Associate DOM as
36.	FootPrint	232.0 mm ²	System	Total Foot Print Area of BOM components
o=	_	700 004 111	Information	
37.	Frequency	769.231 kHz	System	Switching frequency
			Information	
38.	lout	8.0 A	System	lout operating point
			Information	
39.	Mode	FCCM	System	Conduction Mode
			Information	
40.	Pout	60.0 W	System	Total output power
			Information	
41.	Total BOM	\$3.15	System	Total BOM Cost
→ 1.				

#	Name	Value	Category	Description
42.	Vin	26.0 V	System Information	Vin operating point
43.	Vin p-p	411.267 mV	System Information	Peak-to-peak input voltage
44.	Vout	7.5 V	System Information	Operational Output Voltage
45.	Vout Actual	7.498 V	System Information	Vout Actual calculated based on selected voltage divider resistors
46.	Vout Tolerance	2.823 %	System Information	Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable
47.	Vout p-p	34.875 mV	System Information	Peak-to-peak output ripple voltage

Design Inputs

Name	Value	Description	
lout	8.0	Maximum Output Current	
SoftStart	3.0 ms	Soft Start Time (ms)	
VinMax	26.0	Maximum input voltage	
VinMin	10.0	Minimum input voltage	
Vout	7.5	Output Voltage	
base_pn	LM25145	Base Product Number	
source	DC	Input Source Type	
Та	50.0	Ambient temperature	
UserFsw	769.0 k	Customer Selected Frequency	

WEBENCH® Assembly

Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of Cin and Cout, and the inductance and DC resistance of L1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

Soldering Component to Board

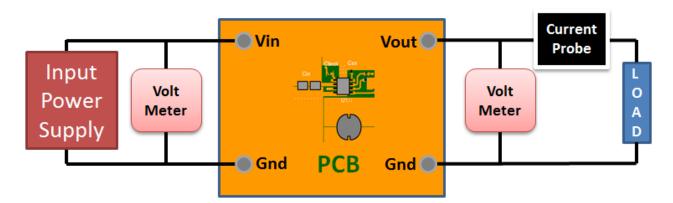
If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab town to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 10.0V and set the input supply's current limit to zero. With the input supply off connect up the input supply to Vin and GND. Connect a digital volt meter and a load if needed to set the minimum lout of the design from Vout and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between Vin and GND, a load is connected between Vout and GND and a current meter is connected in series between Vout and the load. The load must be able to handle at least rated output power + 50% (7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.



Design Assistance

- 1. Master key: 8A757A98C798F1C2[v1]
- 2. LM25145 Product Folder: http://www.ti.com/product/lm25145: contains the data sheet and other resources.

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