Hello.

I am evaluating LM 27403.

With respect to PGOODpin, phenomena of outputting
0 V → 1 V at the time of starting is occurring.
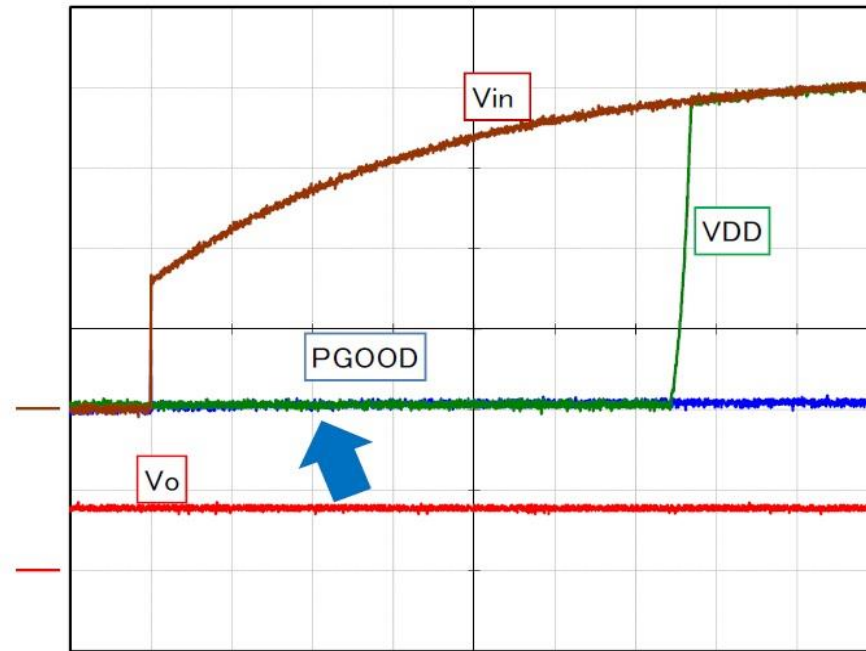
The phenomenon is occurring on the board we created,

It also occurs in TI LM27403EVM.
The waveform is evaluated by placing it on the LM 27403 EVM.

The pull-up of the PGOOD pin is changed from VDD to Vin.
(Rpg: changed from 20 KΩ to 80 KΩ)

what is the reason?

Thank you.

Good IC
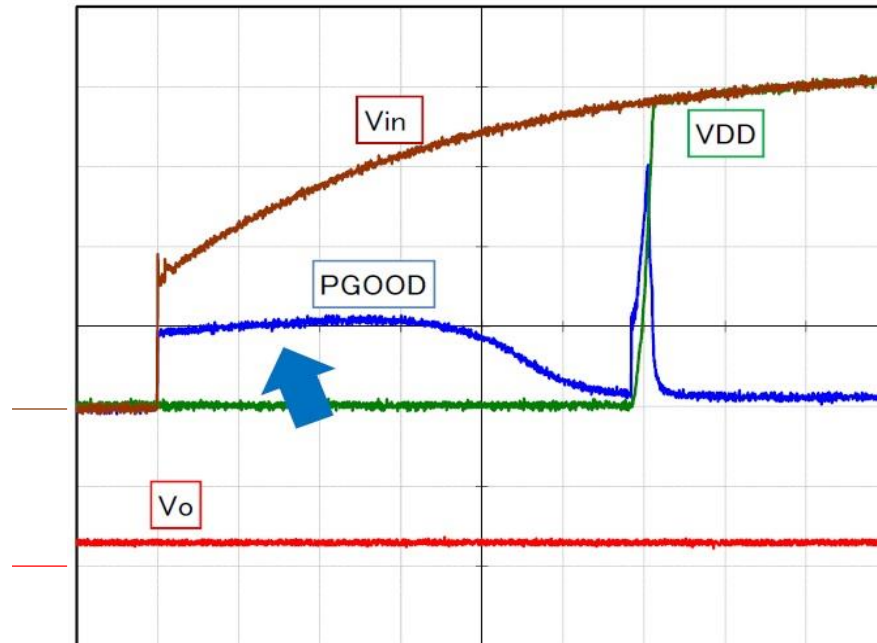
	Scale	Bw
CH1	500 mV/div	20MHz
CH2	1 V/div	20MHz
CH3	1 V/div	20MHz
CH4	1 V/div	20MHz
REF1	–	–
REF2	–	–
REF3	–	–
REF4	–	–
MATH	–	–

TIME	10 ms/div
Mode	Normal

Faulty operation IC

	Scale	Bw
CH1	500 mV/div	20MHz
CH2	1 V/div	20MHz
CH3	1 V/div	20MHz
CH4	1 V/div	20MHz
REF1	–	–
REF2	–	–
REF3	–	–
REF4	–	–
MATH	–	–

TIME	10 ms/div
Mode	Normal

8.1 Schematic

Figure 25. Schematic