We are creating a board for LM 27403.

In the board operation evaluation,
the phenomenon like PGOODpin 's action is occurring on the right side.

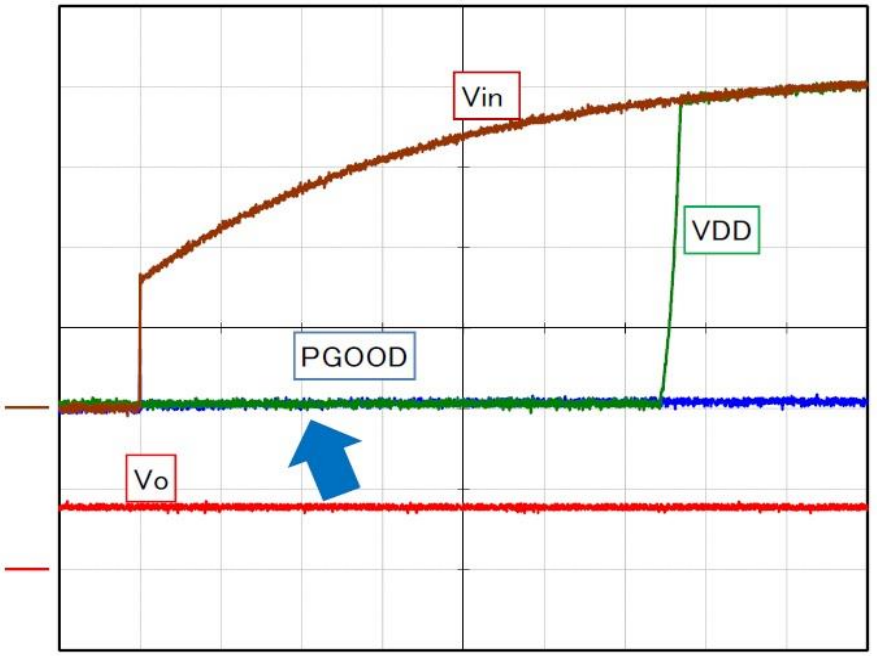
At startup 1 V is generated on the PGOOD pin.

This phenomenon also occurs in TI's LM 27403 EVM.
The waveform on the right is the waveform of the LM 27403 EVM.
The pull-up of the PGOOD pin is changed from VDD to Vin.
(Rpg: changed from 20 KΩ to 80 KΩ)

Our system may be configured as shown on page 3.

At this time, the pull-up of PGOOD is supplied from another power supply.
1.2 V power supply is not turned on.
However,
it seems that the PG voltage is output 1 V and the boosted 3.3 V power supply ramps up for a moment.

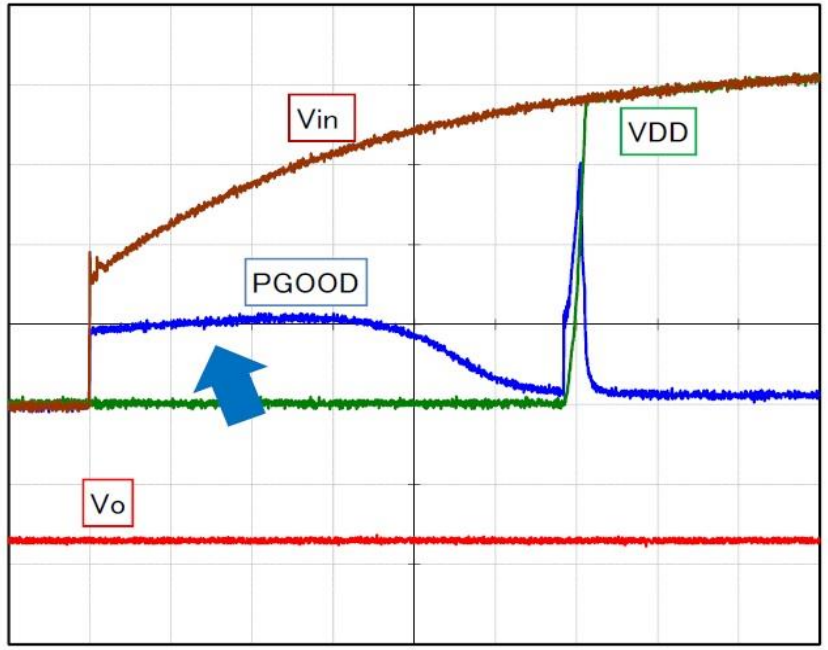
Is this device broken? (Currently 2 occurrences.)
Or may there be cases where PGOOD floats when power is not supplied?



	Scale	Bw
CH1	500 mV/div	20MHz
CH2	1 V/div	20MHz
CH3	1 V/div	20MHz
CH4	1 V/div	20MHz
REF1	–	–
REF2	–	–
REF3	–	–
REF4	–	–
MATH	–	–

TIME	10 ms/div
Mode	Normal

Good IC



	Scale	Bw
CH1	500 mV/div	20MHz
CH2	1 V/div	20MHz
CH3	1 V/div	20MHz
CH4	1 V/div	20MHz
REF1	–	–
REF2	–	–
REF3	–	–
REF4	–	–
MATH	–	–

TIME	10 ms/div
Mode	Normal

Faulty operation IC

8.1 Schematic

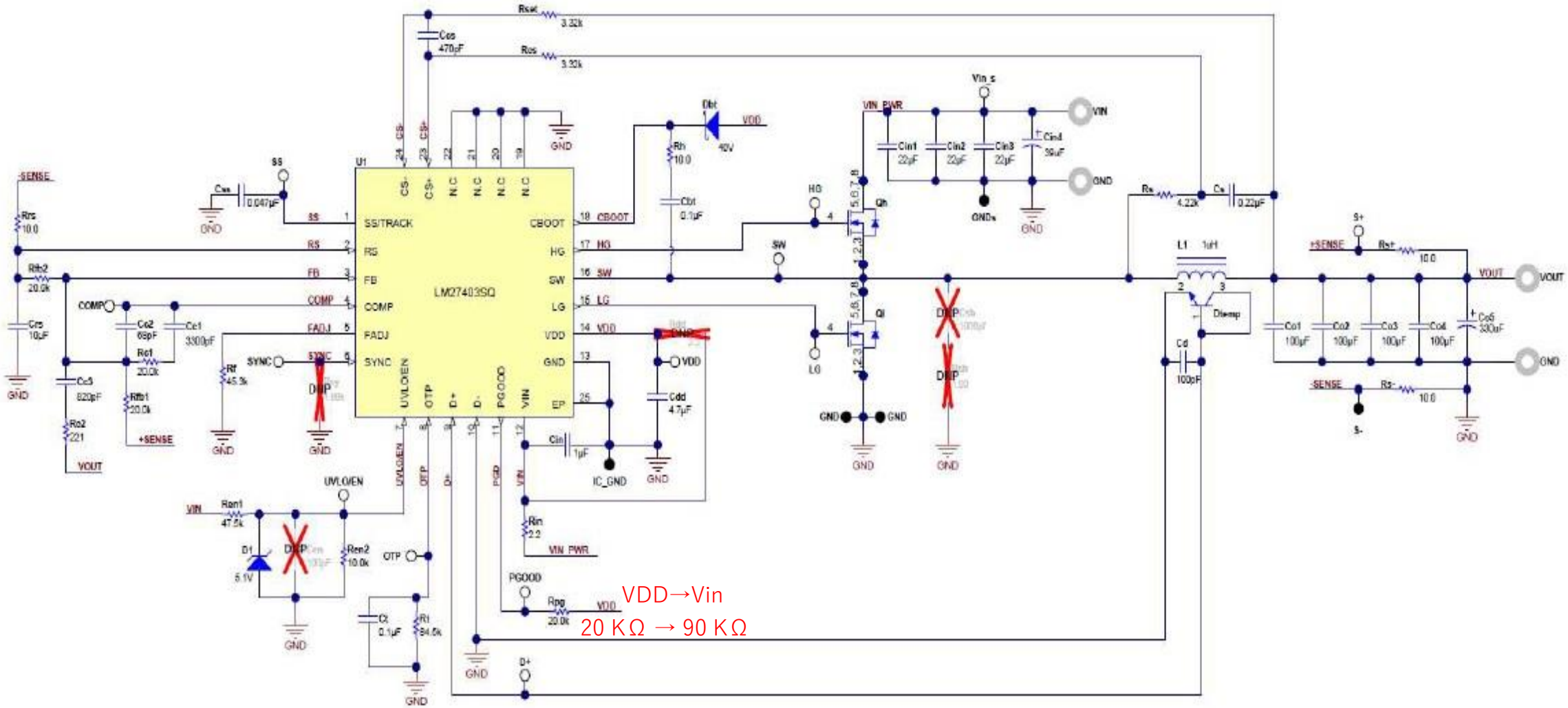


Figure 25. Schematic

Circuit example