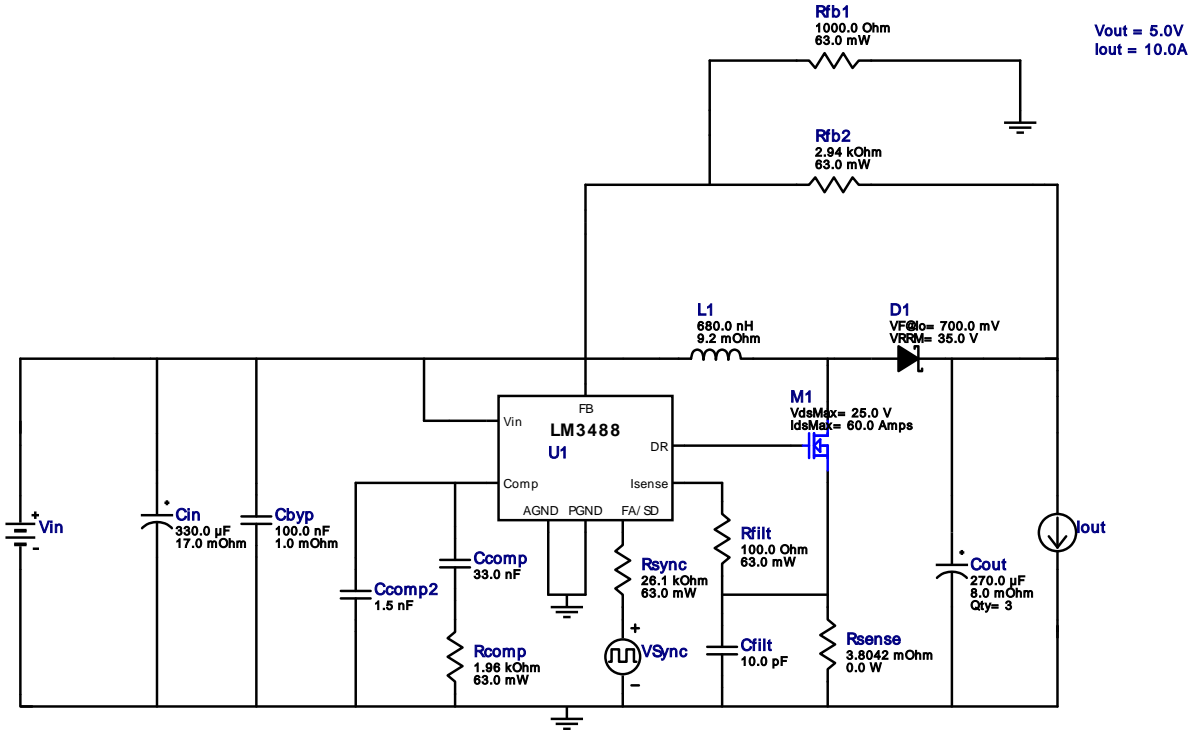







WEBENCH® Design Report

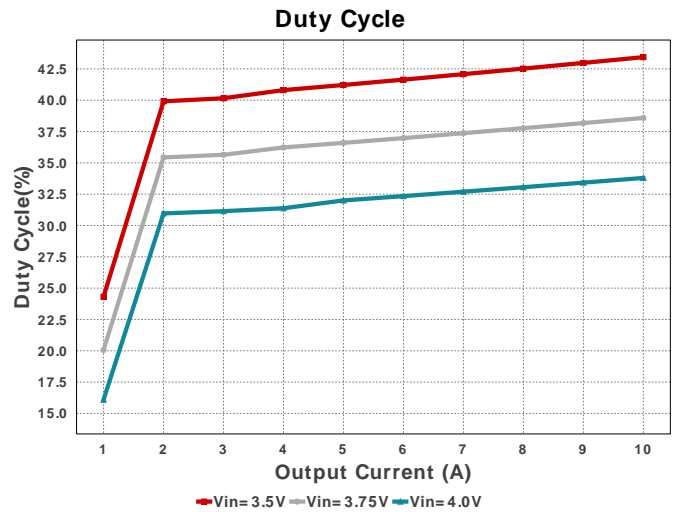
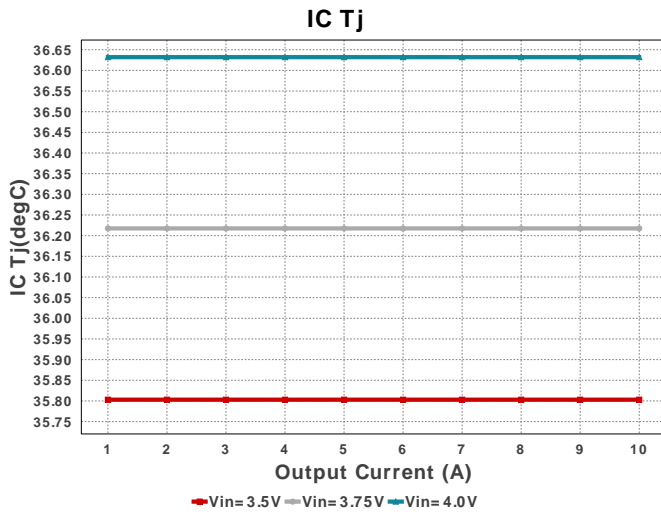
 Design : 20 LM3488MMX/NOPB
 LM3488MMX/NOPB 3.5V-4V to 5.00V @ 10A


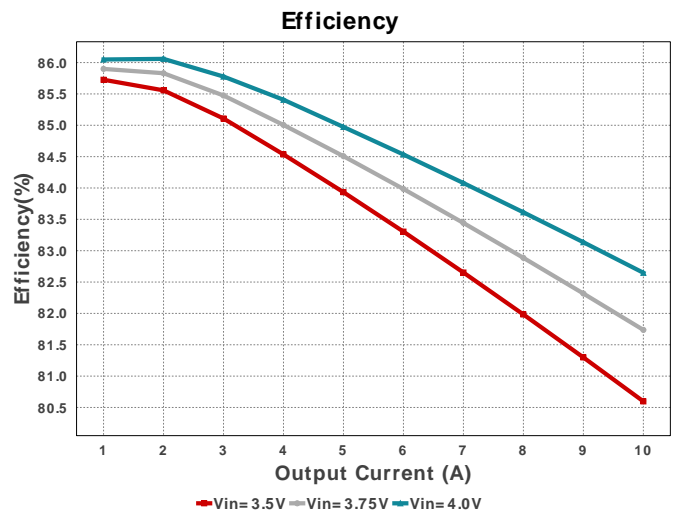
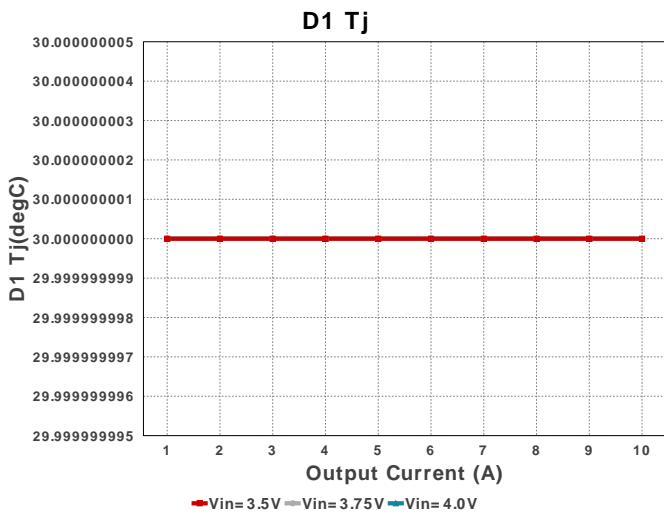
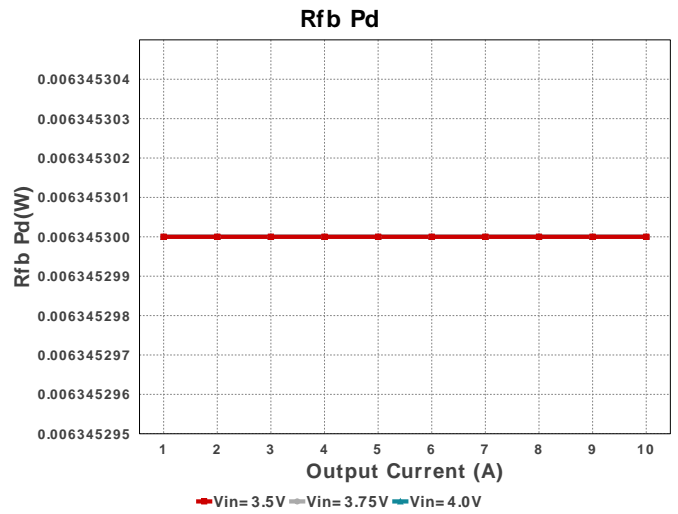
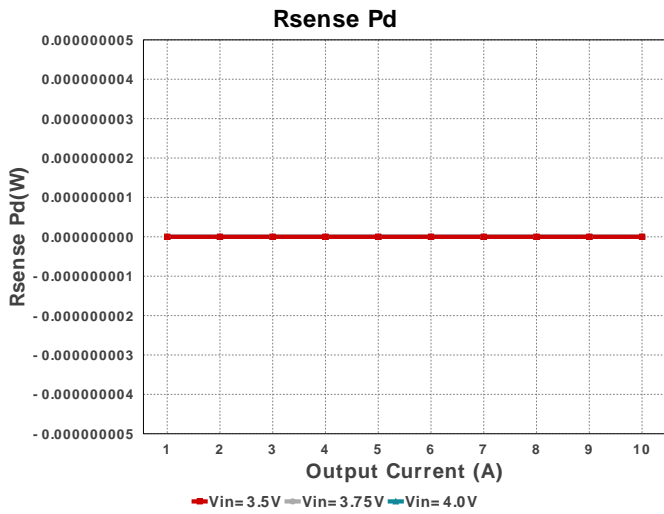
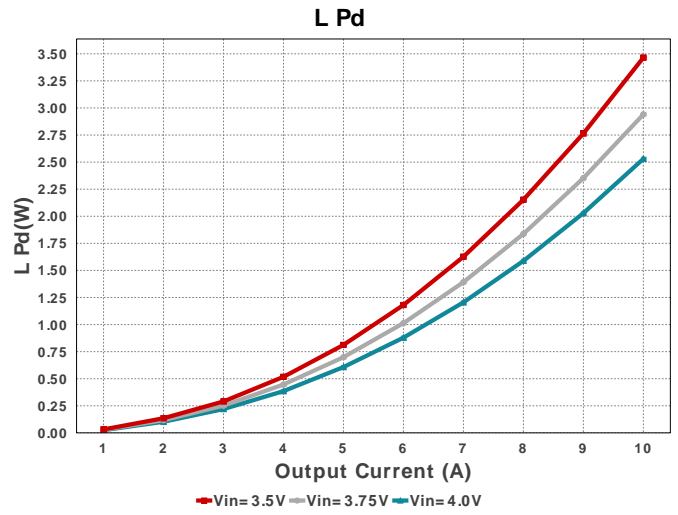
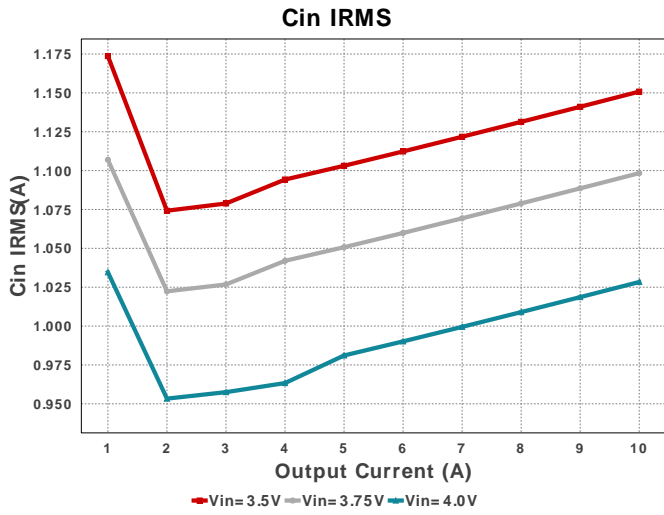
1. With the low turn of voltage of the LM34x8 your power supply may current limit before you reach your working input voltage. If this happens, or to preempt this from happening, you can include a low pass RC filter from input voltage to Vin on the IC. Make sure the rise time on the RC network is slower than your supply's rise time. If you are not using the synchronization feature of the part use the LM3478.

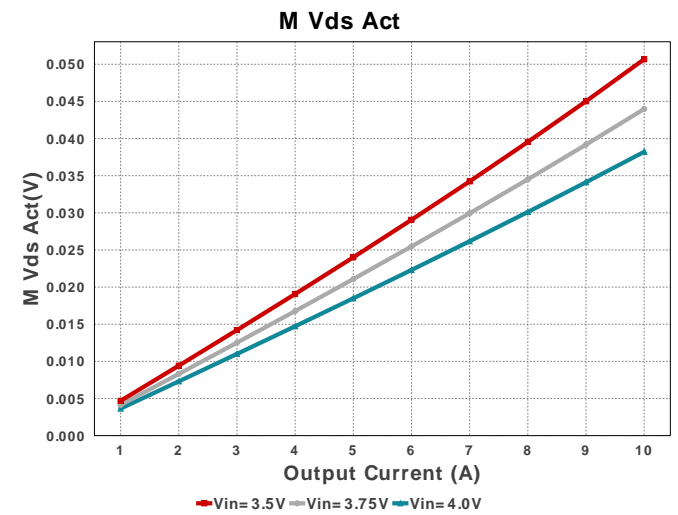
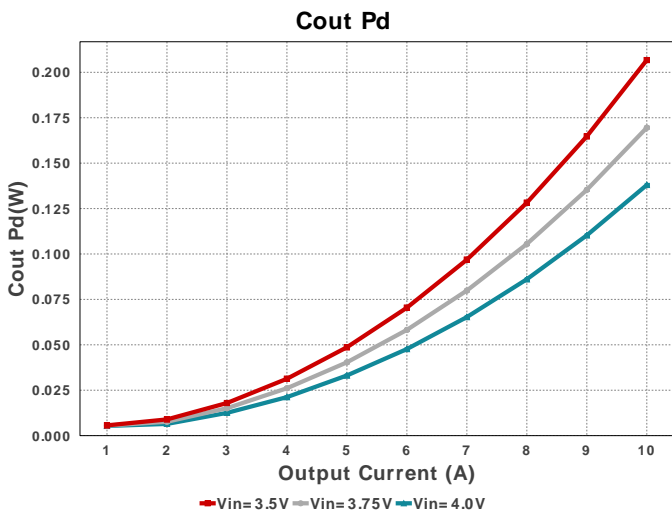
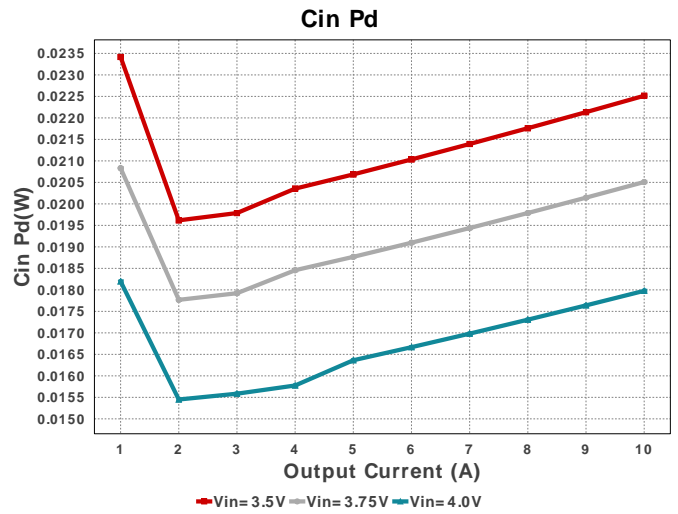
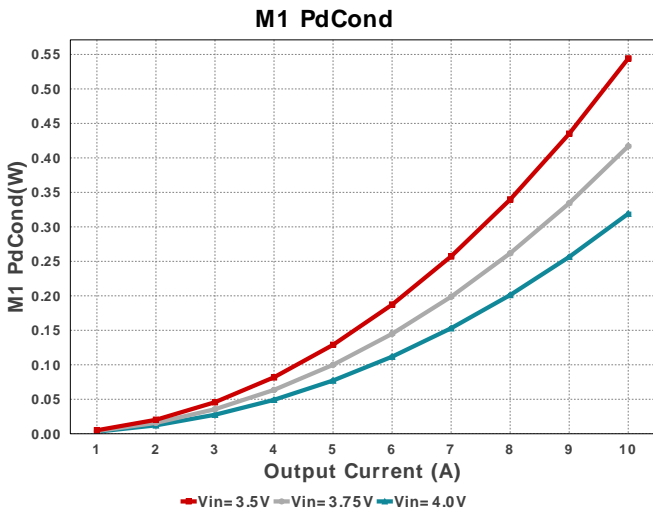
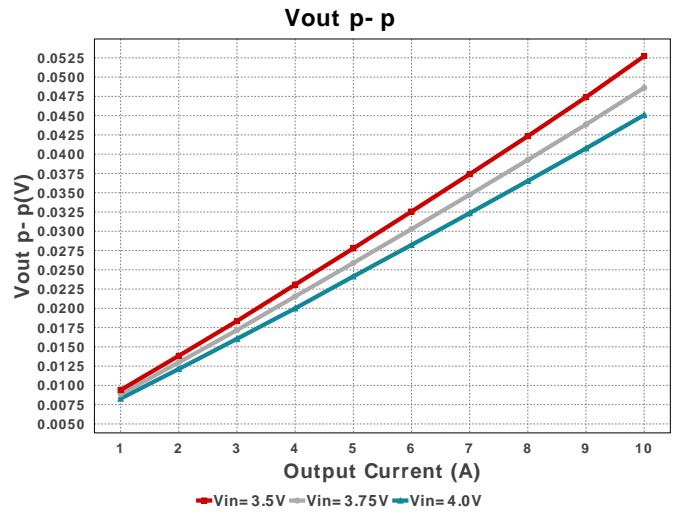
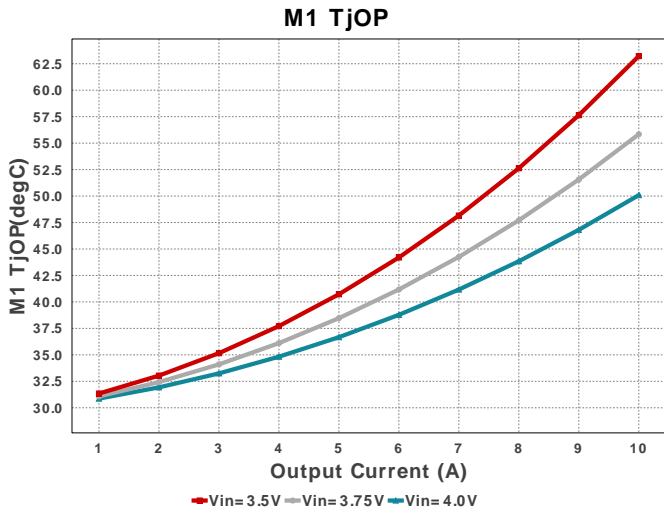
Electrical BOM

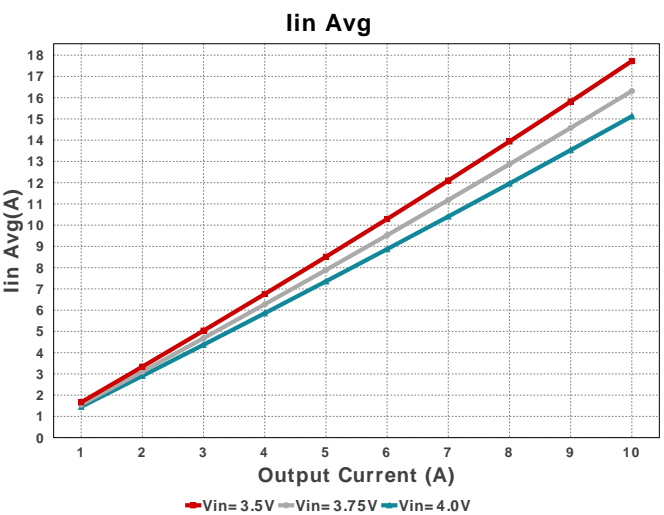
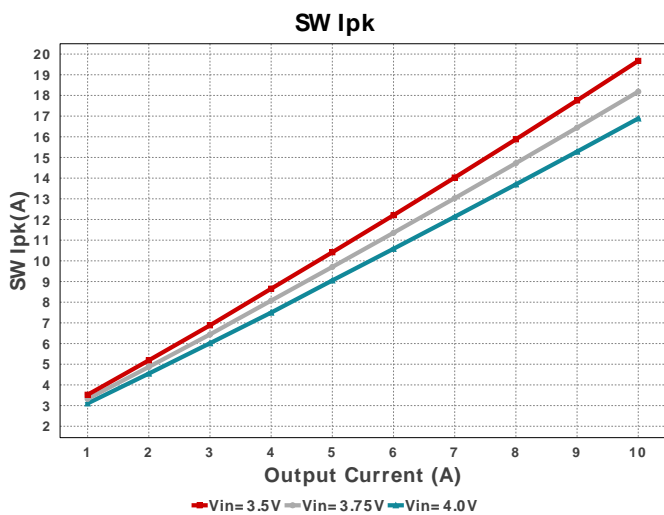
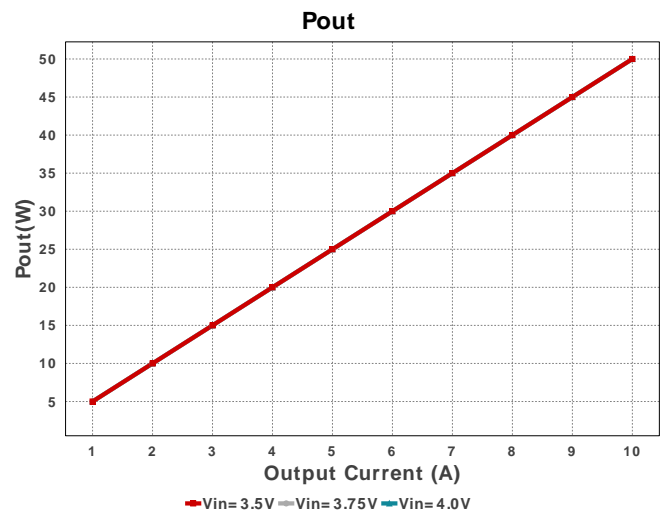
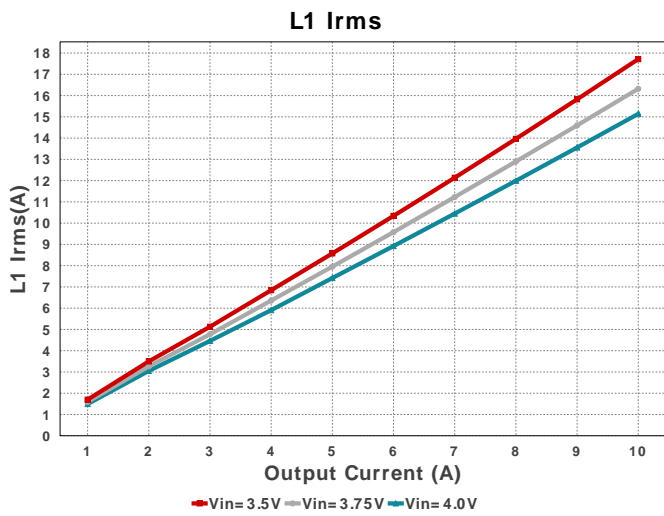
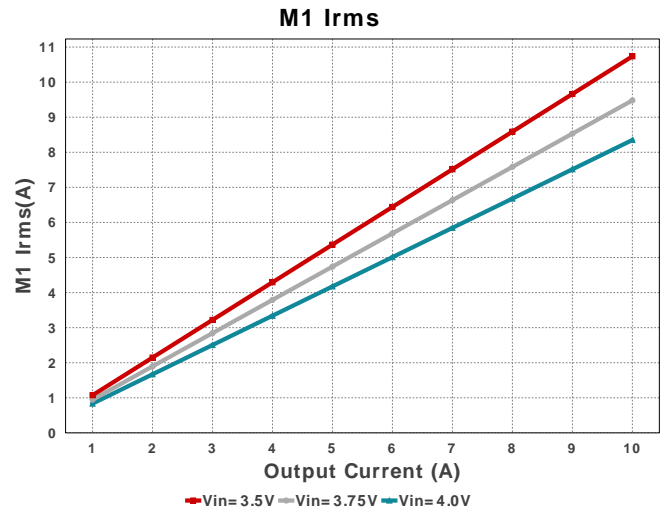
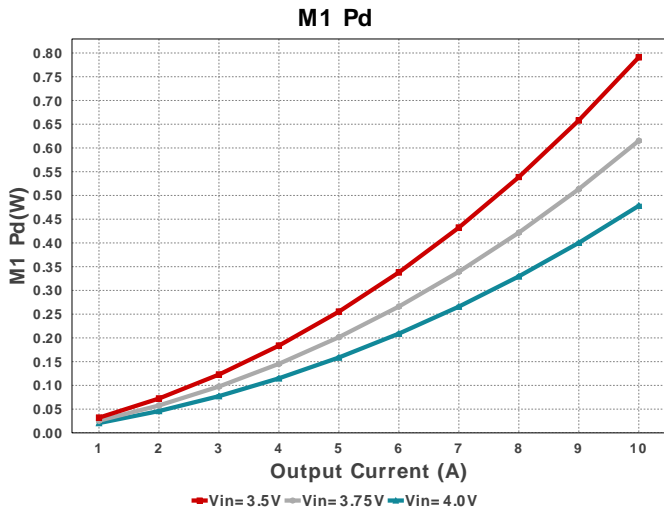
| Name | Manufacturer | Part Number | Properties | Qty | Price | Footprint |
|--------|---------------------------|---|---|-----|--------|-------------------------------------|
| Cbyp | MuRata | GRM155R70J104KA01D Series= X7R | Cap= 100.0 nF ESR= 1.0 mOhm VDC= 6.3 V IRMS= 0.0 A | 1 | \$0.01 | 0402 3 mm ² |
| Ccomp | TDK | CGA4J2C0G1H333J125AA Series= C0G/NP0 | Cap= 33.0 nF VDC= 50.0 V IRMS= 0.0 A | 1 | \$0.09 | 0805 7 mm ² |
| Ccomp2 | TDK | C2012C0G1H152J060AA Series= C0G/NP0 | Cap= 1.5 nF VDC= 50.0 V IRMS= 0.0 A | 1 | \$0.02 | 0805 7 mm ² |
| Cfilt | Samsung Electro-Mechanics | CL21C100JBANNNC Series= C0G/NP0 | Cap= 10.0 pF VDC= 50.0 V IRMS= 0.0 A | 1 | \$0.01 | 0805 7 mm ² |
| Cin | Panasonic | 6SVPC330M Series= SVPC | Cap= 330.0 uF ESR= 17.0 mOhm VDC= 6.3 V IRMS= 3.39 A | 1 | \$0.33 | SM_RADIAL_6.3AMM 80 mm ² |
| Cout | Panasonic | 16SVPG270M Series= SVPG | Cap= 270.0 uF ESR= 8.0 mOhm VDC= 16.0 V IRMS= 5.8 A | 3 | \$0.67 | CAPSMT_62_C10 74 mm ² |

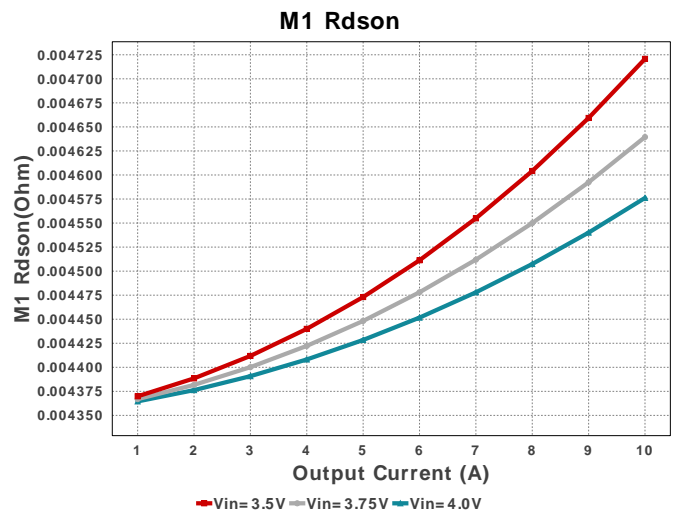
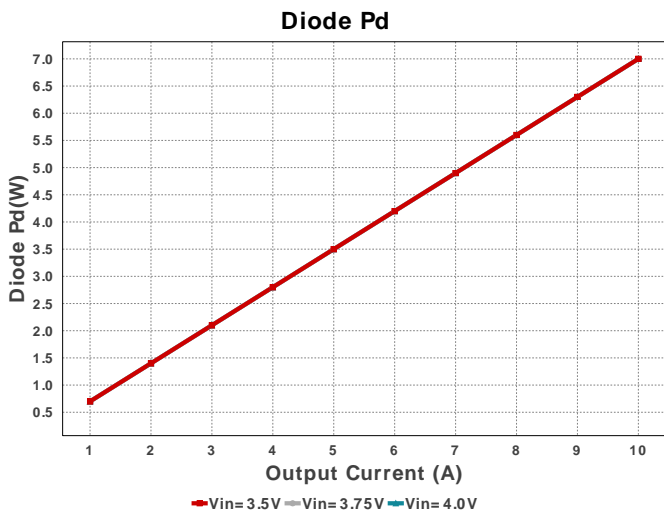
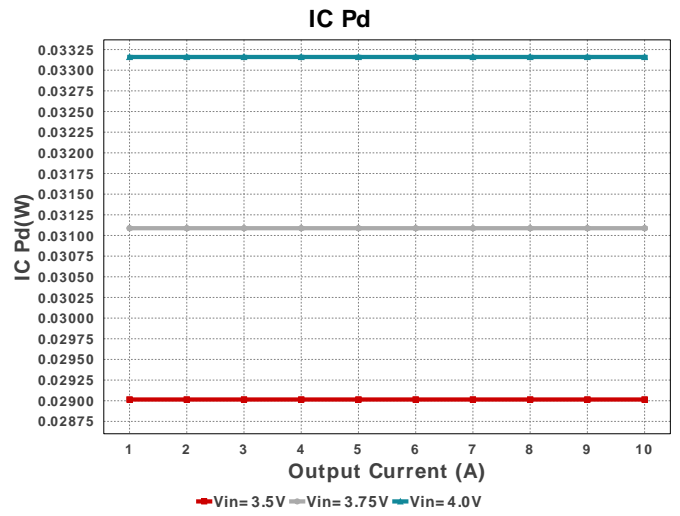
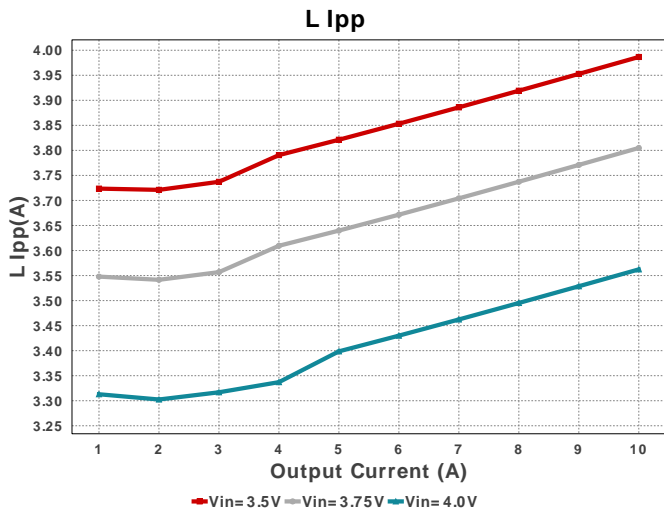
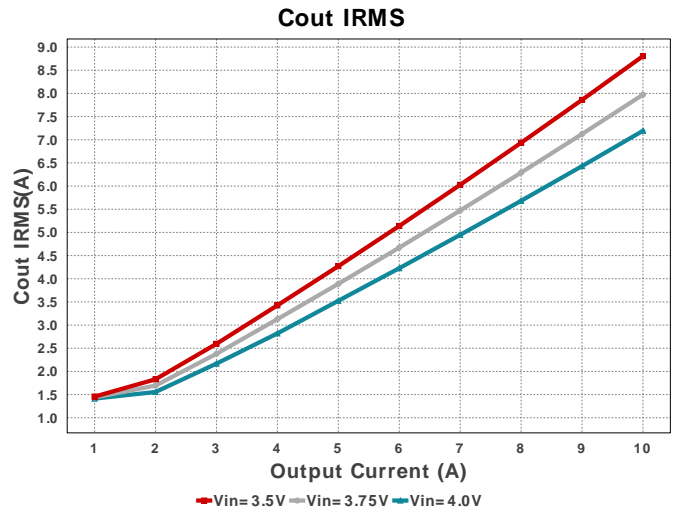
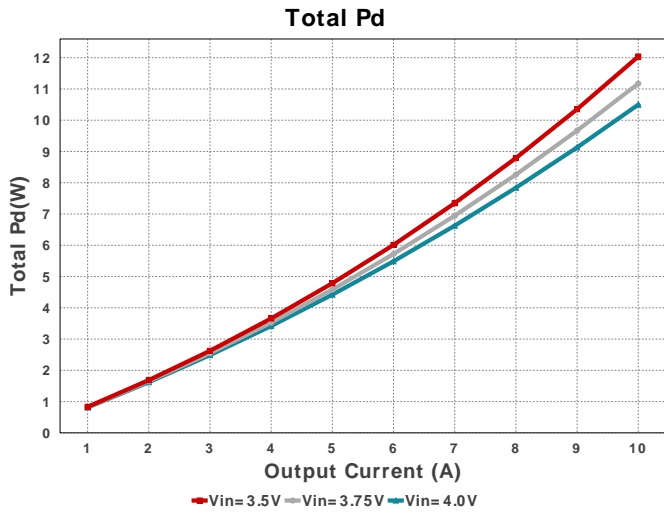
| Name | Manufacturer | Part Number | Properties | Qty | Price | Footprint |
|--------|----------------------|--------------------------------------|--|-----|--------|--|
| D1 | Vishay-Semiconductor | M3035S-E3/4W | VF@Io= 700.0 mV VRRM= 35.0 V | 1 | \$0.60 |  TO-220AB 79 mm ² |
| L1 | Sumida | CDEP63MENP-R68MC | L= 680.0 nH 9.2 mOhm | 1 | \$0.74 |  CDEP63 81 mm ² |
| M1 | Texas Instruments | CSD16340Q3 | VdsMax= 25.0 V IdsMax= 60.0 Amps | 1 | \$0.37 |  DQG0008A 18 mm ² |
| Rcomp | Vishay-Dale | CRCW04021K96FKED Series= CRCW..e3 | Res= 1.96 kOhm Power= 63.0 mW Tolerance= 1.0% | 1 | \$0.01 |  0402 3 mm ² |
| Rfb1 | Vishay-Dale | CRCW04021K00FKED Series= CRCW..e3 | Res= 1000.0 Ohm Power= 63.0 mW Tolerance= 1.0% | 1 | \$0.01 |  0402 3 mm ² |
| Rfb2 | Vishay-Dale | CRCW04022K94FKED Series= CRCW..e3 | Res= 2.94 kOhm Power= 63.0 mW Tolerance= 1.0% | 1 | \$0.01 |  0402 3 mm ² |
| Rfilt | Vishay-Dale | CRCW0402100RFKED Series= CRCW..e3 | Res= 100.0 Ohm Power= 63.0 mW Tolerance= 1.0% | 1 | \$0.01 |  0402 3 mm ² |
| Rsense | CUSTOM | CUSTOM Series= ? | Res= 3.8042 mOhm Power= 0.0 W Tolerance= 0.0% | 1 | NA | CUSTOM 0 mm ² |
| Rsync | Vishay-Dale | CRCW040226K1FKED Series= CRCW..e3 | Res= 26.1 kOhm Power= 63.0 mW Tolerance= 1.0% | 1 | \$0.01 |  0402 3 mm ² |
| U1 | Texas Instruments | LM3488MMX/NOPB | Switcher | 1 | \$0.83 |  MUA08A 24 mm ² |

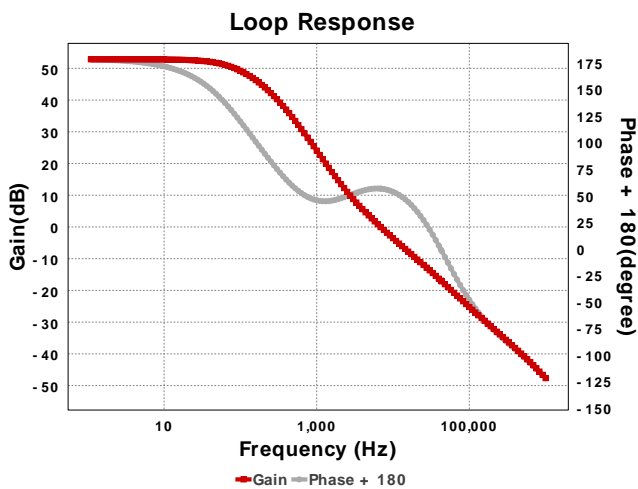
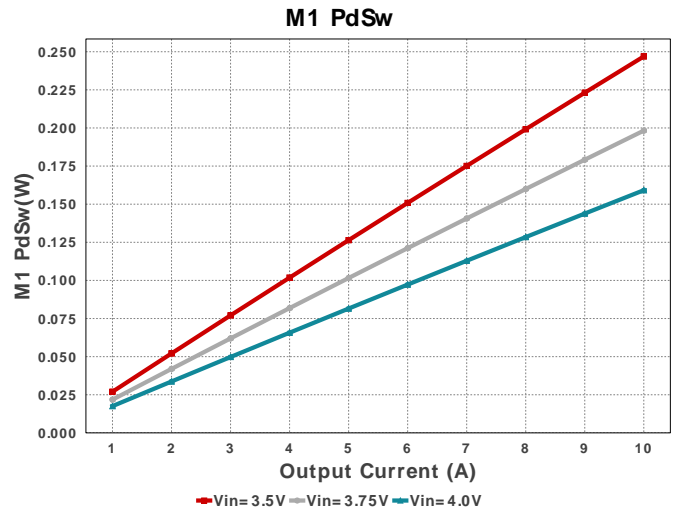
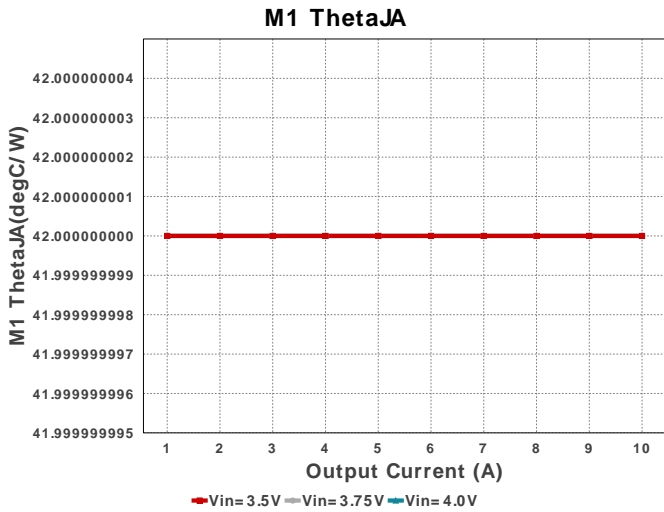












Operating Values

| # | Name | Value | Category | Description |
|-----|--------------|--------------|-----------|---|
| 1. | Cin IRMS | 1.129 A | Capacitor | Input capacitor RMS ripple current |
| 2. | Cin Pd | 21.665 mW | Capacitor | Input capacitor power dissipation |
| 3. | Cout IRMS | 8.656 A | Capacitor | Output capacitor RMS ripple current |
| 4. | Cout Pd | 199.81 mW | Capacitor | Output capacitor power dissipation |
| 5. | D1 Tj | 30.0 degC | Diode | D1 junction temperature |
| 6. | Diode Pd | 7.0 W | Diode | Diode power dissipation |
| 7. | IC Pd | 29.015 mW | IC | IC power dissipation |
| 8. | IC Tj | 35.803 degC | IC | IC junction temperature |
| 9. | IC Tolerance | 15.3 mV | IC | IC Feedback Tolerance |
| 10. | ICThetaJA | 200.0 degC/W | IC | IC junction-to-ambient thermal resistance |
| 11. | Iin Avg | 17.461 A | IC | Average input current |
| 12. | L Ipp | 3.911 A | Inductor | Peak-to-peak inductor ripple current |
| 13. | L Pd | 3.364 W | Inductor | Inductor power dissipation |
| 14. | L1 Irms | 17.456 A | Inductor | Inductor ripple current |
| 15. | M Vds Act | 50.681 mV | Mosfet | M Vds |
| 16. | M1 Irms | 10.736 A | Mosfet | M1 MOSFET Irms |
| 17. | M1 Pd | 791.0 mW | Mosfet | M1 MOSFET total power dissipation |
| 18. | M1 PdCond | 544.1 mW | Mosfet | M1 MOSFET conduction losses |
| 19. | M1 PdSw | 246.9 mW | Mosfet | M1 MOSFET switching losses |
| 20. | M1 Rdson | 4.721 mOhm | Mosfet | Drain-Source On-resistance |
| 21. | M1 ThetaJA | 42.0 degC/W | Mosfet | MOSFET junction-to-ambient thermal resistance |
| 22. | M1 TjOP | 63.222 degC | Mosfet | M1 MOSFET junction temperature |
| 23. | Cin Pd | 21.665 mW | Power | Input capacitor power dissipation |
| 24. | Cout Pd | 199.81 mW | Power | Output capacitor power dissipation |
| 25. | Diode Pd | 7.0 W | Power | Diode power dissipation |
| 26. | IC Pd | 29.015 mW | Power | IC power dissipation |
| 27. | L Pd | 3.364 W | Power | Inductor power dissipation |
| 28. | M1 Pd | 791.0 mW | Power | M1 MOSFET total power dissipation |
| 29. | M1 PdCond | 544.1 mW | Power | M1 MOSFET conduction losses |
| 30. | M1 PdSw | 246.9 mW | Power | M1 MOSFET switching losses |
| 31. | Rfb Pd | 6.345 mW | Power | Rfb Power Dissipation |
| 32. | Rsense Pd | 1.009 W | Power | LED Current Rsns Power Dissipation |

| # | Name | Value | Category | Description |
|-----|----------------|-----------------------|--------------------|--|
| 33. | Total Pd | 11.113 W | Power | Total Power Dissipation |
| 34. | Rfb Pd | 6.345 mW | Resistor | Rfb Power Dissipation |
| 35. | Rsense Pd | 1.009 W | Resistor | LED Current Rsns Power Dissipation |
| 36. | BOM Count | 18 | System Information | Total Design BOM count |
| 37. | Cross Freq | 6.098 kHz | System Information | Bode plot crossover frequency |
| 38. | Duty Cycle | 42.594 % | System Information | Duty cycle |
| 39. | Efficiency | 81.816 % | System Information | Steady state efficiency |
| 40. | FootPrint | 548.0 mm ² | System Information | Total Foot Print Area of BOM components |
| 41. | Frequency | 550.0 kHz | System Information | Switching frequency |
| 42. | Gain Marg | -15.866 dB | System Information | Bode Plot Gain Margin |
| 43. | Iout | 10.0 A | System Information | Iout operating point |
| 44. | Low Freq Gain | 51.601 dB | System Information | Gain at 1Hz |
| 45. | Mode | CCM | System Information | Conduction Mode |
| 46. | Phase Marg | 55.707 deg | System Information | Bode Plot Phase Margin |
| 47. | Pout | 50.0 W | System Information | Total output power |
| 48. | SW Ipk | 19.375 A | System Information | Peak switch current |
| 49. | Total BOM | NA | System Information | Total BOM Cost |
| 50. | Vin | 3.5 V | System Information | Vin operating point |
| 51. | Vout | 5.0 V | System Information | Operational Output Voltage |
| 52. | Vout Actual | 4.964 V | System Information | Vout Actual calculated based on selected voltage divider resistors |
| 53. | Vout Tolerance | 2.74 % | System Information | Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable |
| 54. | Vout p-p | 51.938 mV | System Information | Peak-to-peak output ripple voltage |

Design Inputs

| Name | Value | Description |
|---------|---------|-----------------------------|
| Iout | 10.0 | Maximum Output Current |
| VinMax | 4.0 | Maximum input voltage |
| VinMin | 3.5 | Minimum input voltage |
| Vout | 5.0 | Output Voltage |
| base_pn | LM3488 | Base Product Number |
| source | DC | Input Source Type |
| Ta | 30.0 | Ambient temperature |
| UserFsw | 550.0 k | Customer Selected Frequency |

WEBENCH® Assembly

Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of C_{in} and C_{out} , and the inductance and DC resistance of L1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

Soldering Component to Board

If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab down to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 3.5V and set the input supply's current limit to zero. With the input supply off connect up the input supply to V_{in} and GND. Connect a digital volt meter and a load if needed to set the minimum load of the design from V_{out} and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between V_{in} and GND, a load is connected between V_{out} and GND and a current meter is connected in series between V_{out} and the load. The load must be able to handle at least rated output power + 50% (7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.



Design Assistance

1. Master key : 4228023EAEE69D8F[v1]
2. **LM3488** Product Folder : <http://www.ti.com/product/LM3488> : contains the data sheet and other resources.

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