Using LM5067 for High Voltage Hot-Swap Designs
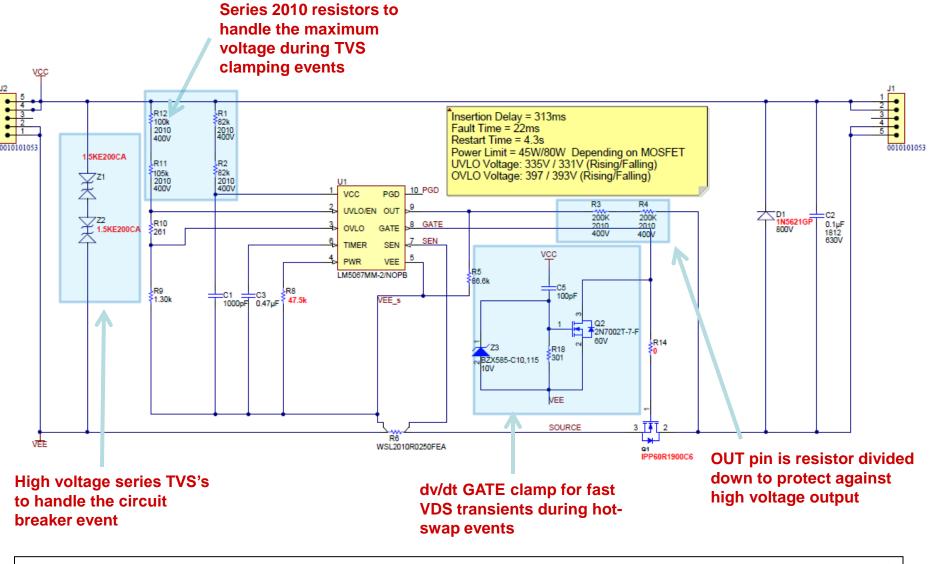# Example Circuit: 360V 2A

Series 2010 resistors to handle the maximum voltage during TVS clamping events

Insertion Delay = 313ms
Fault Time = 22ms
Restart Time = 4.3s
Power Limit = 45W/80W Depending on MOSFET
UVLO Voltage: 335V / 331V (Rising/Falling)
OVLO Voltage: 397 / 393V (Rising/Falling)

High voltage series TVS's to handle the circuit breaker event

dv/dt GATE clamp for fast VDS transients during hot-swap events

OUT pin is resistor divided down to protect against high voltage output