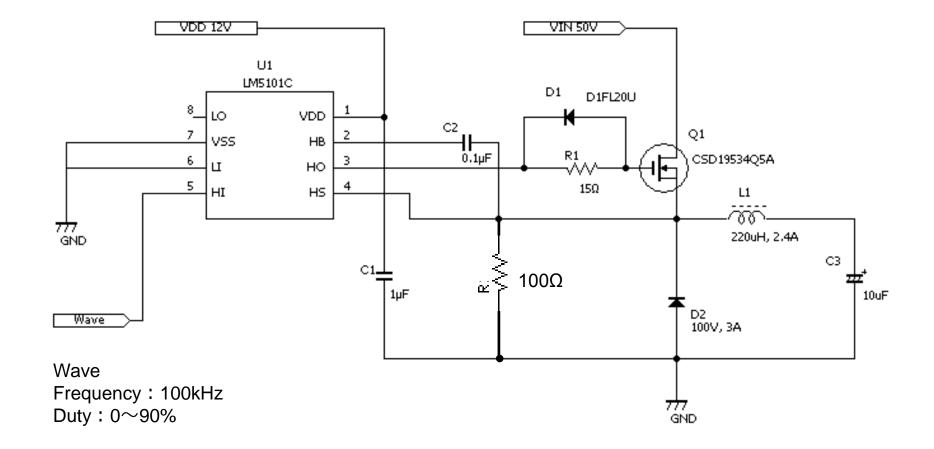
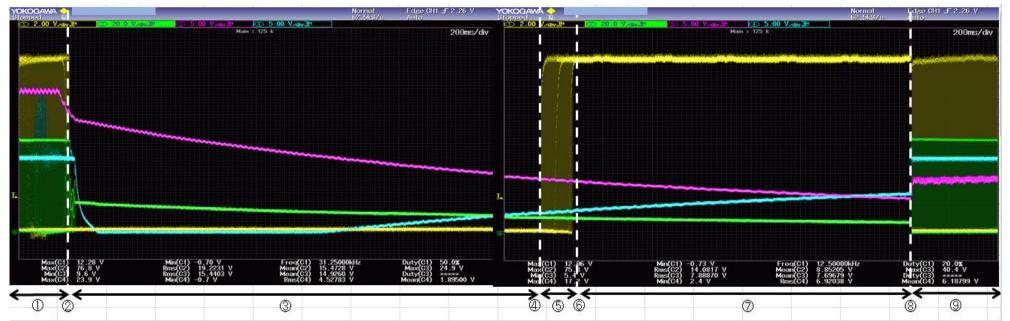
LM501C circuit diagram



LM501C Measured waveform



<Conditions>

- ① normal operation.
- ② Fan stop instruction = No instruction to HI (0 V).
- ③ Stop period.
- 4 Resume instruction to HI.
- ⑤ HI is instructed but HO is stopped (the capacitor at both ends of C2 is not fully charged).

Therefore, the circuit judges that the command voltage is insufficient, and the PWM width to HI increases steadily (PWM: 0% to increase steadily).

- 6 The instruction to HI finally becomes PWM 100% (full ON).
- ② Since the charge of C2 is still insufficient, HO does not operate.
- ® Voltage sufficient to operate C 2 is charged and HO also starts operation.
- After normal operation.

<Problems>

- · Response delay occurs
- · Since PWM 100% is applied, input voltage = output voltage is applied although it is instantaneous.

Is it not working when input voltage and output voltage are close?

Measurement points CH1: HI

CH2: HO CH3: Vout

CH4: C2 Voltage (Differential probe)