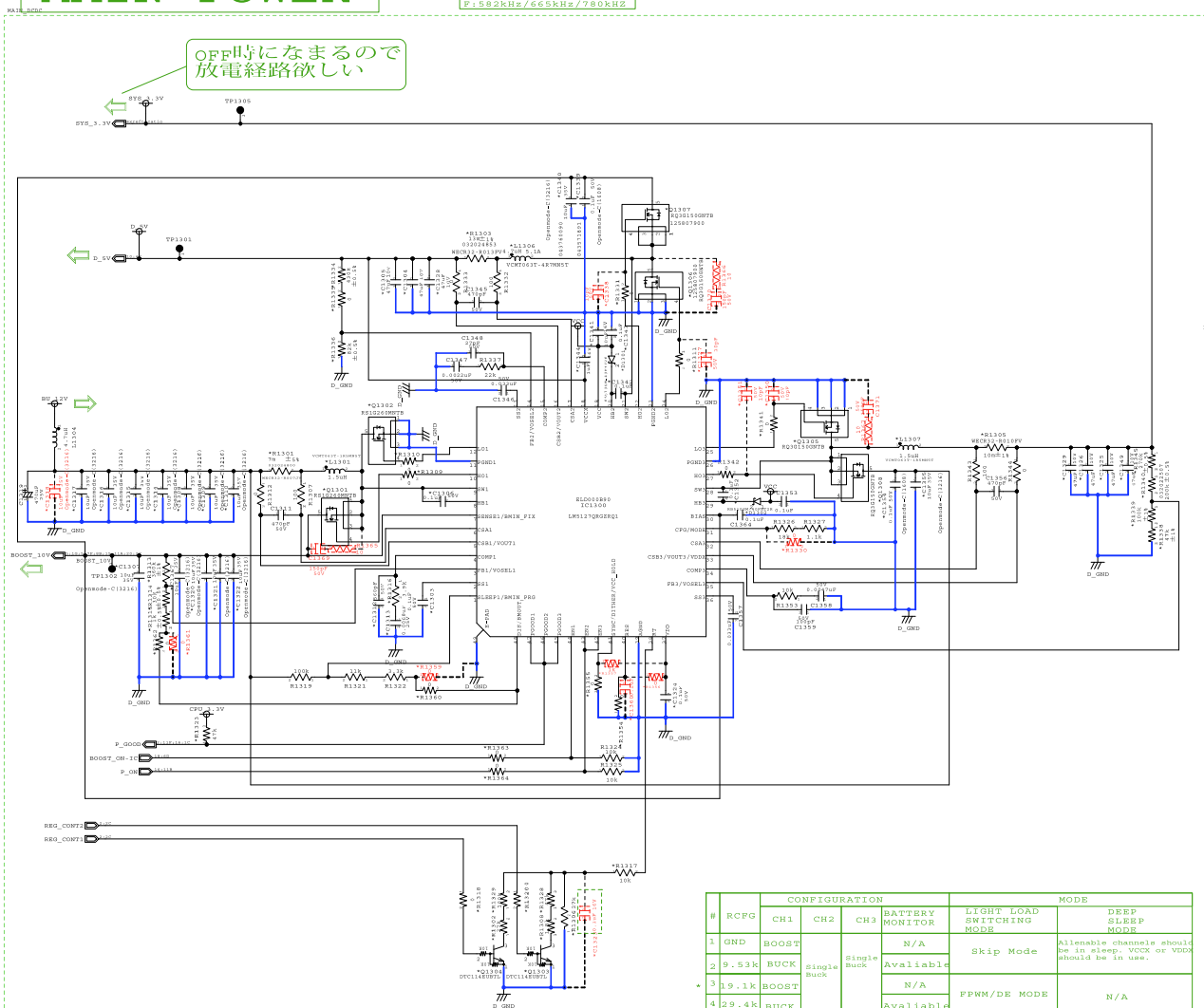
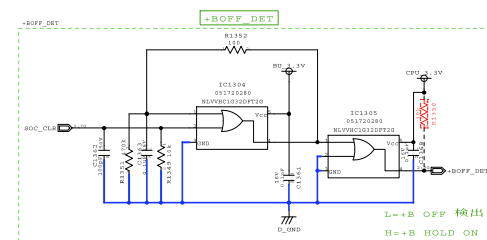
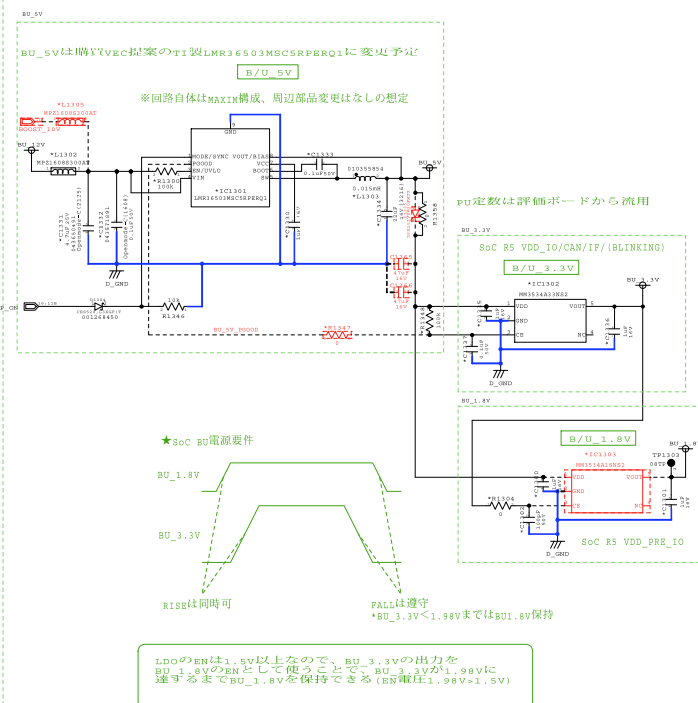


OFF時になまるので
放電経路欲しい



#	CONFIGURATION				MODE	
	CH1	CH2	CH3	BATTERY MONITOR	LIGHT LOAD SWITCHING MODE	DEEP SLEEP
1	GN2	BOOST	Single Buck	N/A	Skip Mode	Allowable channels should be in sleep. VCCX or VDI should be in use.
2	9.53k	BUCK		Available		
3	19.1k	BOOST	Single Buck	N/A	PFWM/DE MODE	N/A
4	29.4k	BUCK		Available		
5	41.2k	BOOST	Dual-phase Buck	N/A	CH1: Skip Mode CH2, CH3: PFWM /DE MODE	CH1 should be in sleep while CH2 and CH3 are shutdown. VCCX should be in use.
6	54.9k	BUCK		Available		
7	71.5k	BOOST	Dual-phase Buck	N/A	PFWM/DE MODE	N/A
8	90.9k	BUCK		Available		

FET Body Diode経路でBOOST 10Vへの流れ込み発生する。
暗電流増加の可能性あるので後段でBoost10V使用箇所は要注意
(特にBacklight DRVの入力に注意)



*****!*****			DWG	主	230104	CAL WT(φ)	MATERIAL	NAME
			DSGN	主	230104			CIRCUIT DIAGRAM
			CHK			ACT WT(φ)	FINISH	DWG NO.
SYM	DATE	REVISION						/AH-A0102-AA-CA-13/22
MODEL		DATE	APPR				SCALE	FREE
P0-003HO-0		230104					UNIT	mm
faurecia clarion ELECTRONICS								