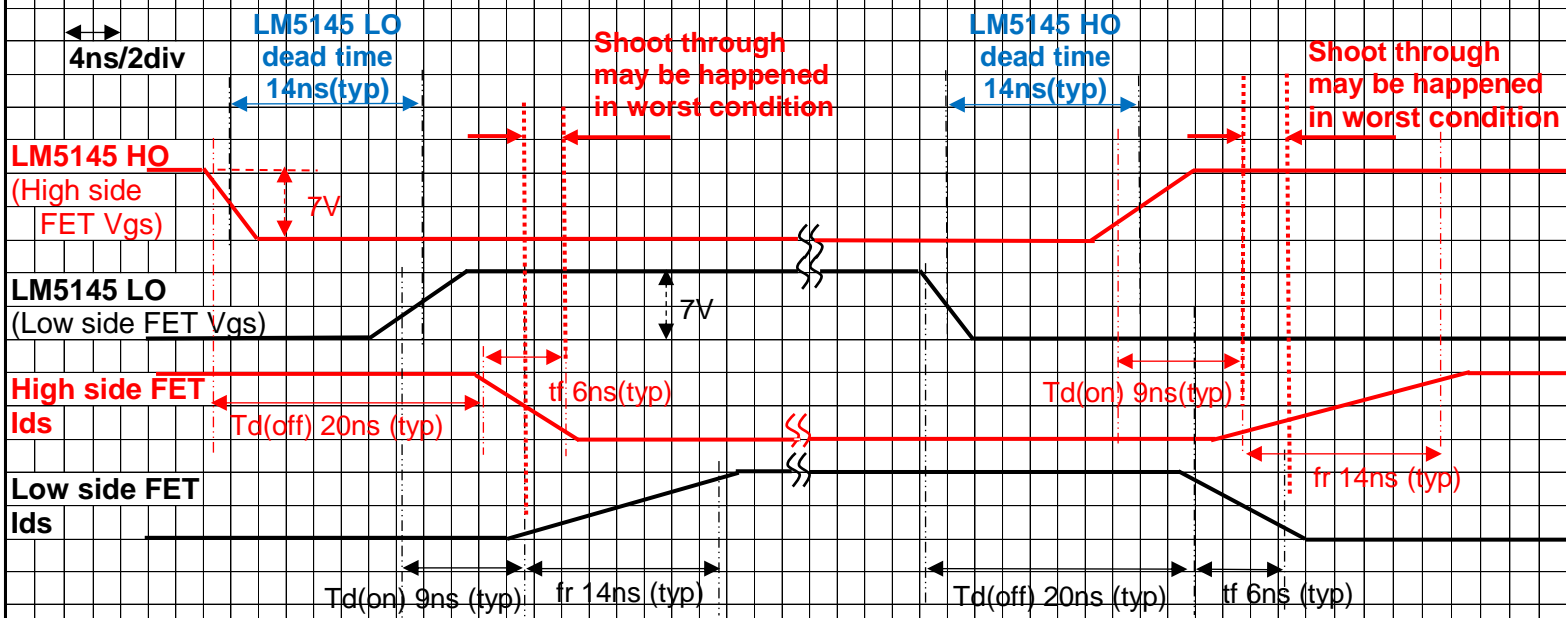


LM5145 shoot through calculation

base on LM5145 HO/LO dead time and CSD19534Q5A Drain rising/falling time / delaytime.

Feb/08/2019



According to LM5145 datasheet,
 HO terminal voltage rising (FET=off->on) : 7ns
 HO terminal voltage falling (FET=on->off) : 4ns
 LO off -> HO on dead time : 14ns

LO terminal voltage rising (FET=off->on) : 7ns
 LO terminal voltage falling (FET=on->off) : 4ns
 HO off -> LO on dead time : 14ns

According to [CSD19534Q5A](#) datasheet FET
 FET Ids rising (FET off->on) : 14ns FET on delay : 9ns
 FET Ids falling (FET on->off) : 6ns FET off delay : 20ns