

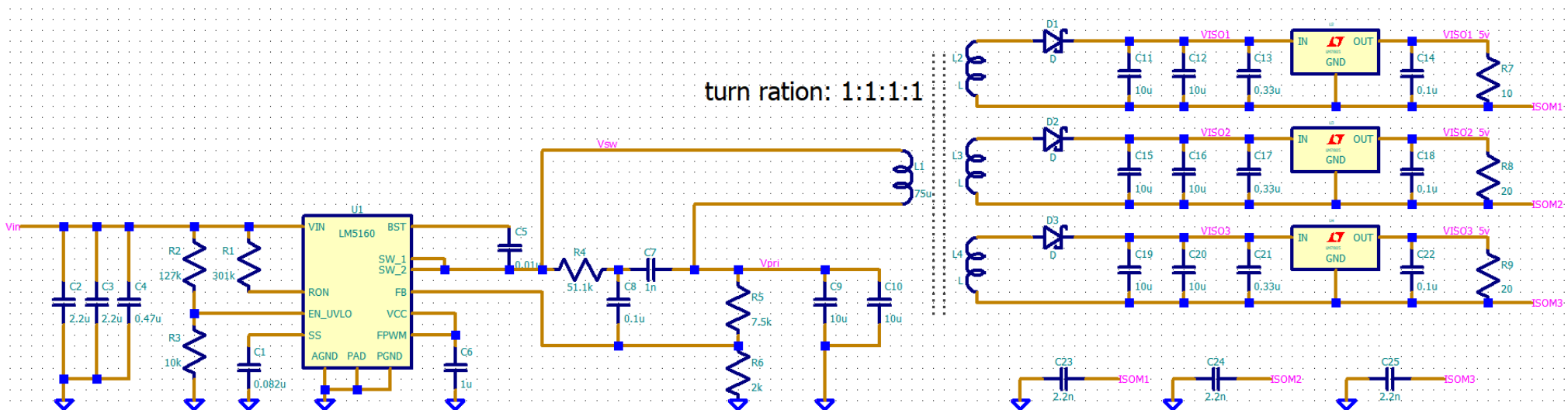
Design spec.:

Input: 24V dc

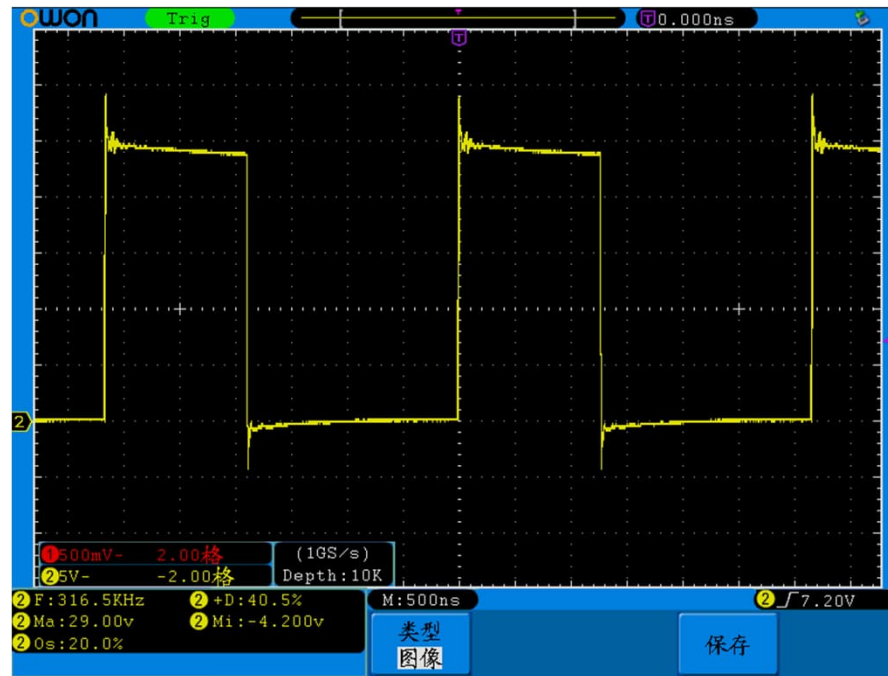
I_{viso1}@max: 0.5A

I_{viso2}@max: 0.2A

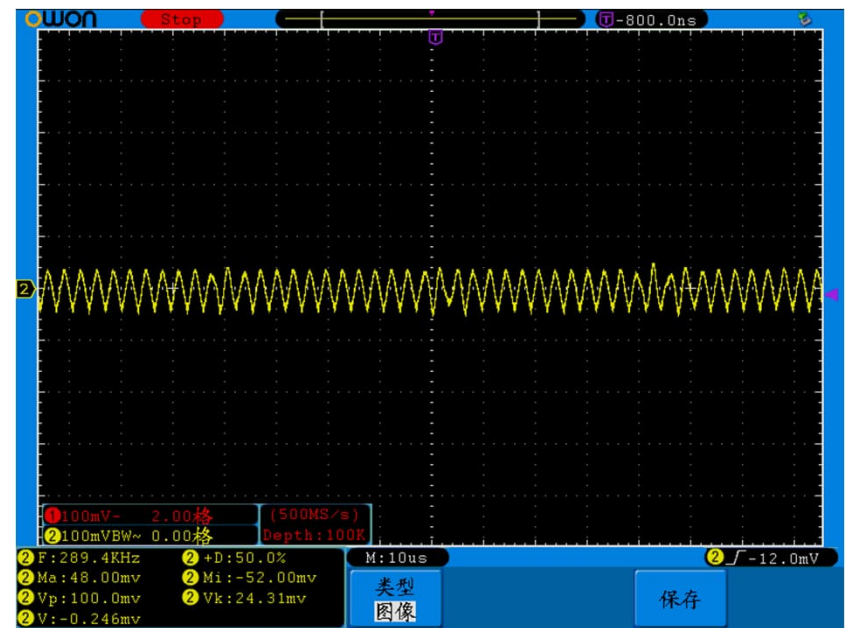
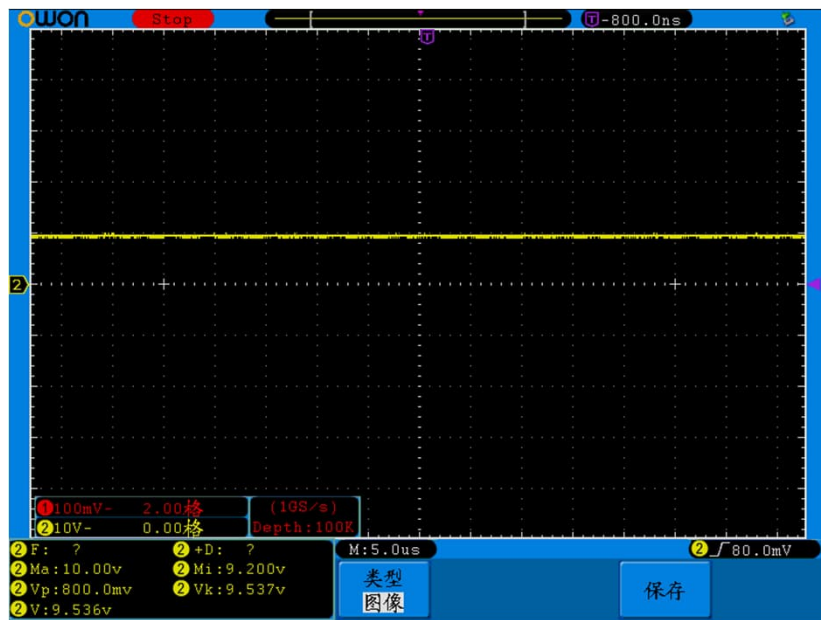
I_{viso3}@max: 0.2A



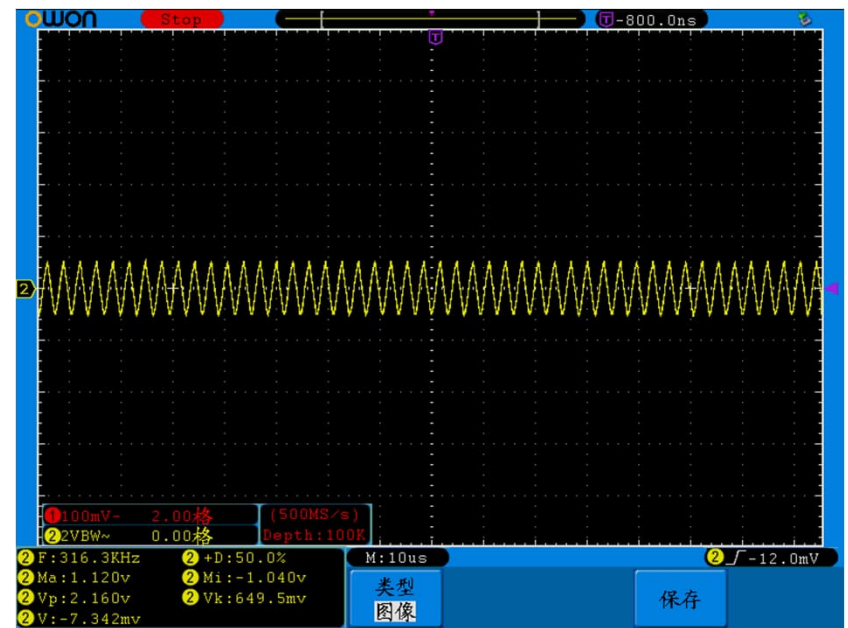
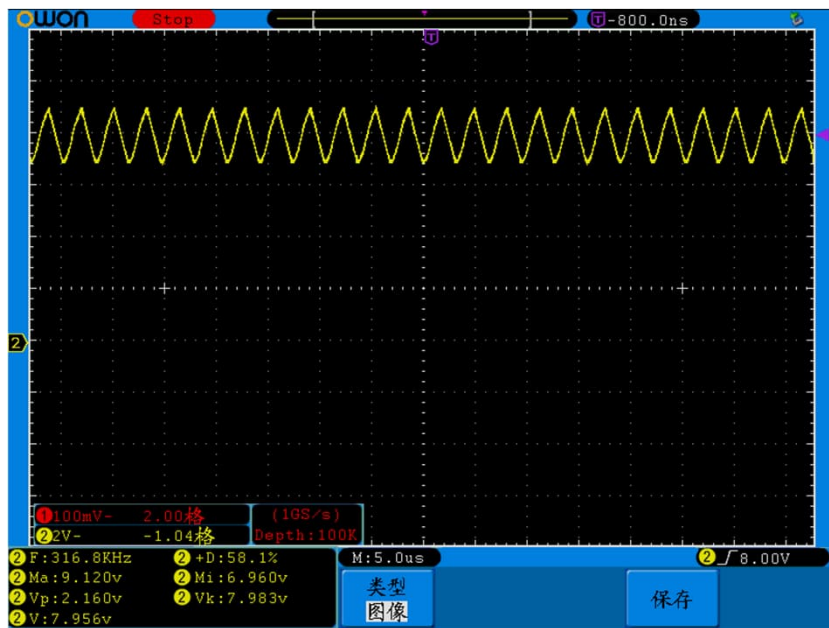
- Static load test, $V_{in}@24v$



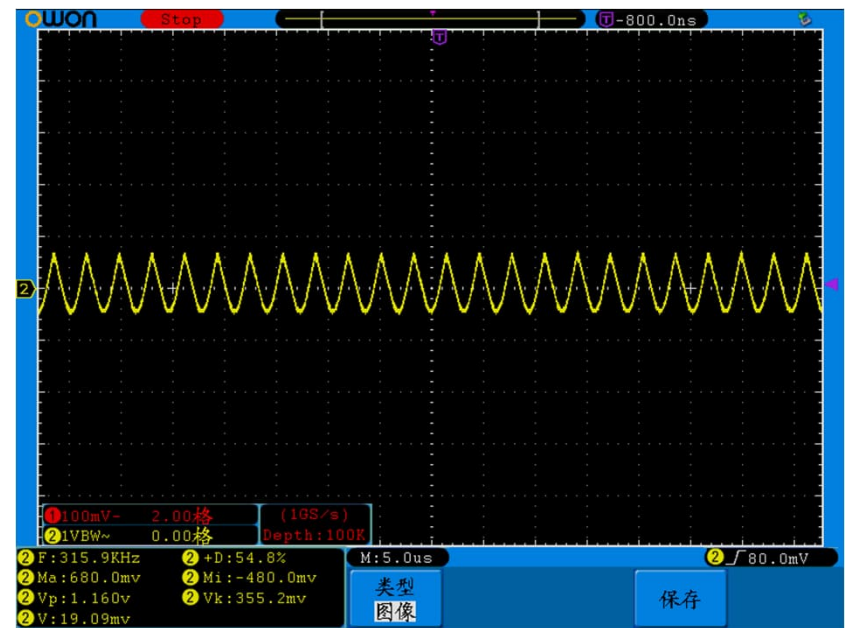
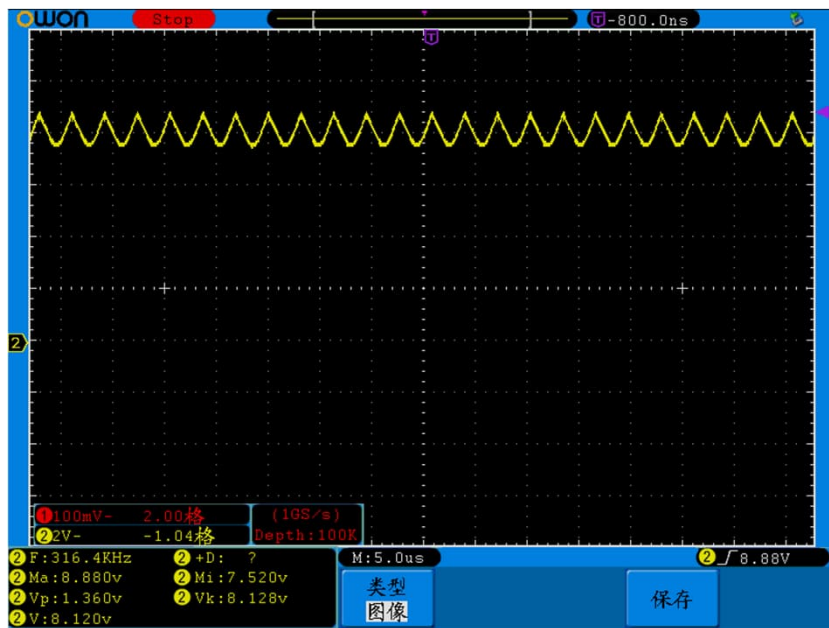
- Static load test, $V_{in}@24v$
- V_{pri} & V_{pri} ripple



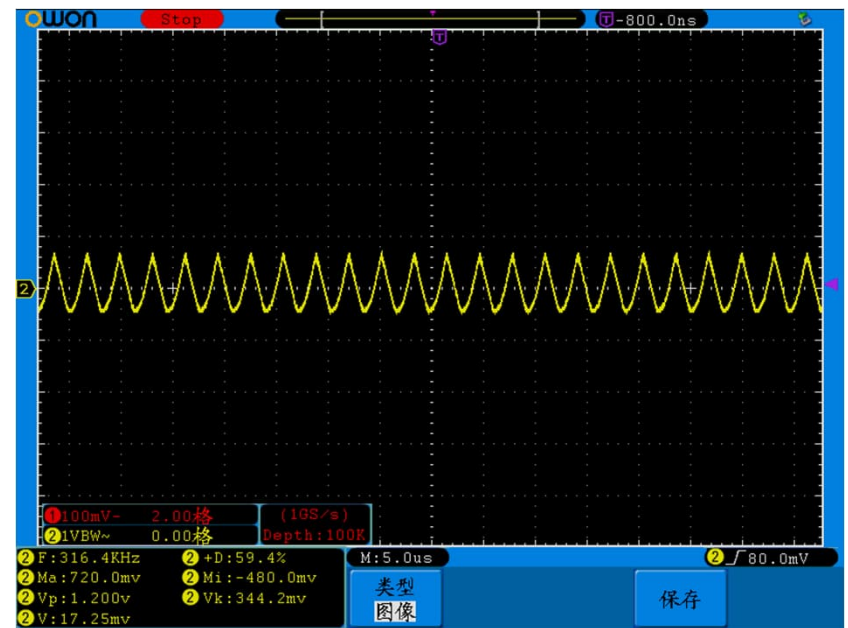
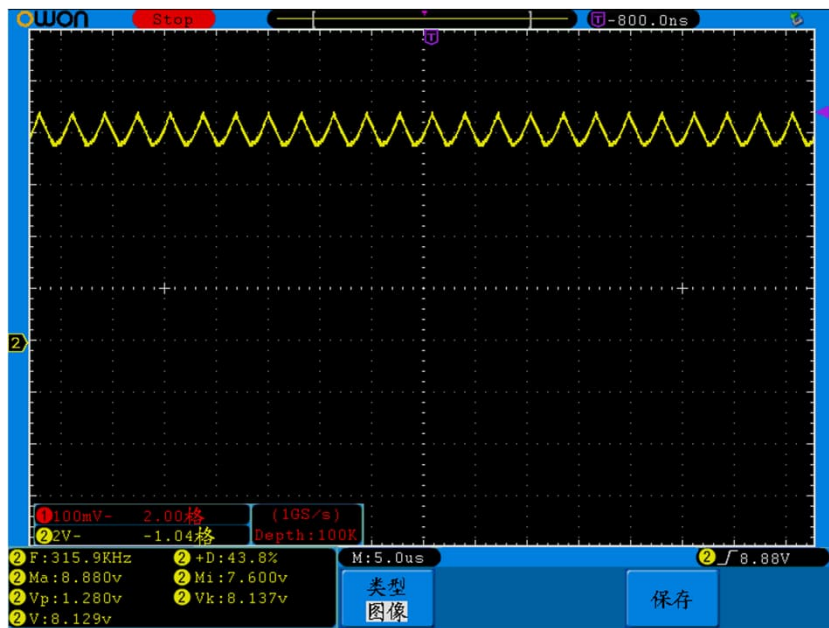
- Static load test, Vin@24v
- VISO1 & VISO1 ripple



- Static load test, Vin@24v
- VISO2 & VISO2 ripple



- Static load test, Vin@24v
- VISO3 & VISO3 ripple



- Static load test, Vin@24v
- VISO1_5v ripple

