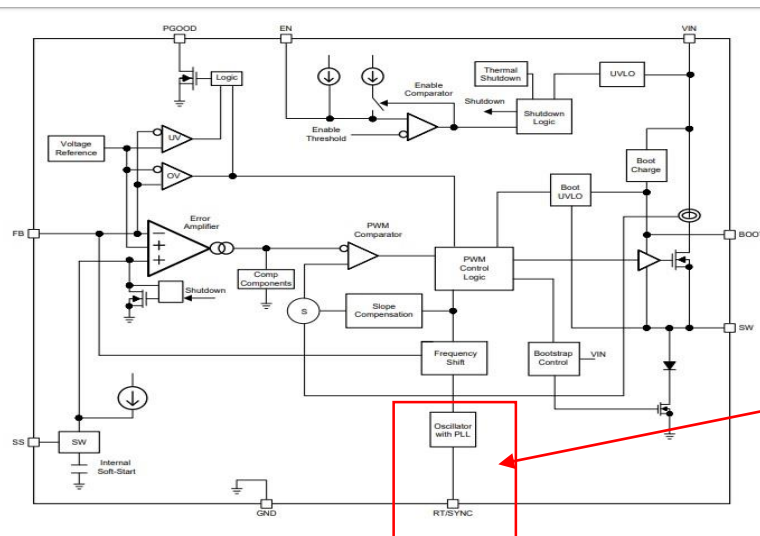




Question ①

How is the oscillation frequency determined inside the sync terminal?

Is the frequency determined by the RT-PIN voltage or current?

Is there any data on the internal circuit of oscillation?

Question ②

If you try to move this IC at a minimum frequency of about 240kHz, RT will be 110kΩ.

Please let me know an example of the external clock circuit at this time

Question ③

In a state where voltage is applied by dividing the resistance externally, Please let me know if the terminal can be used open / short with a transistor.

Example: V_{rt}: 11V divided by 27kΩ and 11kΩ to 3.18V, signal ON → 3.18V⇔0V

Question ④

The minimum frequency is 200Khz. What happens when RTpin is 133kΩ or higher? What is the frequency when RT-pin is set to GND?

