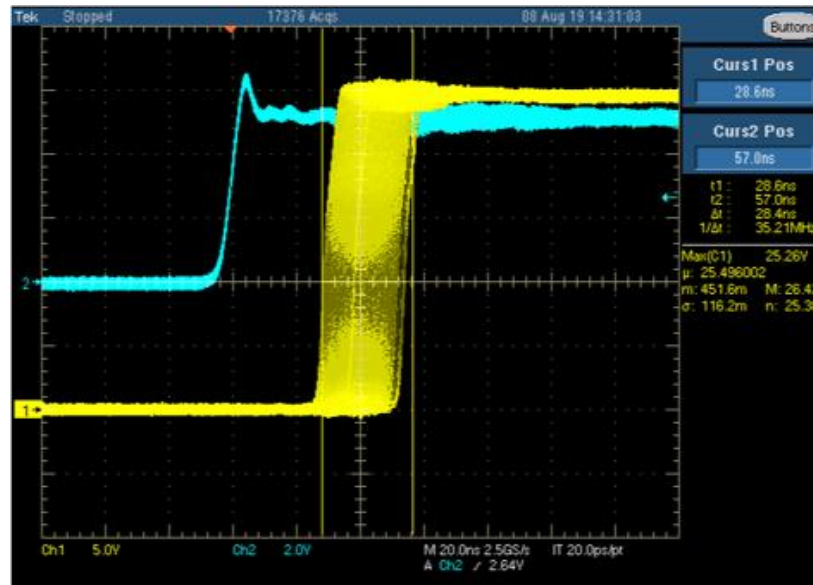
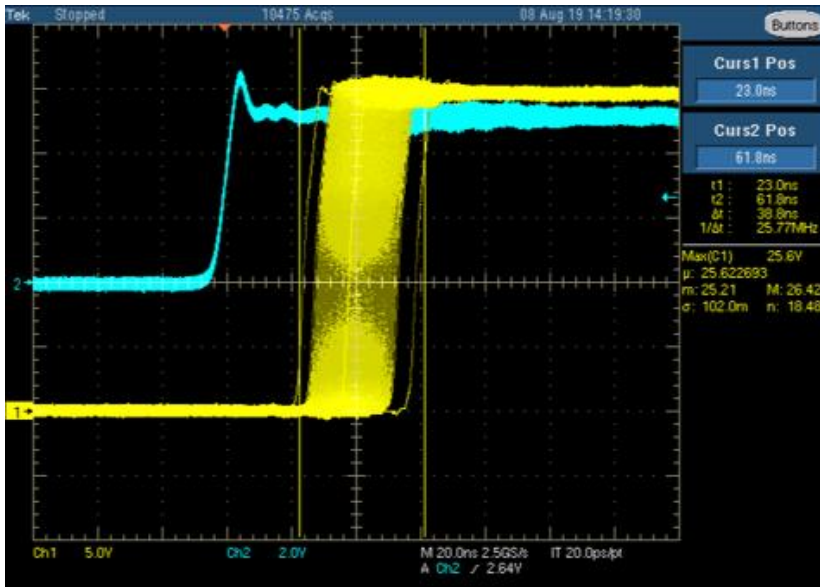


load : none



load : 3A

CH1 : SW
CH2 : sync signal



load : 2A

load	jitter
none	30.4ns
2A	38.8ns
3A	28.4ns